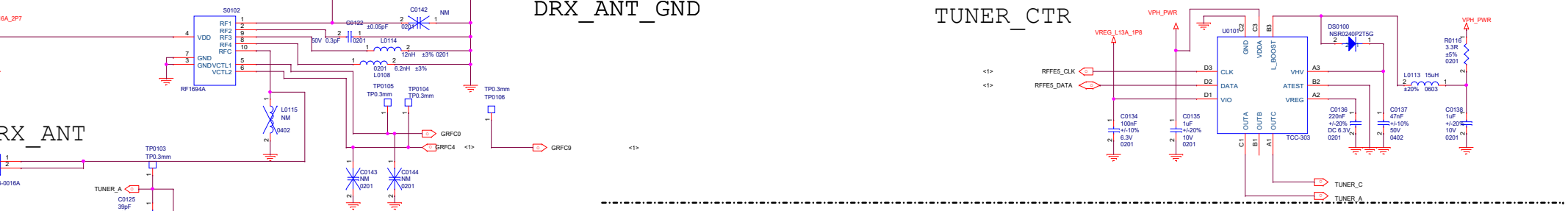


屏蔽罩 屏蔽罩 螺母 长地弹片
 cable夹
 camera地弹片
 分级天线座地弹片



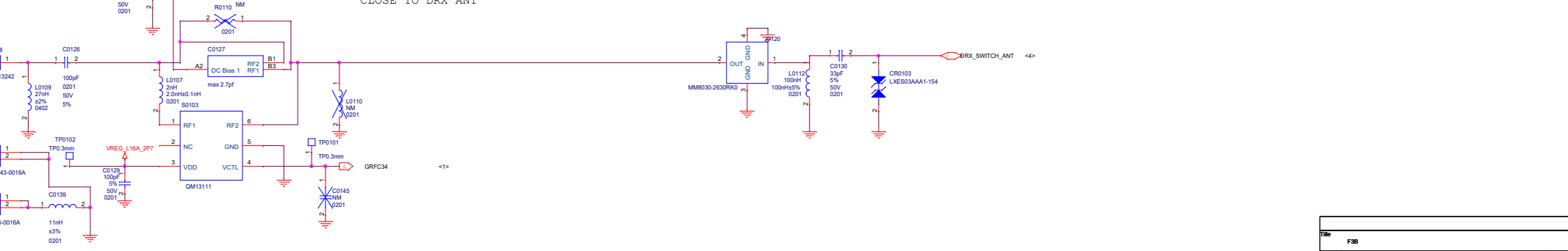
DRX_ANT_GND

TUNER_CTR



DRX_ANT

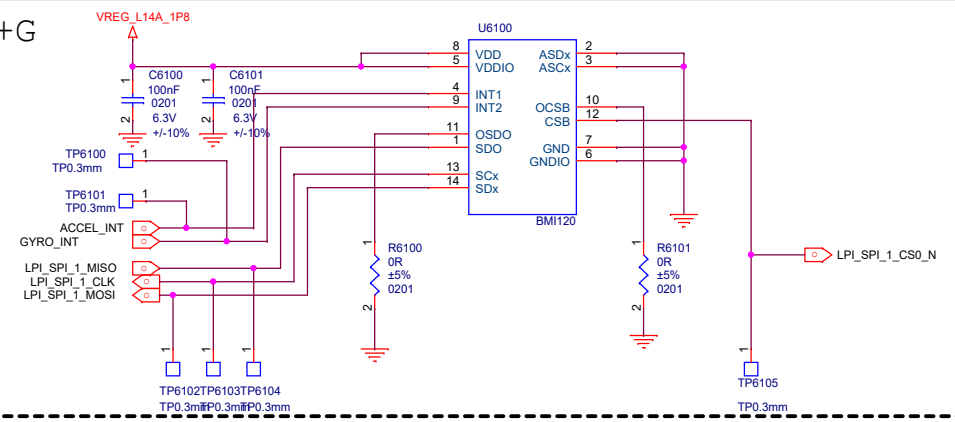
CLOSE TO DRX ANT



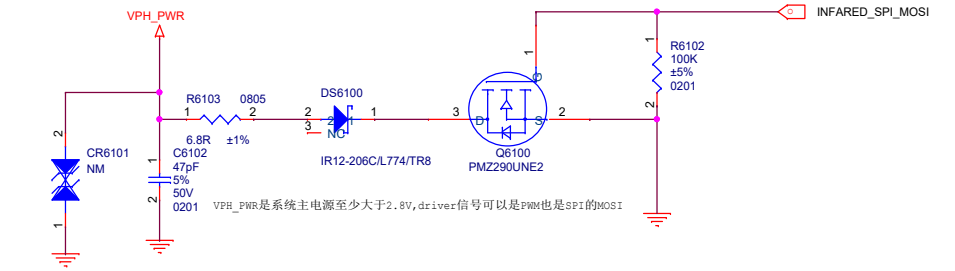
Title		F38	
Size	A2	Document Number	ANT_CONCOUPLERS&SWITCH
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FP

A+G

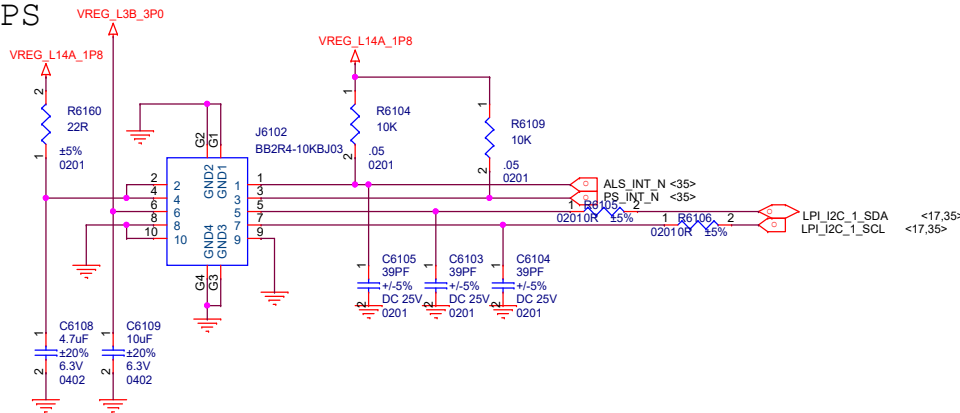


INFARED

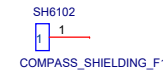
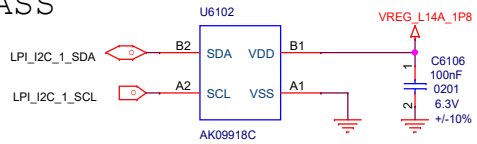



HALL

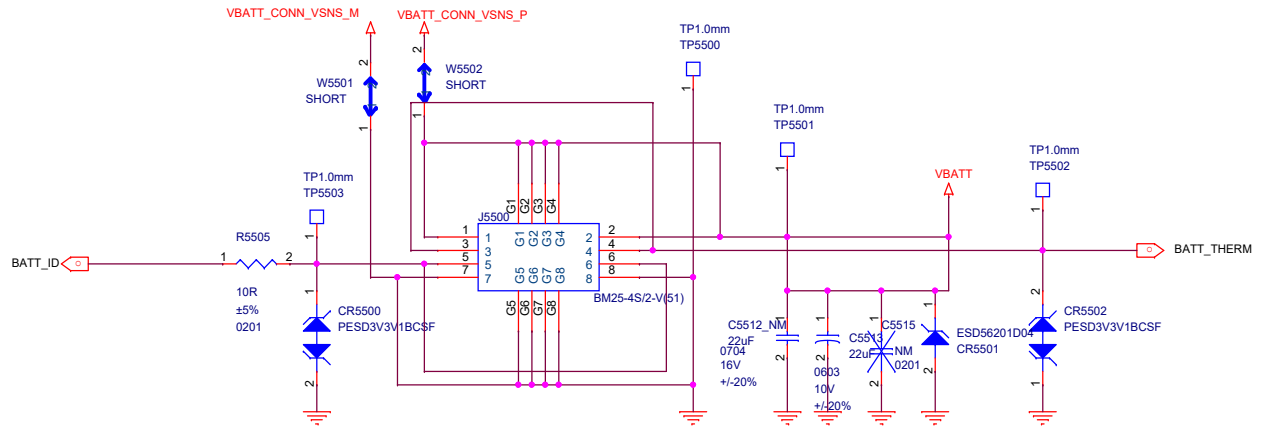
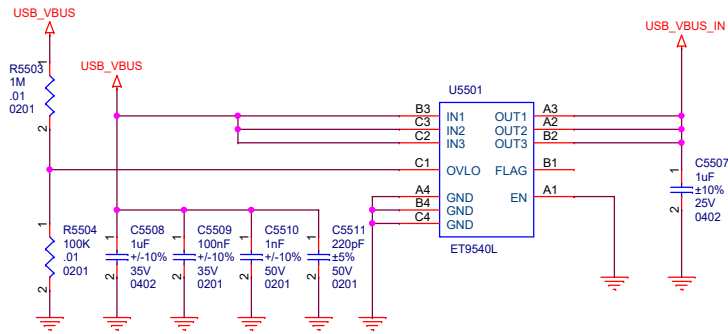
ALS+PS




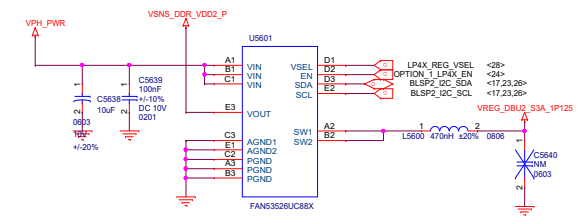
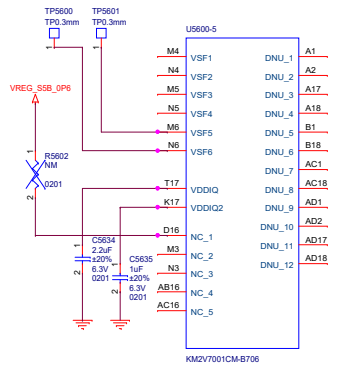
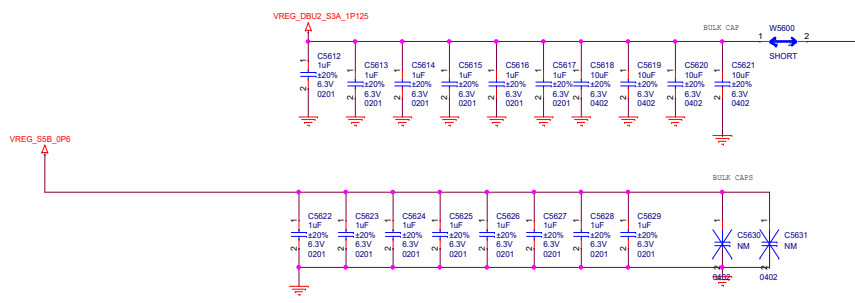
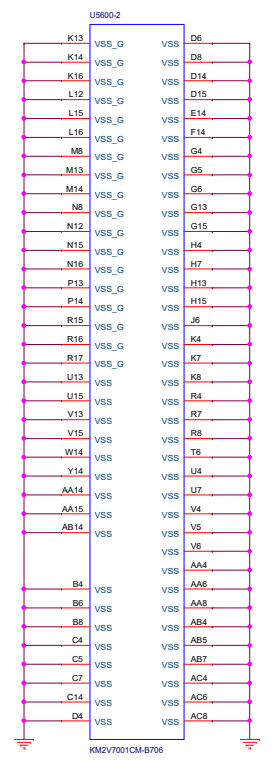
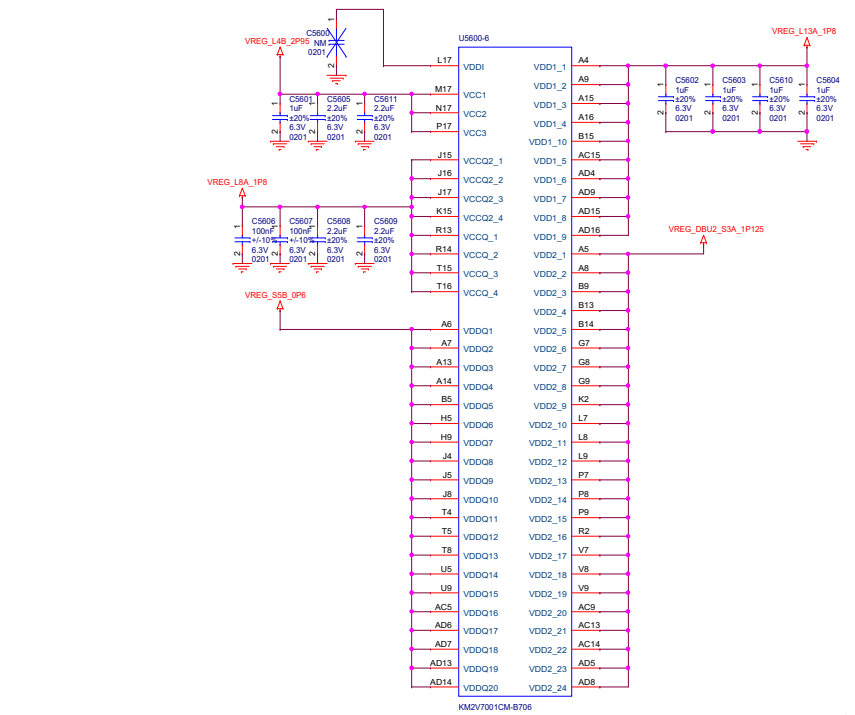
COMPASS



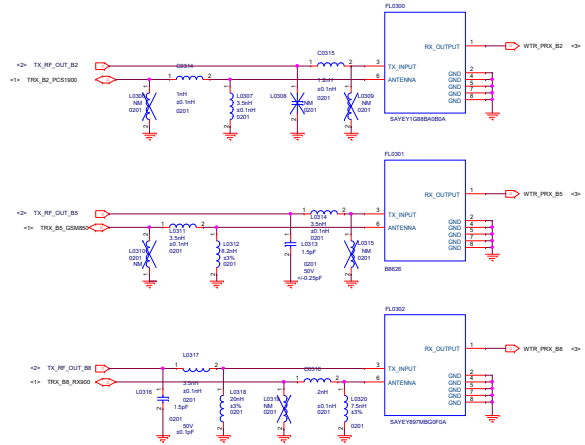
Title		F3B	
Size	Document Number	SENSOR	
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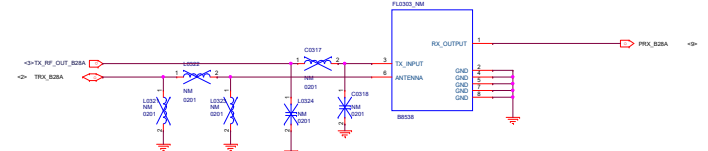
Title		F3B			
Size	A3	Document Number	SMB1390&WL POWER		
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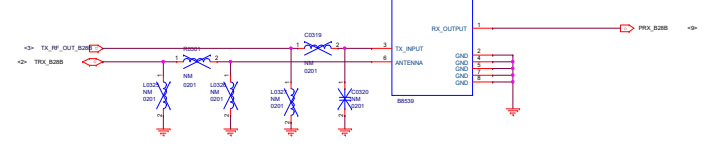
B2&B5&B8



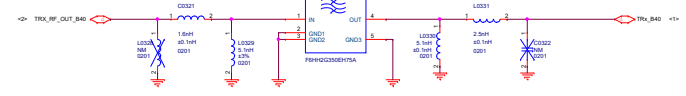
TX B28A



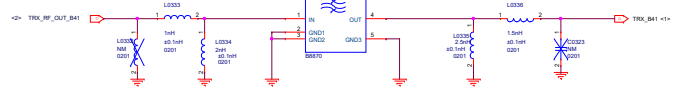
TX B28B



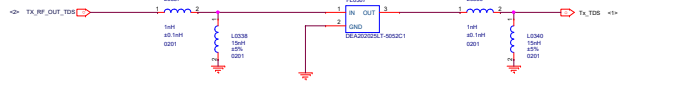
B40



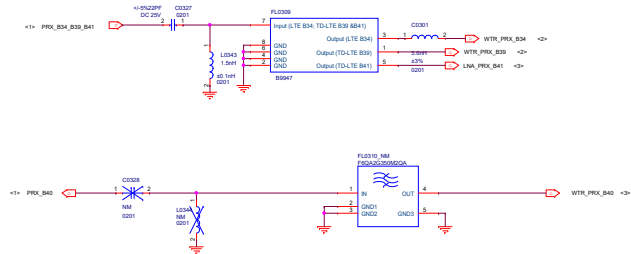
TX B41



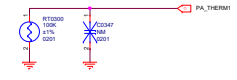
TX TDS



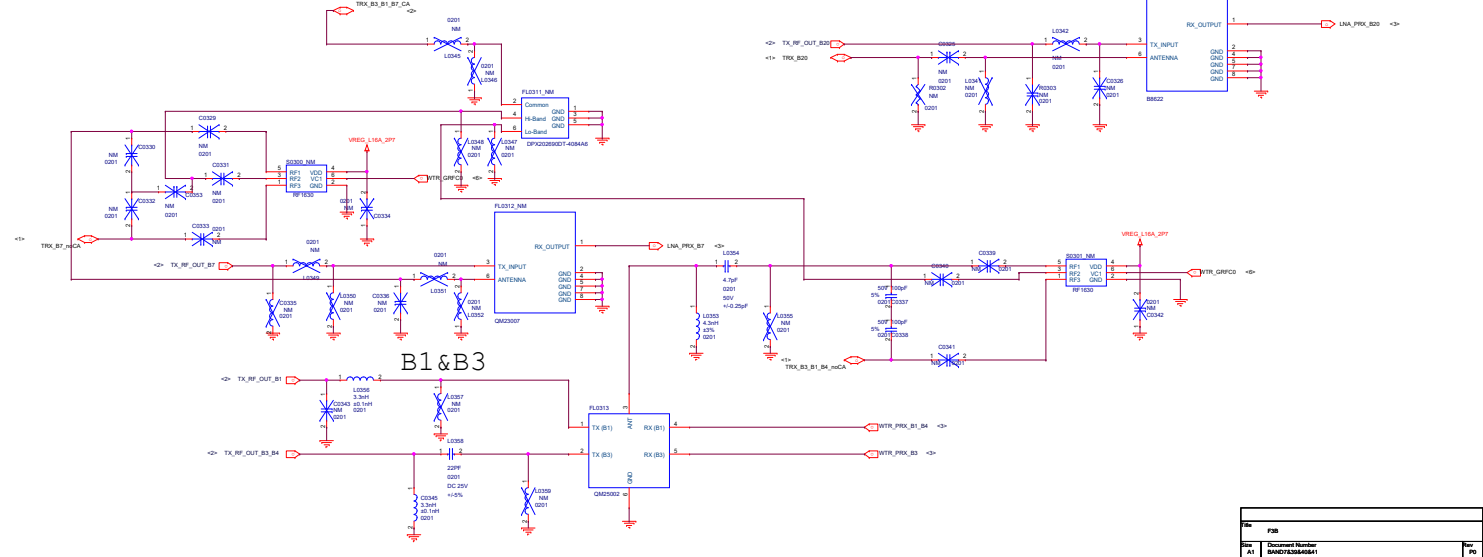
TRI FILTER B34 B39 B41

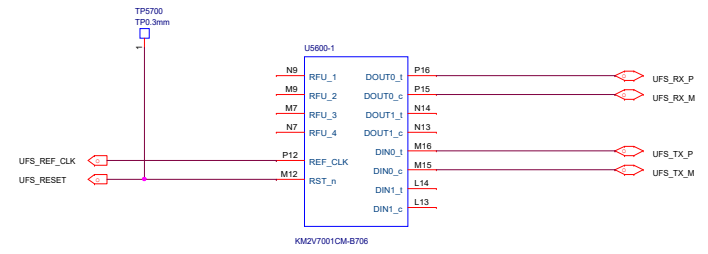


Thermal

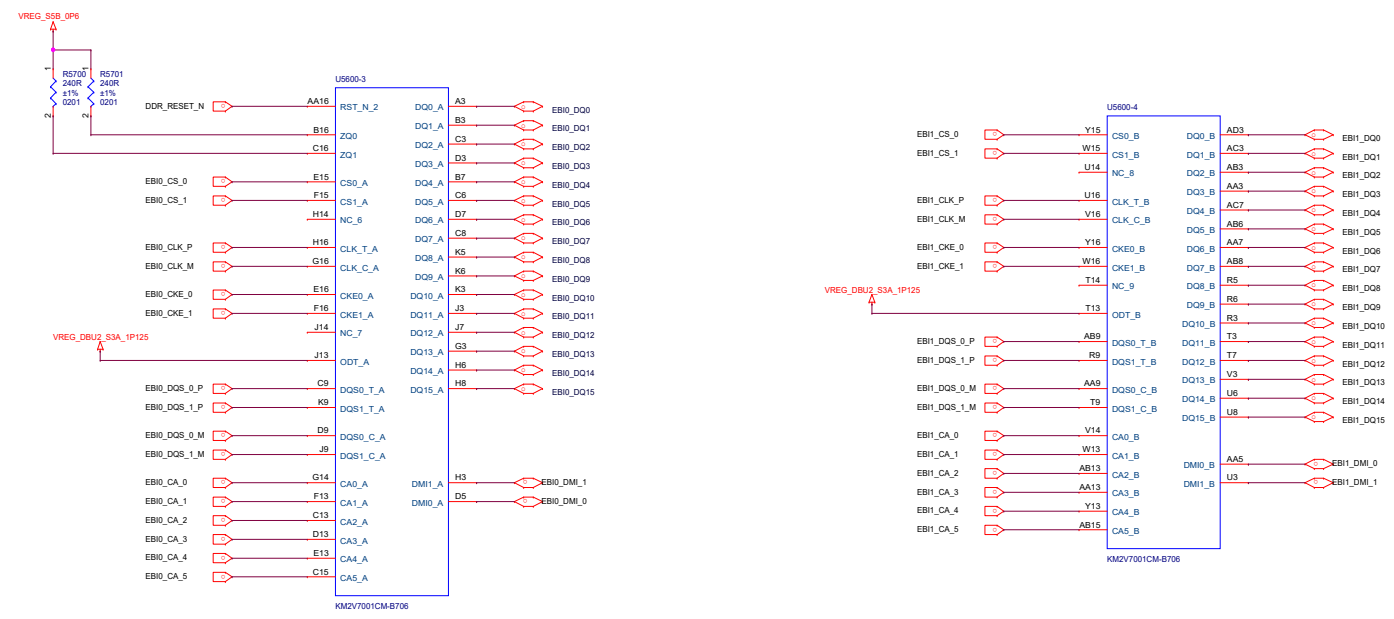


B1&B3

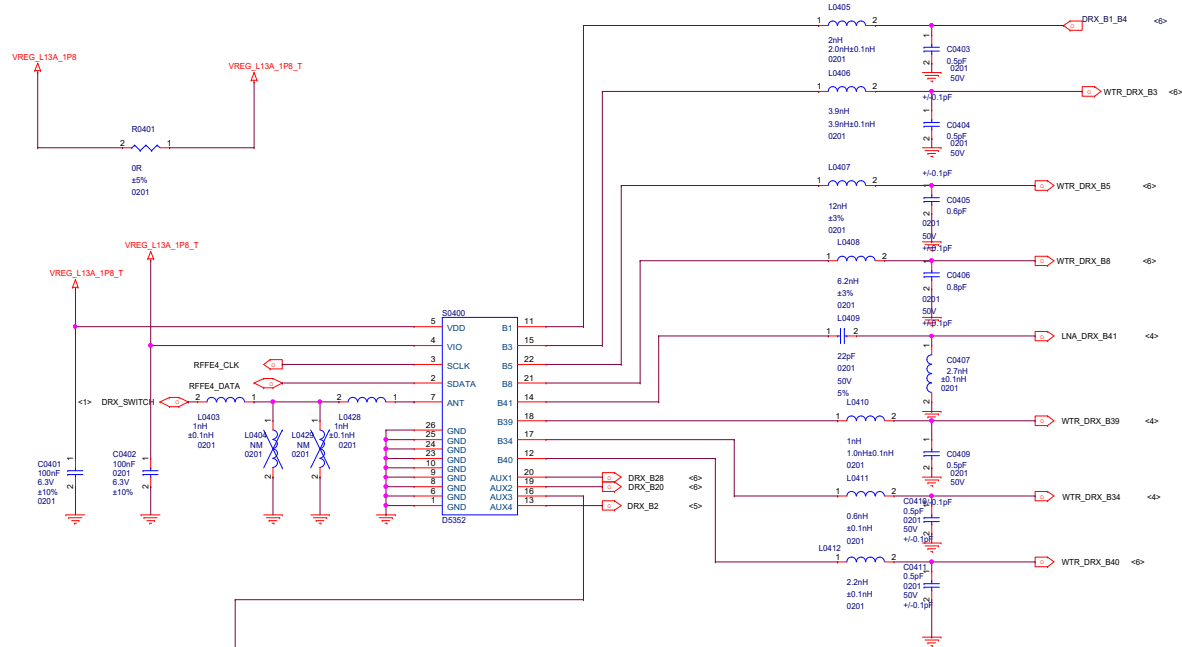




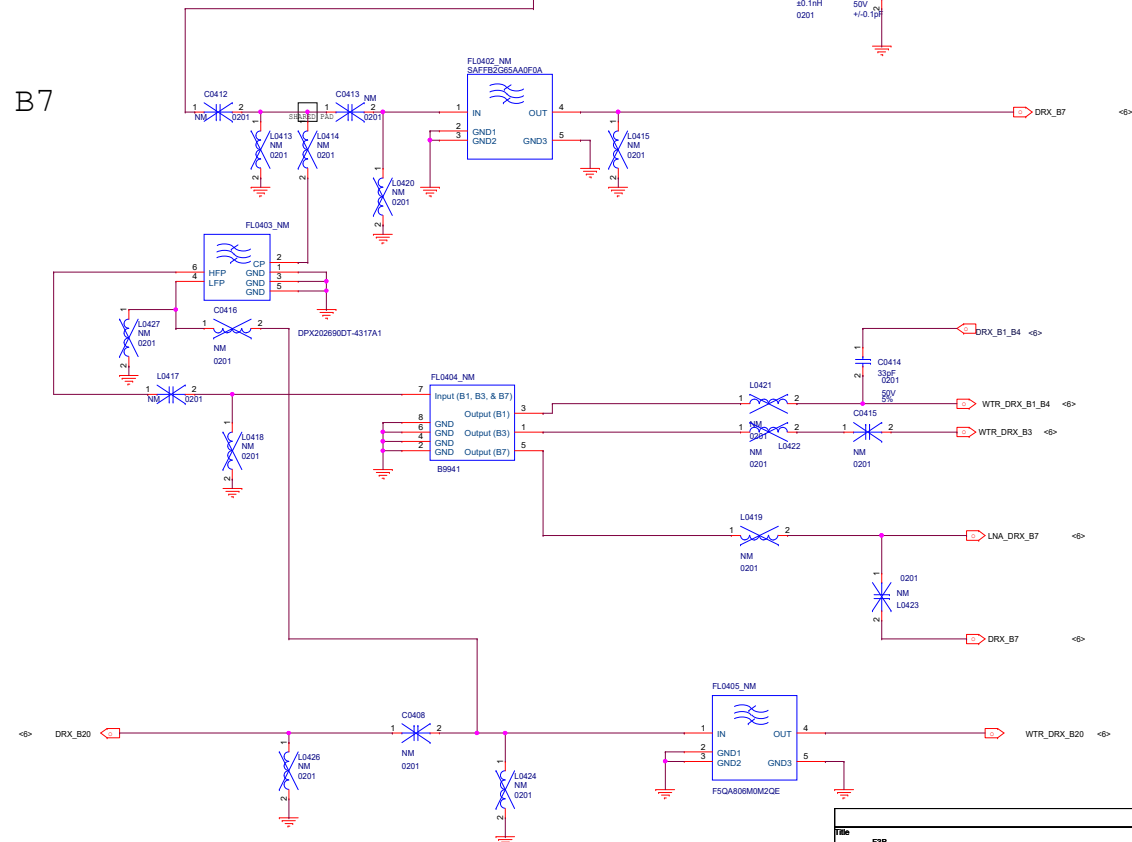
LPDDR4 / 4x



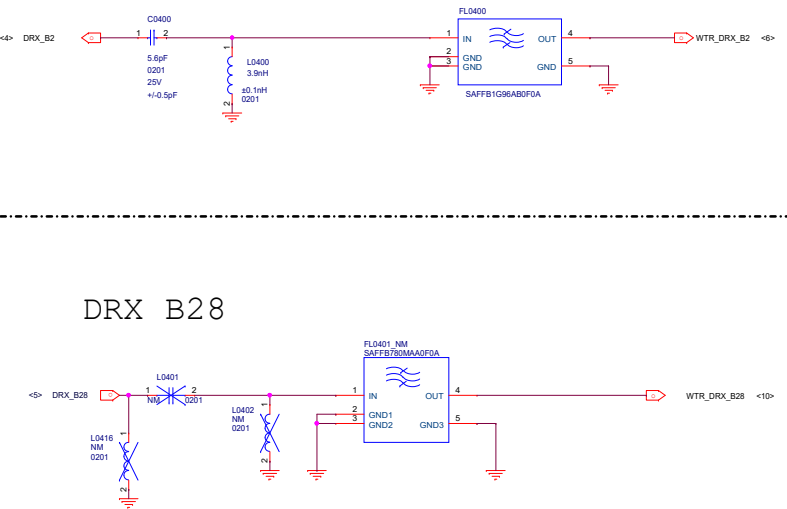
DRX



DRX B7

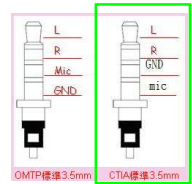
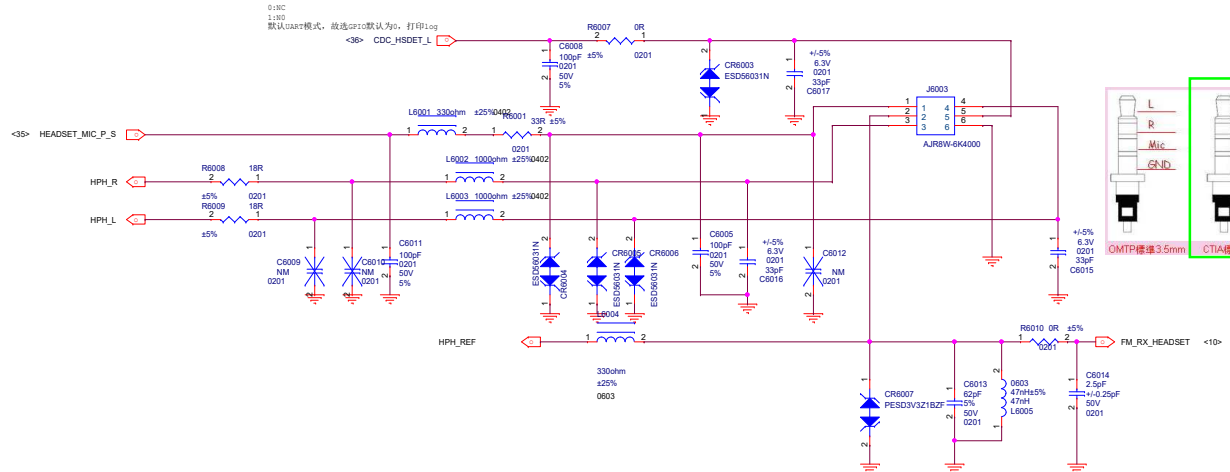


DRX B28

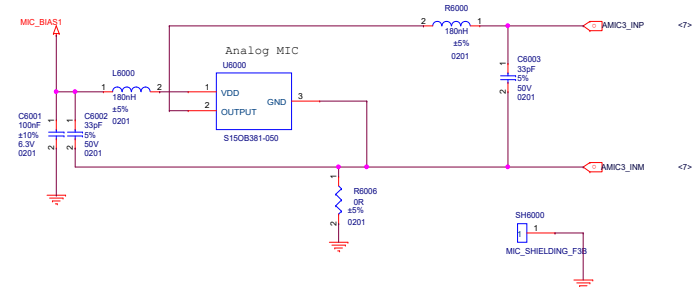


File	FSB		
Size	Document Number	Rev	PO
A2	FILTERS&COMPONENTS/DRX_B		
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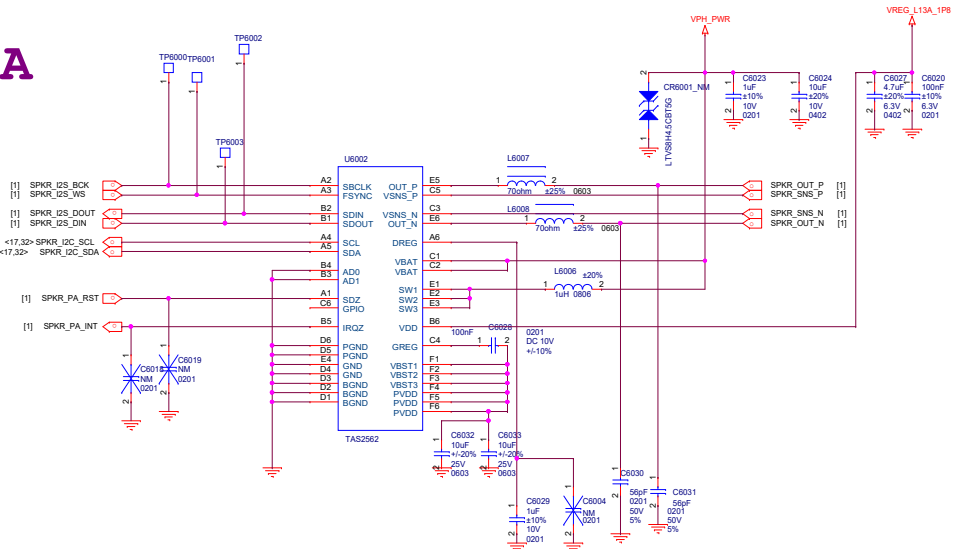
HEADSET



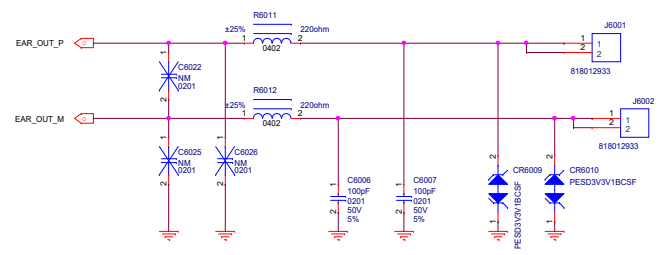
AUX Mic



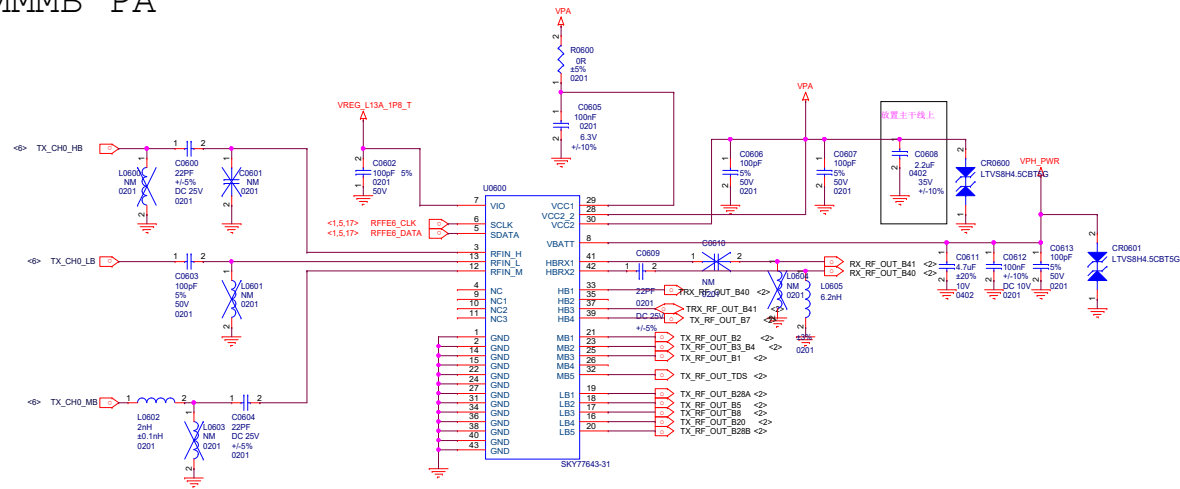
Speaker PA

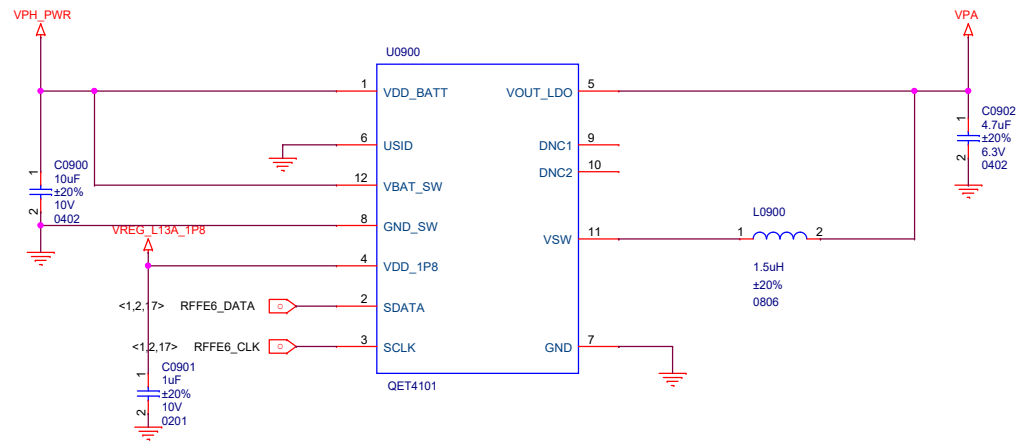



Receiver

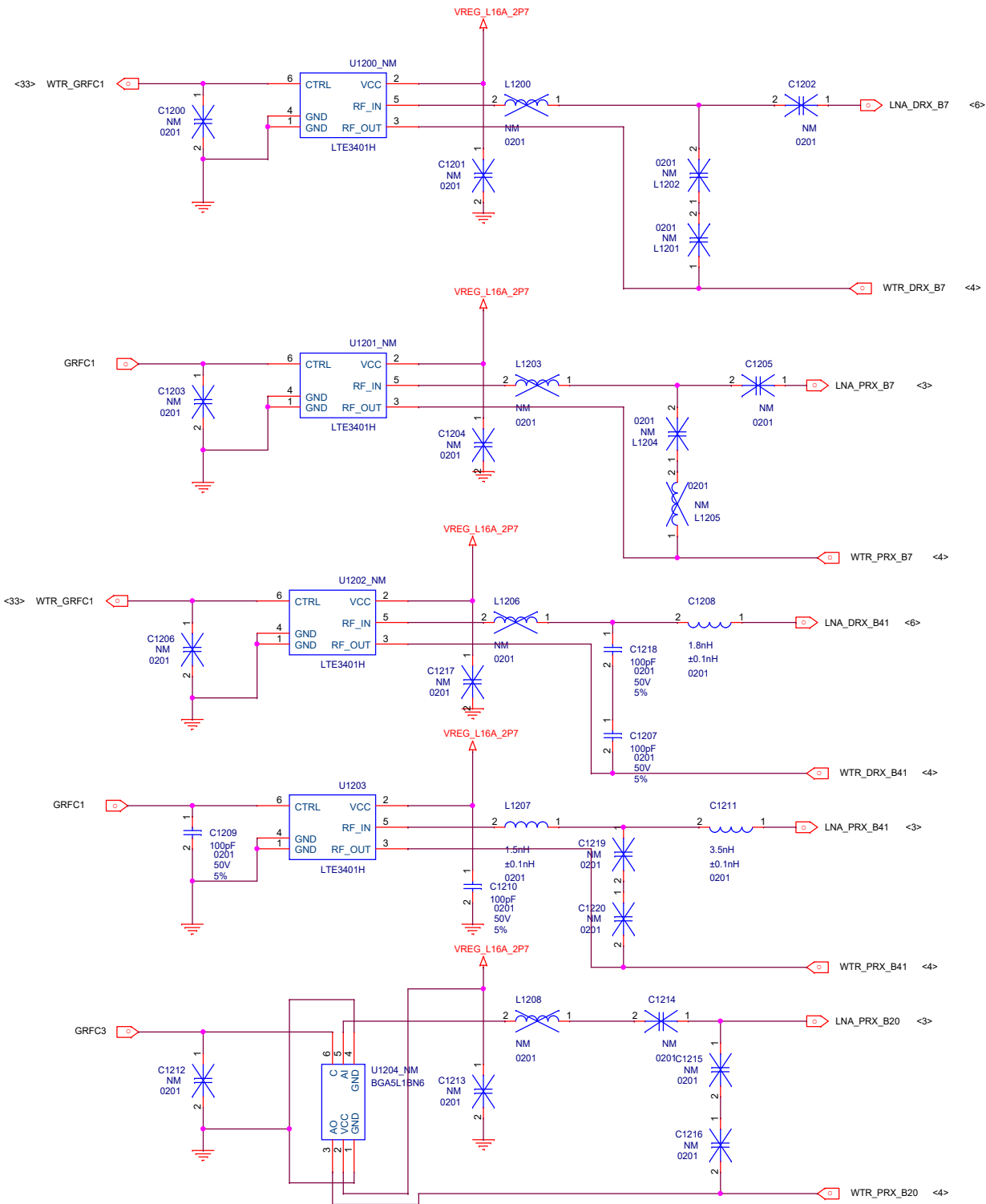



MMMB PA

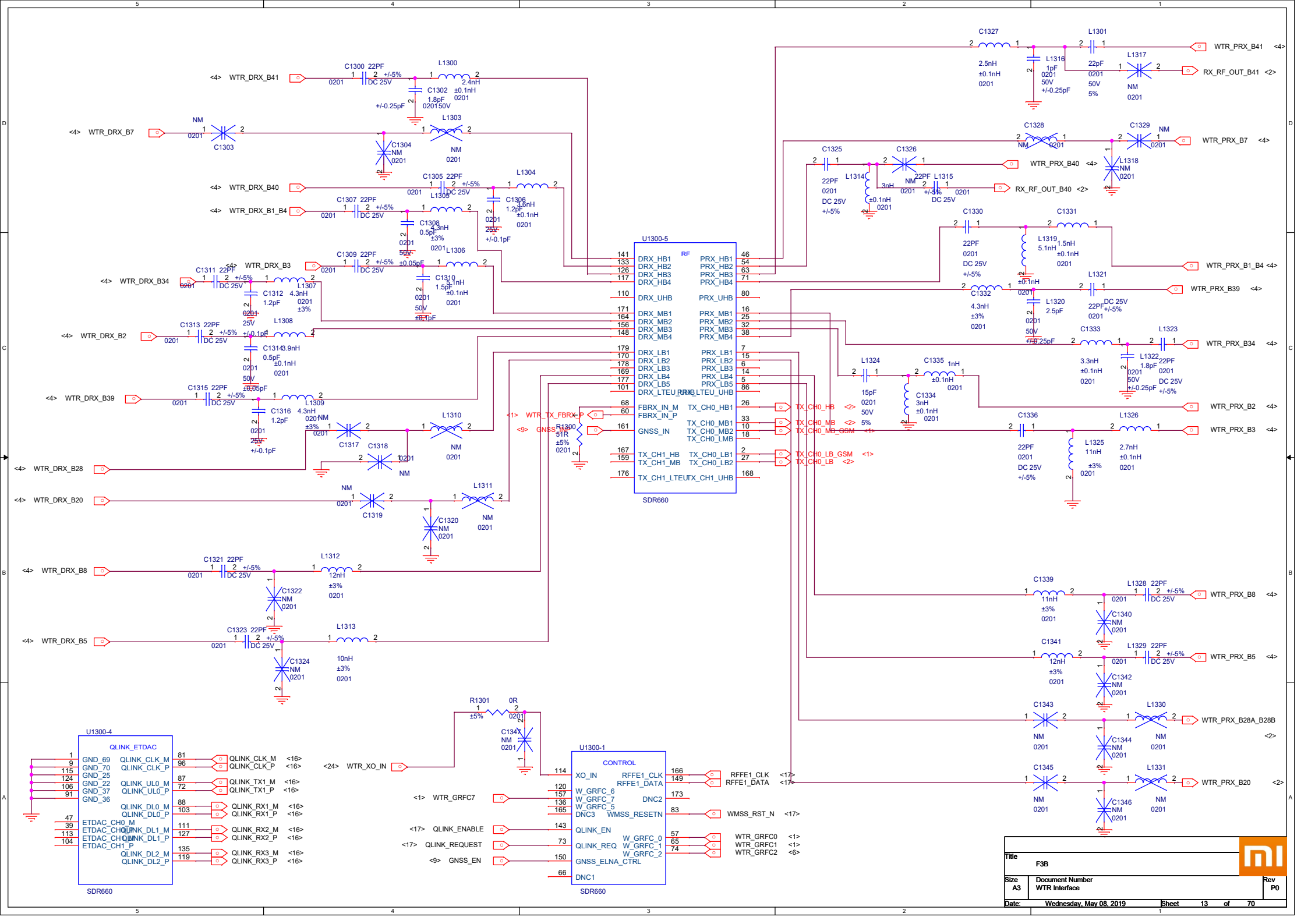




Title		F3B			
Size	Document Number	RF DC-DC			Rev
A3					P0
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Title		F3B	
Size	Document Number	Reserved(for LNA)	
A3			P0
Date:	Wednesday, May 08, 2019	Sheet	12 of 70

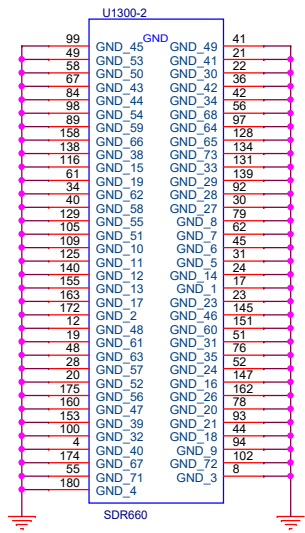
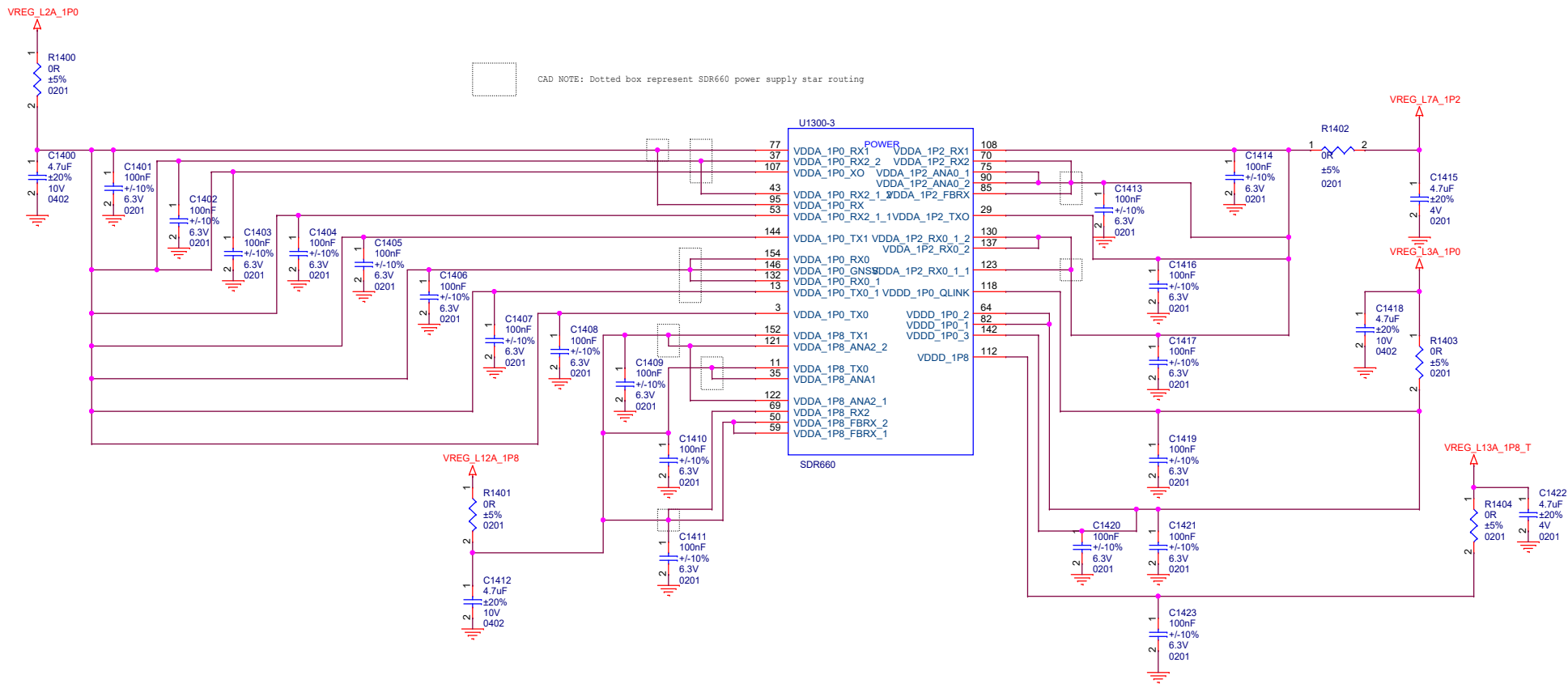


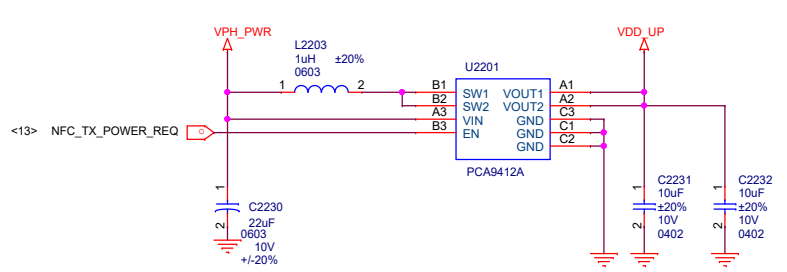
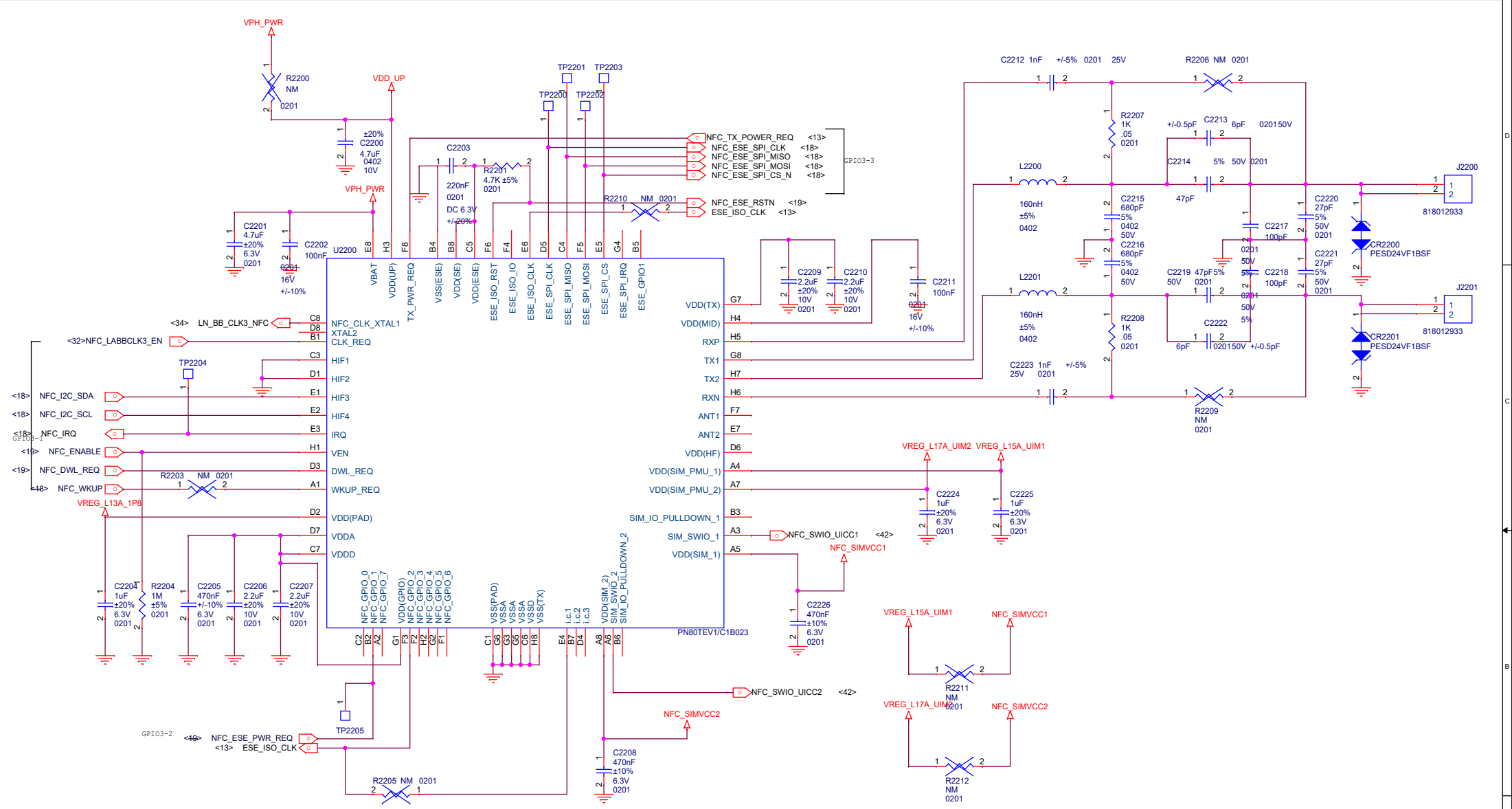
U1300-5		
RF		
141	DRX_HB1	PRX_HB1
133	DRX_HB2	PRX_HB2
126	DRX_HB3	PRX_HB3
117	DRX_HB4	PRX_HB4
110	DRX_UHB	PRX_UHB
171	DRX_MB1	PRX_MB1
164	DRX_MB2	PRX_MB2
156	DRX_MB3	PRX_MB3
148	DRX_MB4	PRX_MB4
179	DRX_LB1	PRX_LB1
170	DRX_LB2	PRX_LB2
178	DRX_LB3	PRX_LB3
169	DRX_LB4	PRX_LB4
177	DRX_LB5	PRX_LB5
101	DRX_LTEU	PRX_LTEU
68	FBRX_IN_M	TX_CH0_HB1
60	FBRX_IN_P	TX_CH0_HB2
161	GNSS_IN	TX_CH0_MB1
177		TX_CH0_MB2
101		TX_CH0_LMB
167	TX_CH1_HB	TX_CH0_LB1
159	TX_CH1_MB	TX_CH0_LB2
176	TX_CH1_LTEU	TX_CH1_UHB
		168


U1300-1		
CONTROL		
114	XO_IN	RFFE1_CLK
120	W_GRFC_6	RFFE1_DATA
157	W_GRFC_7	DNC2
136	W_GRFC_5	83
165	DNC3	WMSS_RST_N
143	QLINK_EN	57
73	QLINK_REQ	WTR_GRFC0
150	GNSS_ELNA_CTRL	WTR_GRFC1
66	DNC1	74
		WTR_GRFC2

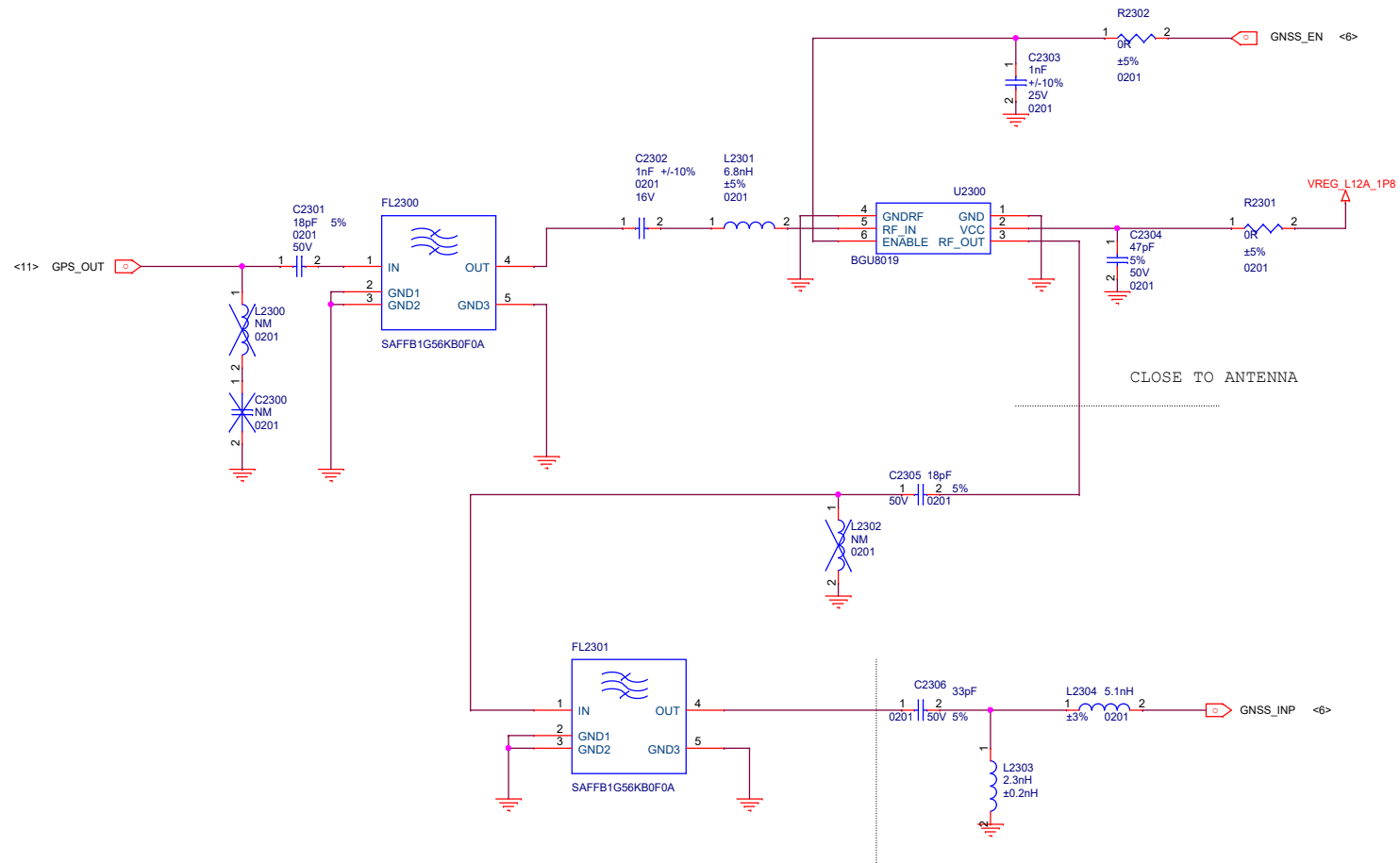
U1300-4		
QLINK_ETDAC		
1	QLINK_CLK_M	<16>
9	QLINK_CLK_P	<16>
115		
124	QLINK_TX1_M	<16>
106	QLINK_TX1_P	<16>
91		
88	QLINK_RX1_M	<16>
103	QLINK_RX1_P	<16>
111	QLINK_RX2_M	<16>
127	QLINK_RX2_P	<16>
113	QLINK_RX3_M	<16>
104	QLINK_RX3_P	<16>

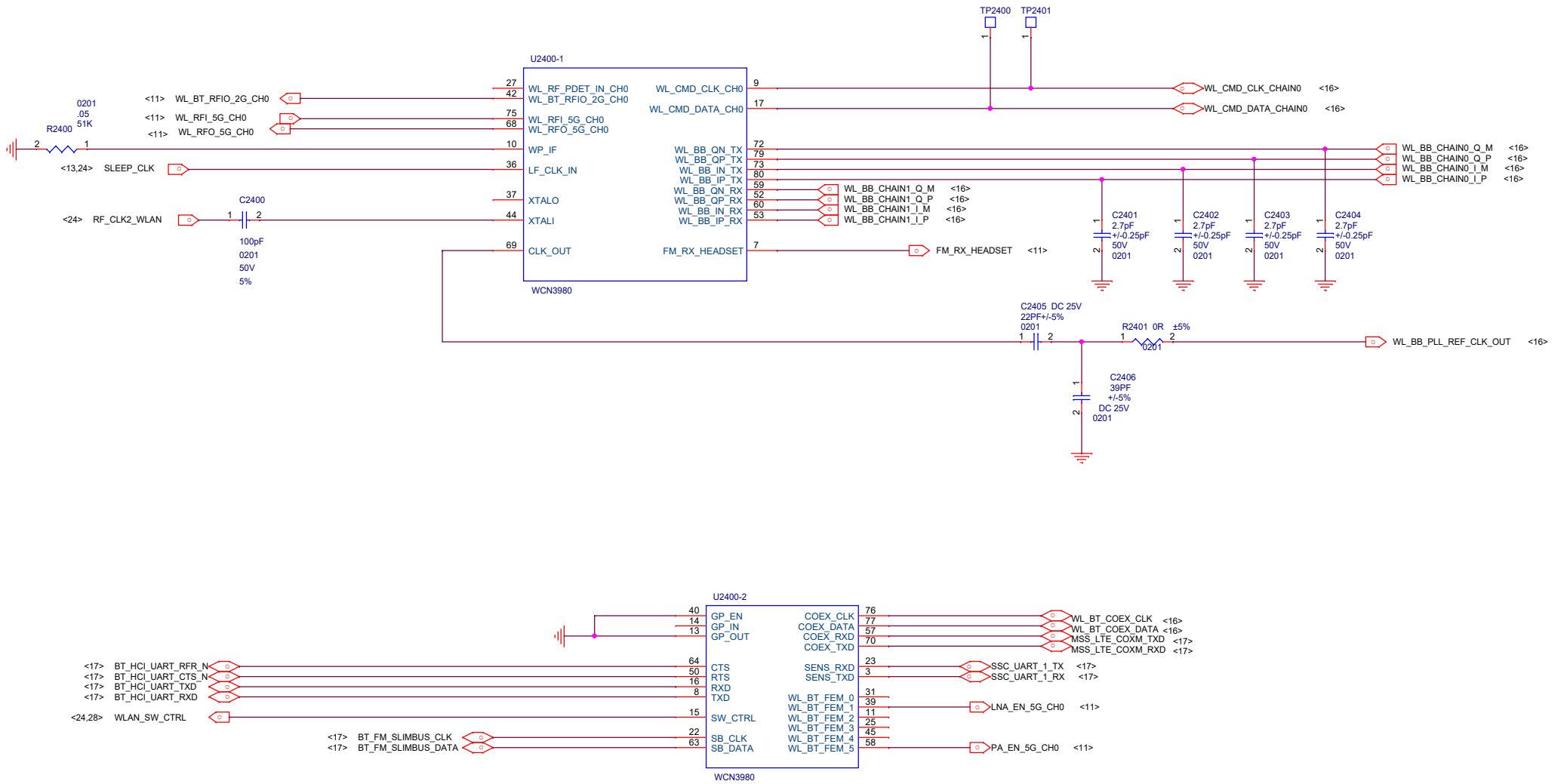
Title		F3B	
Size	Document Number	WTR Interface	
A3			P0
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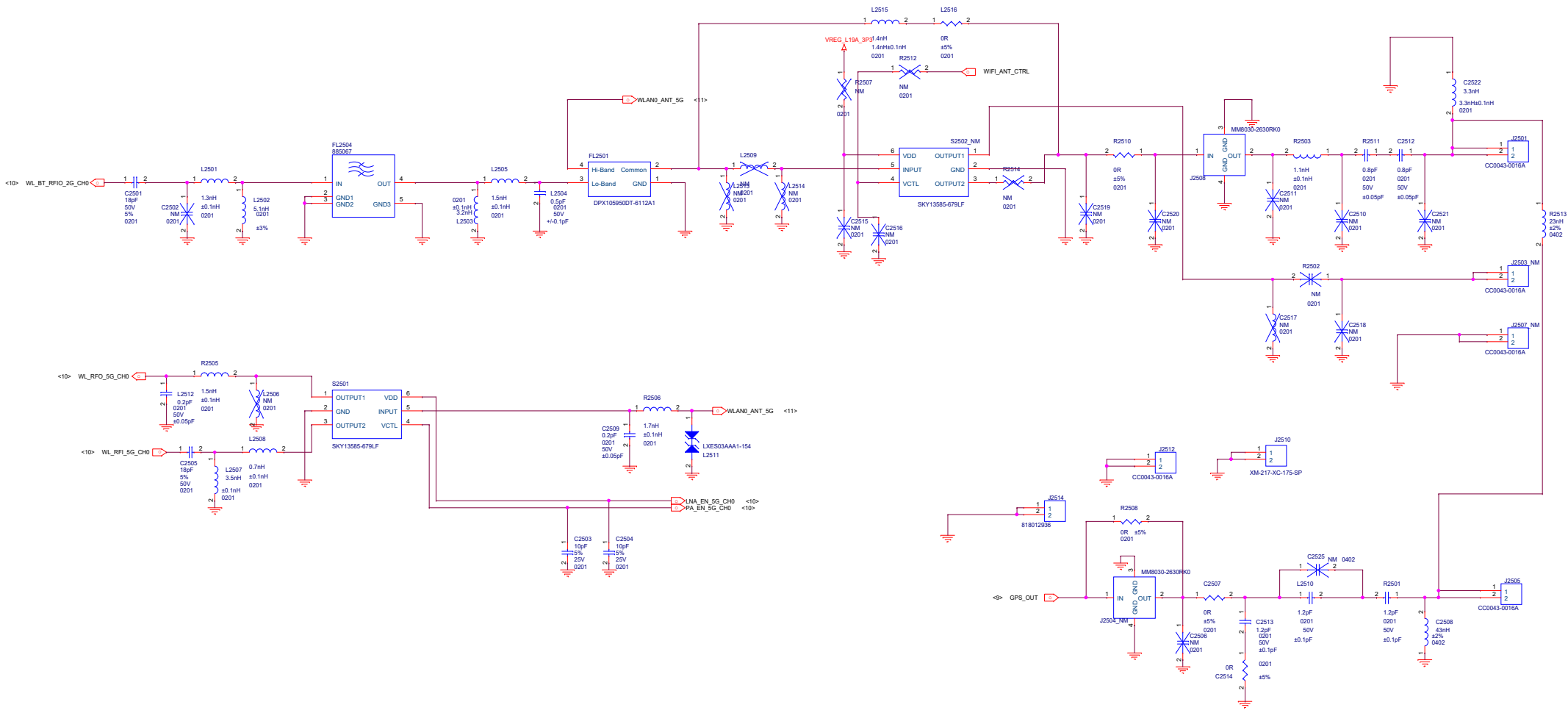


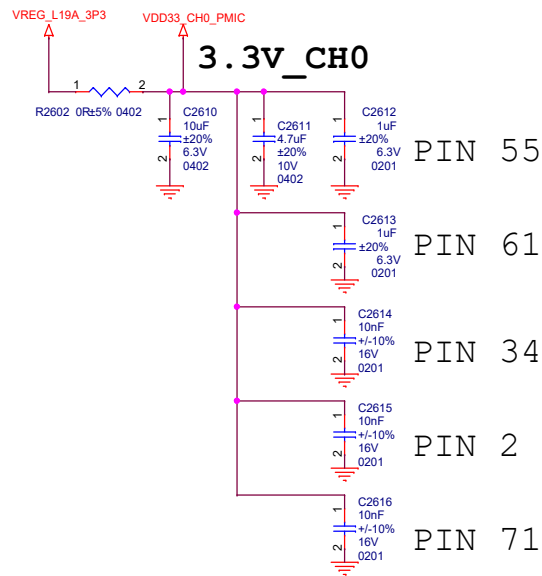
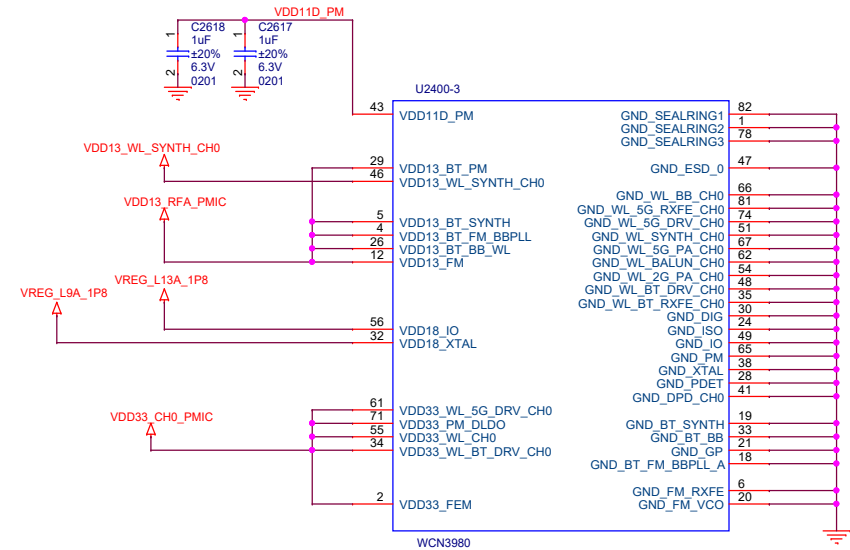
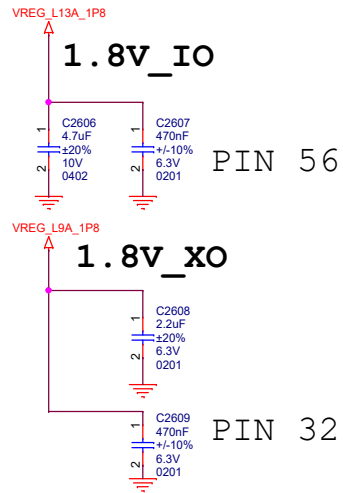
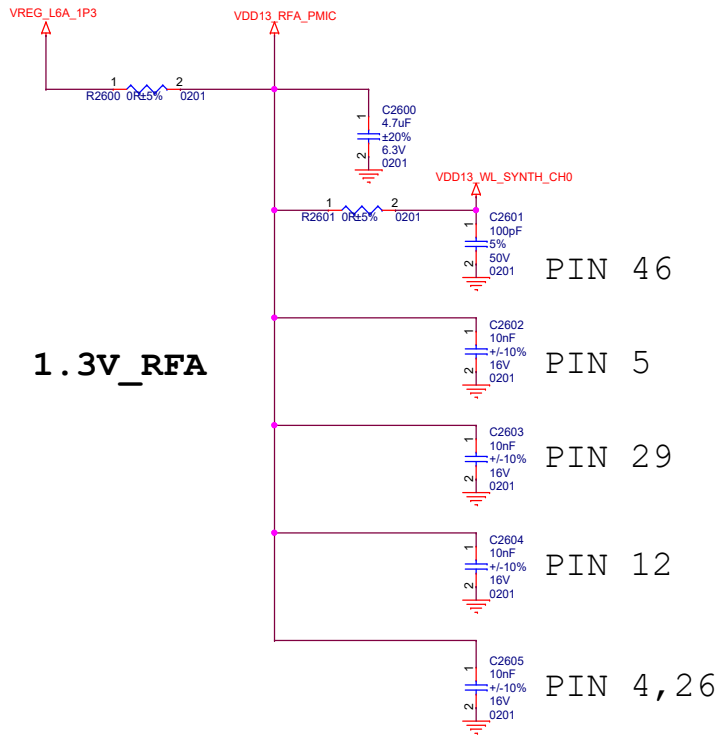


Title		
F3B		
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A3	Reserved(for PA)	
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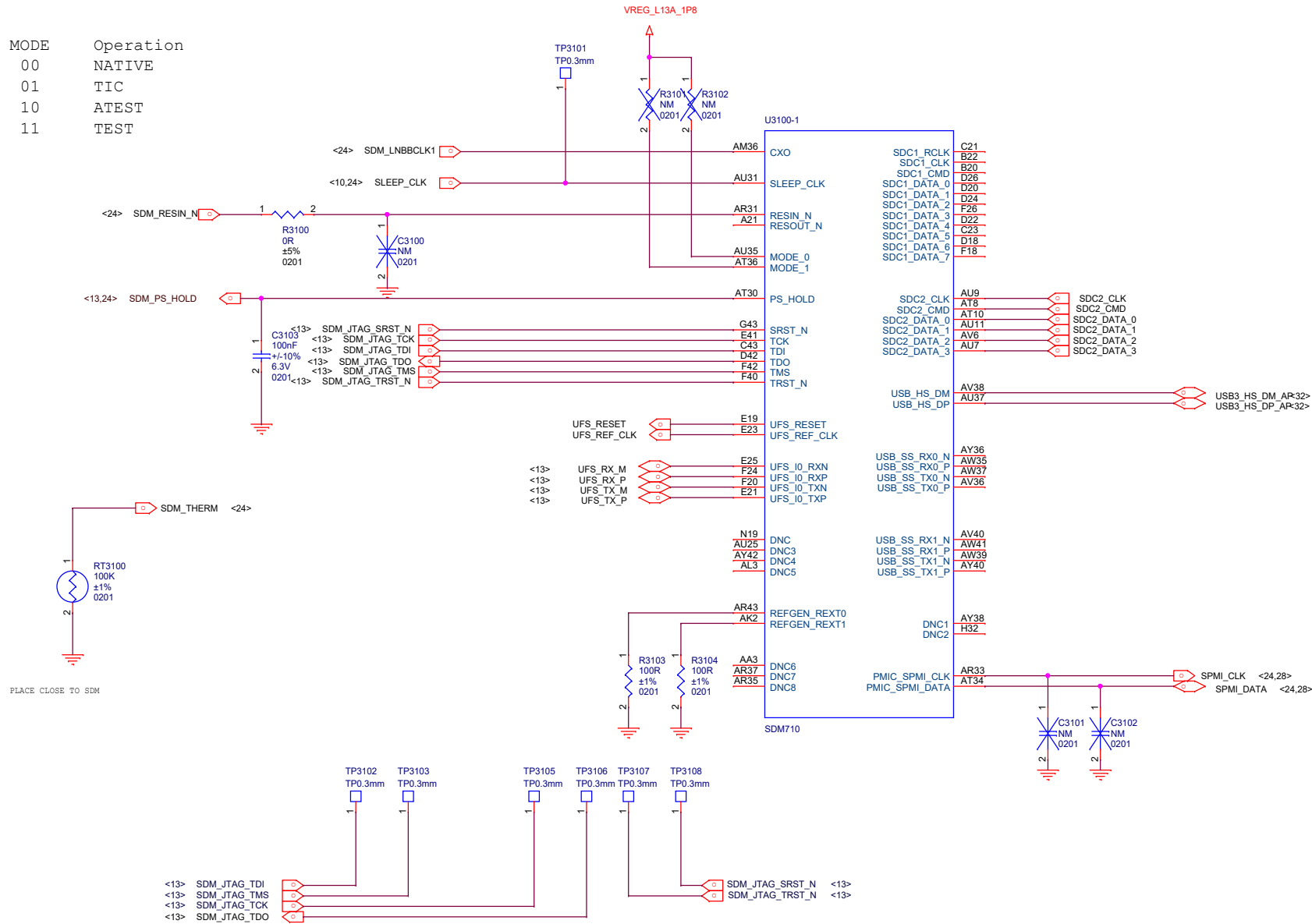




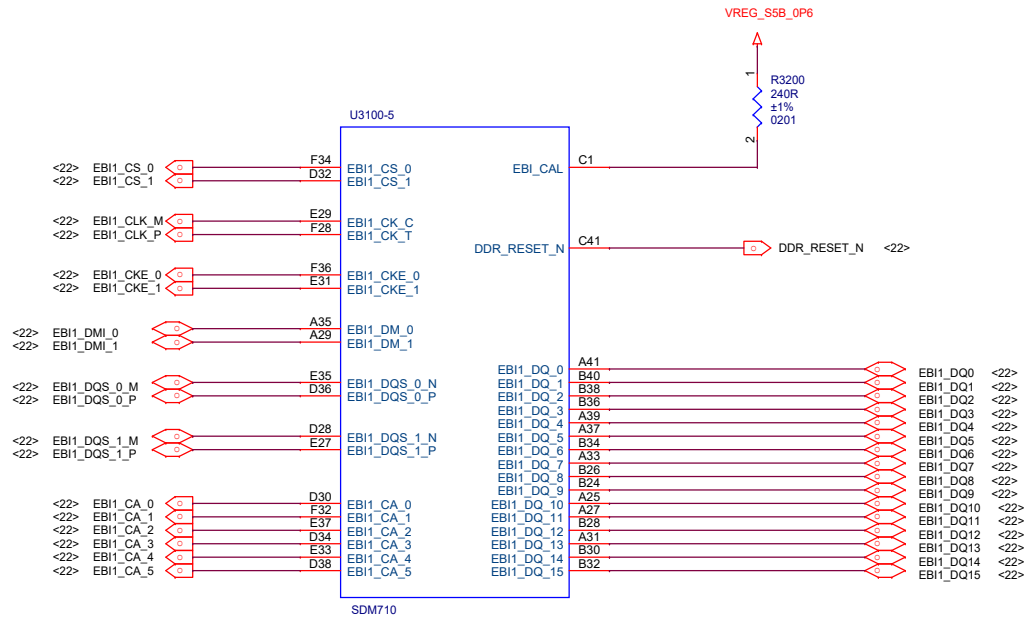
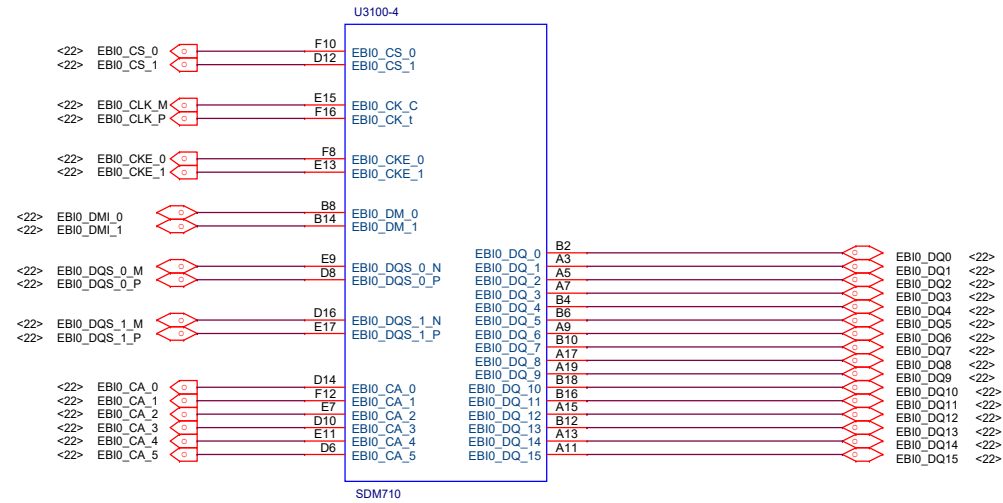


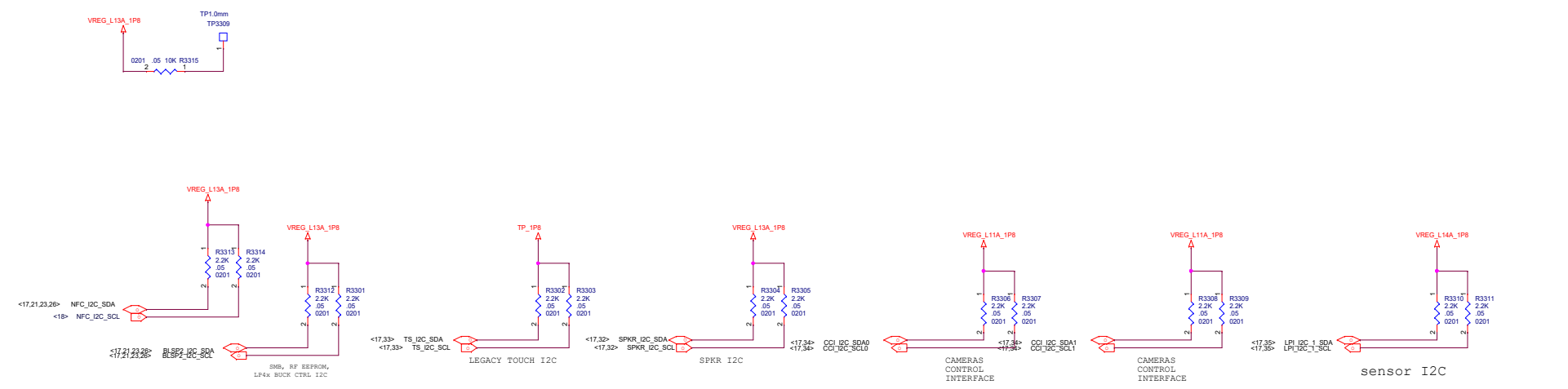
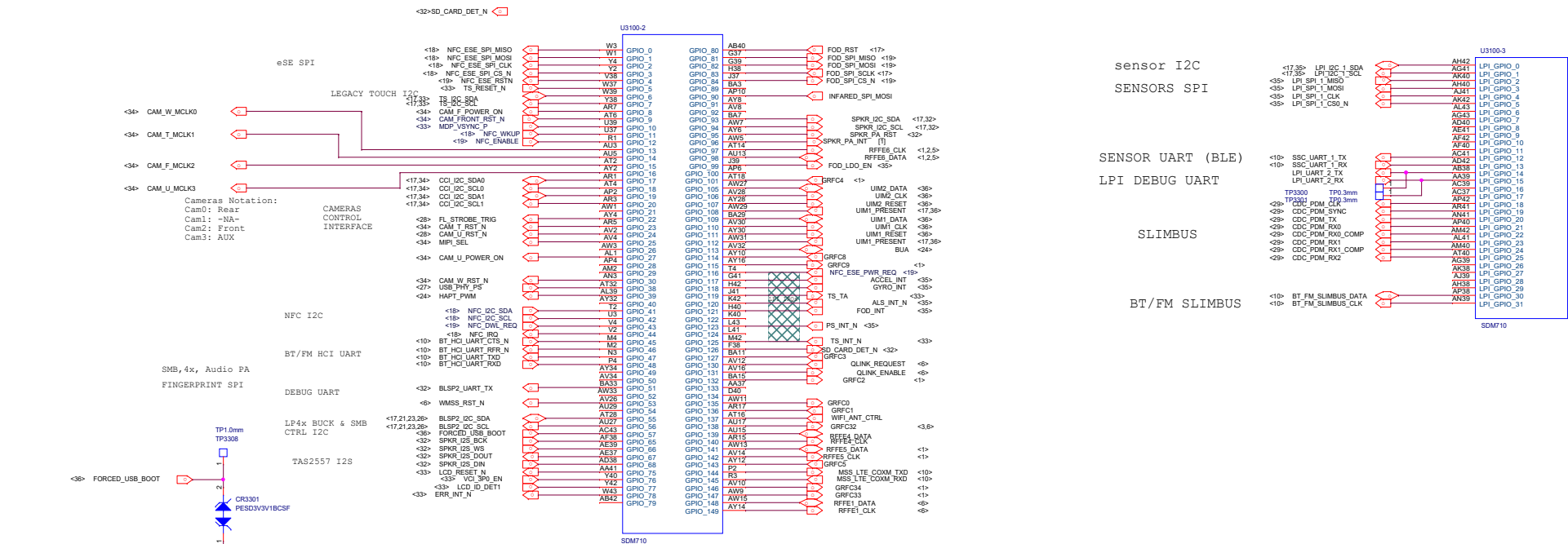


MODE	Operation
00	NATIVE
01	TIC
10	ATEST
11	TEST



PLACE CLOSE TO SDM





REAR CAMERA
IMX586 48M

FRONT CAMERA
S5K3T1SP 20M
ULTRA WIDE CAMERA
3P9 16M

REAR CAMERA TELE
3m5 13M

<35>MIPI_WIDE_CLK_P
<35>MIPI_WIDE_CLK_M
<35>MIPI_WIDE_LANE0_P
<35>MIPI_WIDE_LANE0_M
<35>MIPI_WIDE_LANE1_P
<35>MIPI_WIDE_LANE1_M
<35>MIPI_WIDE_LANE2_P
<35>MIPI_WIDE_LANE2_M
<35>MIPI_WIDE_LANE3_P
<35>MIPI_WIDE_LANE3_M
<34> MIPI_CSI2_CLK_P
<34> MIPI_CSI2_CLK_M
<34> MIPI_CSI2_LANE0_P
<34> MIPI_CSI2_LANE0_M
<34> MIPI_CSI2_LANE1_P
<34> MIPI_CSI2_LANE1_M
<15> MIPI_CSI2_LANE2_P
<15> MIPI_CSI2_LANE2_M
<15> MIPI_CSI2_LANE3_P
<15> MIPI_CSI2_LANE3_M
<67> MIPI_TELE_CLK_P
<67> MIPI_TELE_CLK_M
<67> MIPI_TELE_LANE0_P
<67> MIPI_TELE_LANE0_M

AF6
AE5
AE7
AD6
AC5
AB4
AB6
AA5
AA7
Y6
AN5
AM4
AM6
AL5
AK4
AJ3
AJ5
AH4
AH6
AG5
AB2
AC3
AC1
AD2
AE3
AF4
AF2
AG3
AG1
AH2

U3100-6

SDM710

MIPI_CSI0_DCLK_P
MIPI_CSI0_DCLK_N
MIPI_CSI0_DLN0_P
MIPI_CSI0_DLN0_N
MIPI_CSI0_DLN1_P
MIPI_CSI0_DLN1_N
MIPI_CSI0_DLN2_P
MIPI_CSI0_DLN2_N
MIPI_CSI0_DLN3_P
MIPI_CSI0_DLN3_N
MIPI_CSI1_DCLK_P
MIPI_CSI1_DCLK_N
MIPI_CSI1_DLN0_P
MIPI_CSI1_DLN0_N
MIPI_CSI1_DLN1_P
MIPI_CSI1_DLN1_N
MIPI_CSI1_DLN2_P
MIPI_CSI1_DLN2_N
MIPI_CSI1_DLN3_P
MIPI_CSI1_DLN3_N
MIPI_CSI2_DCLK_P
MIPI_CSI2_DCLK_N
MIPI_CSI2_DLN0_P
MIPI_CSI2_DLN0_N
MIPI_CSI2_DLN1_P
MIPI_CSI2_DLN1_N
MIPI_CSI2_DLN2_P
MIPI_CSI2_DLN2_N
MIPI_CSI2_DLN3_P
MIPI_CSI2_DLN3_N

MIPI_DSI0_CLK_N
MIPI_DSI0_CLK_P
MIPI_DSI0_LN0_N
MIPI_DSI0_LN0_P
MIPI_DSI0_LN1_N
MIPI_DSI0_LN1_P
MIPI_DSI0_LN2_N
MIPI_DSI0_LN2_P
MIPI_DSI0_LN3_N
MIPI_DSI0_LN3_P

MIPI_DSI1_CLK_N
MIPI_DSI1_CLK_P
MIPI_DSI1_LN0_N
MIPI_DSI1_LN0_P
MIPI_DSI1_LN1_N
MIPI_DSI1_LN1_P
MIPI_DSI1_LN2_N
MIPI_DSI1_LN2_P
MIPI_DSI1_LN3_N
MIPI_DSI1_LN3_P

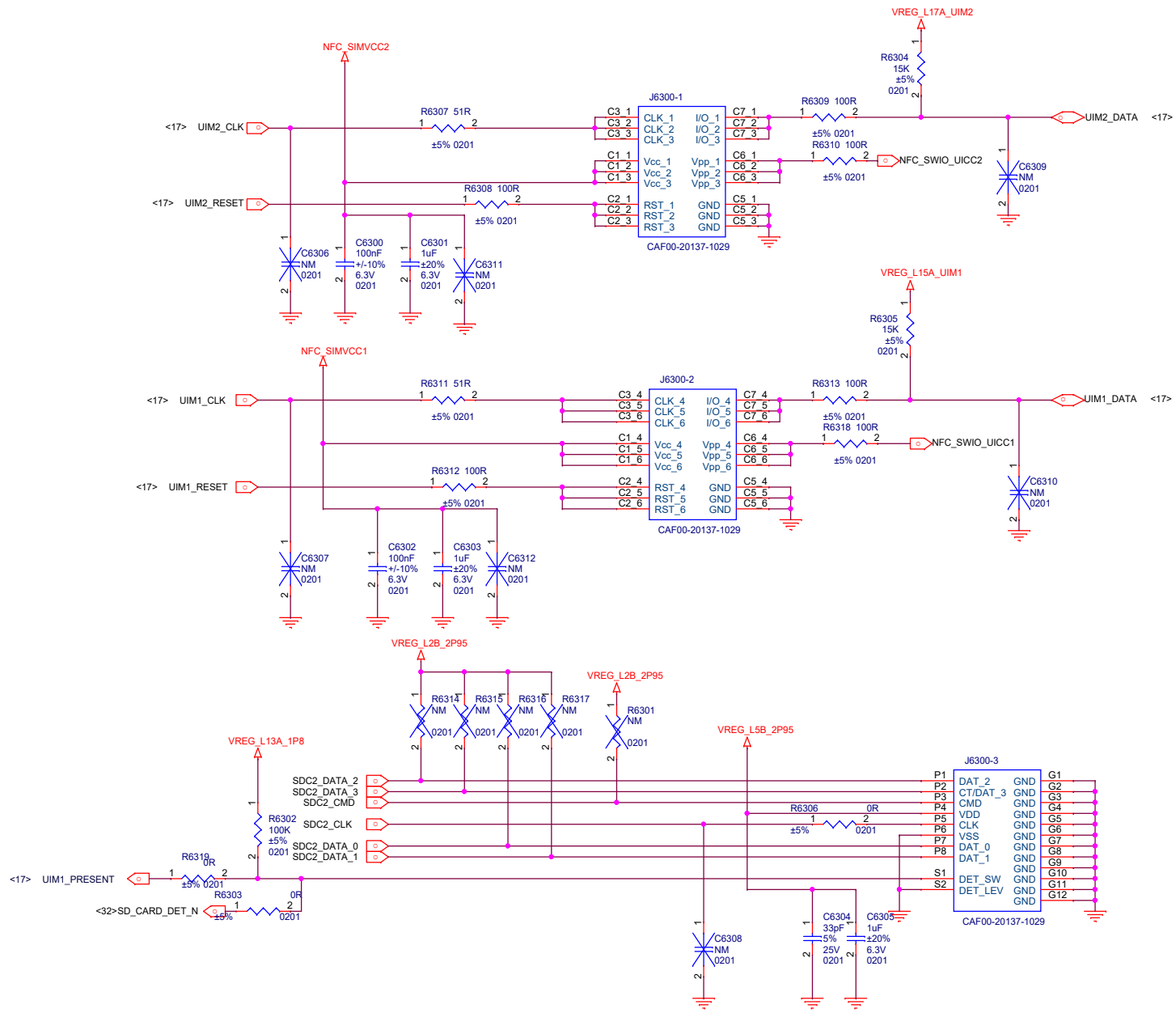
DP_AUX_N
DP_AUX_P

M40
N39
R39
T38
P38
R37
L39
M38
K38
L37

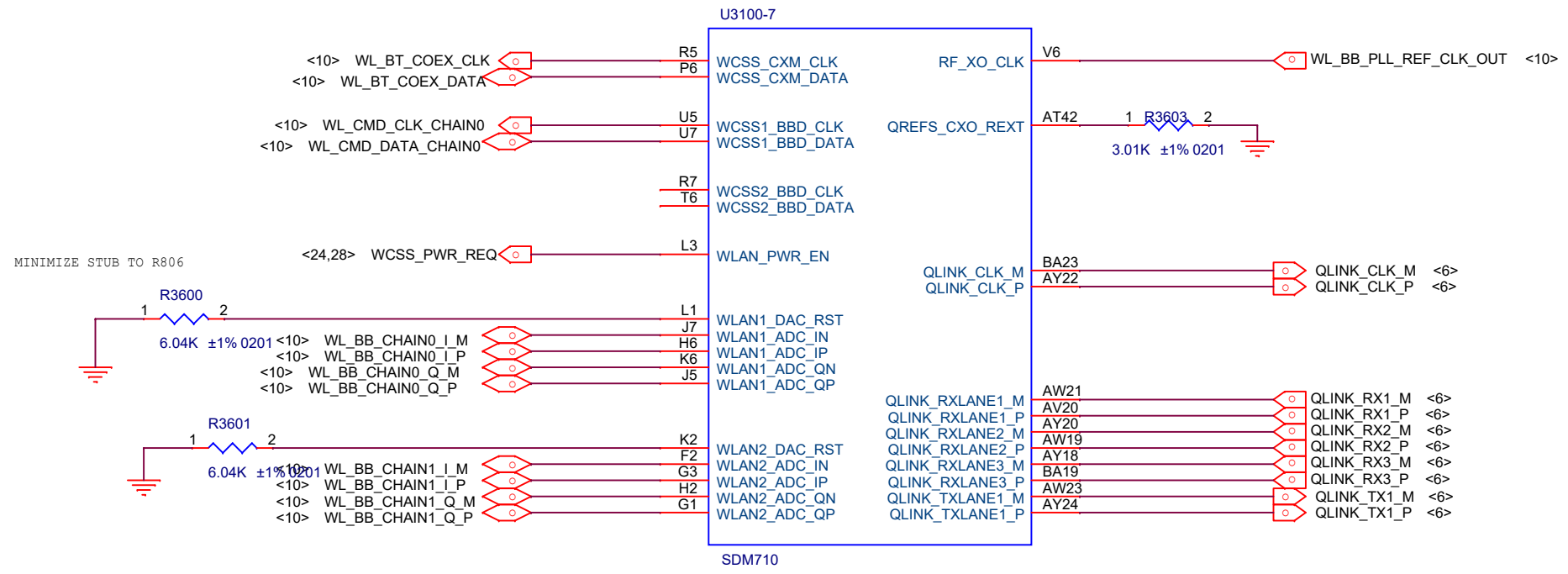
R43
T42
V42
W41
U41
V40
P42
R41
N41
P40

AV42
AU41


MIPI_DSI0_CLK_M <33>
MIPI_DSI0_CLK_P <33>
MIPI_DSI0_LANE0_M <33>
MIPI_DSI0_LANE0_P <33>
MIPI_DSI0_LANE1_M <33>
MIPI_DSI0_LANE1_P <33>
MIPI_DSI0_LANE2_M <33>
MIPI_DSI0_LANE2_P <33>
MIPI_DSI0_LANE3_M <33>
MIPI_DSI0_LANE3_P <33>

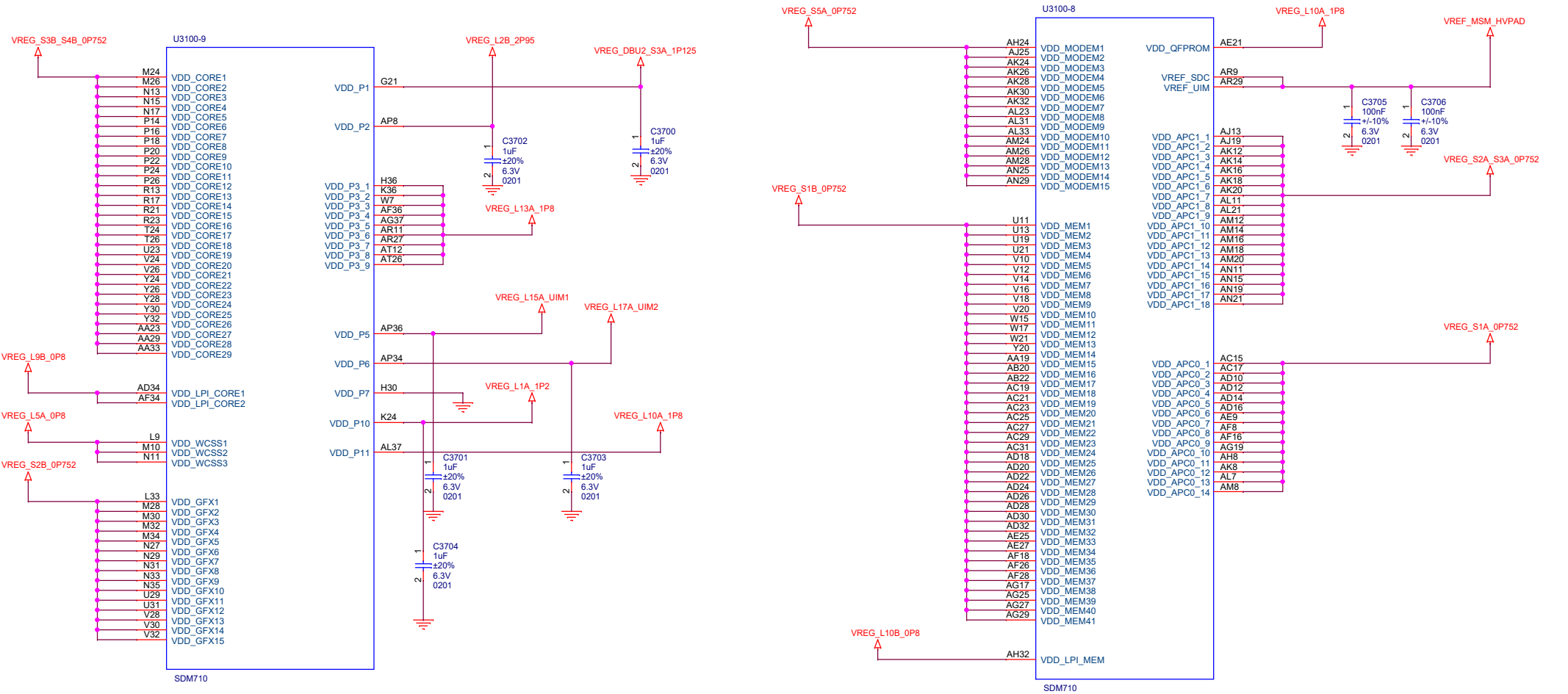


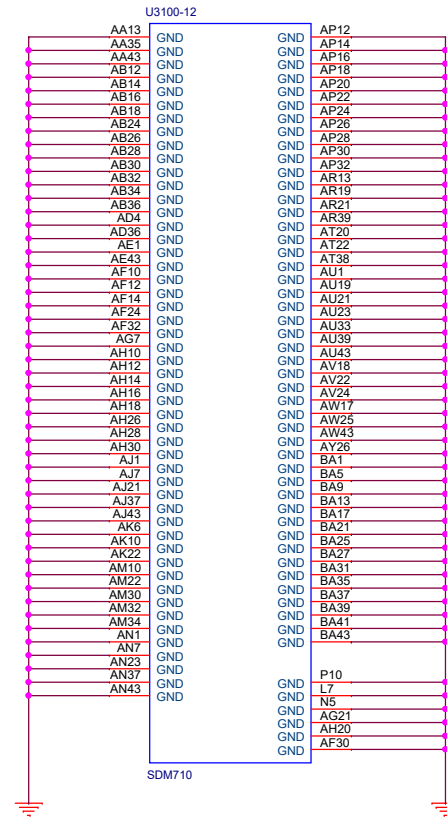
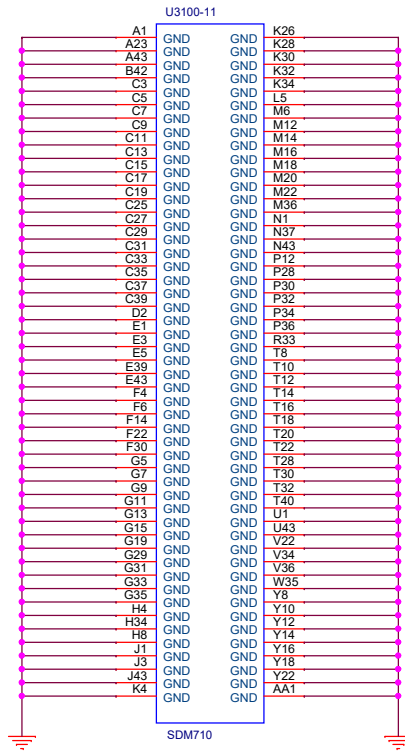
Title		
F3B		
Size	Document Number	Rev
A3	SIM CONNECTOR	P0
Date:	Sheet 37 of 37	

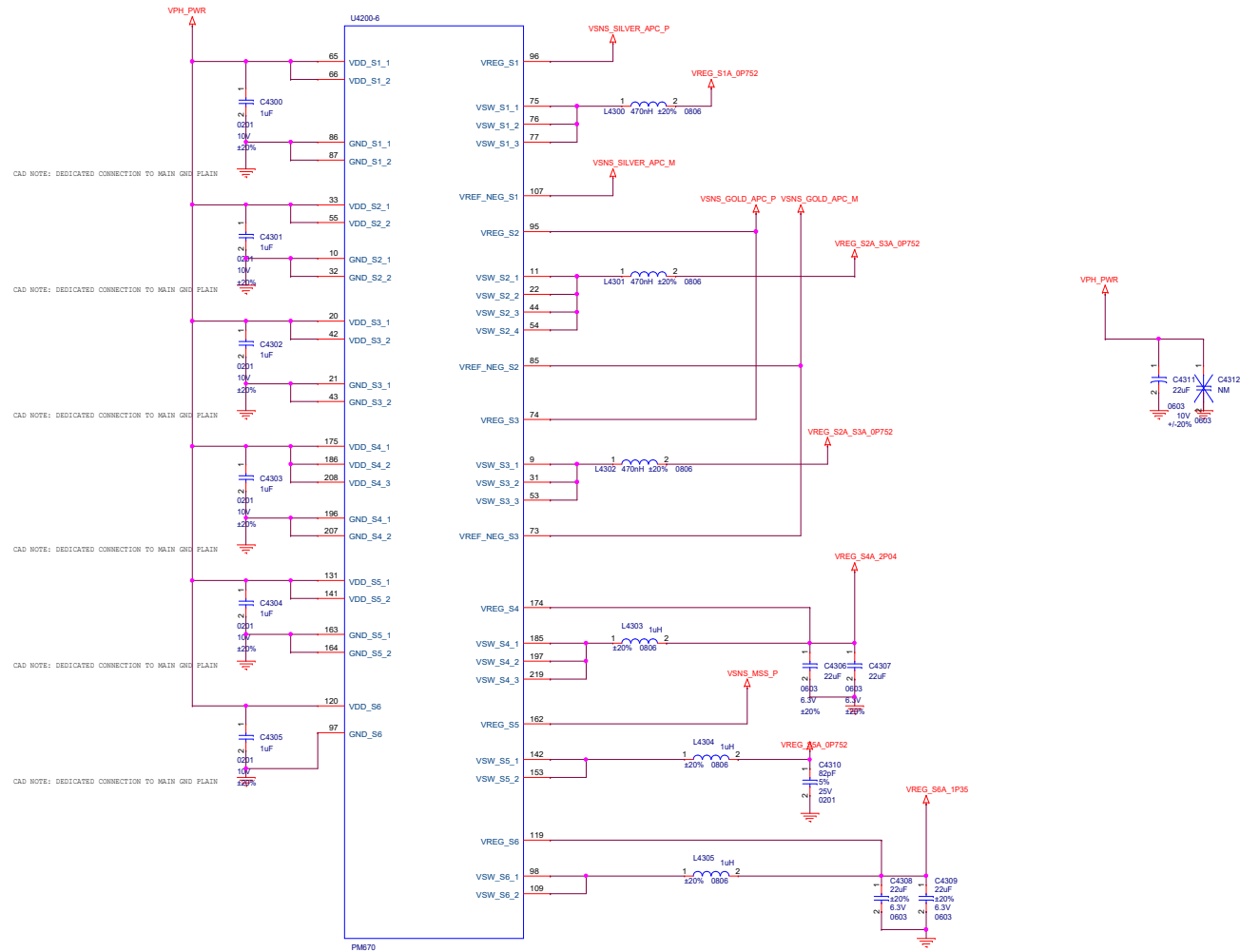


MINIMIZE STUB TO R806

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Size	A4	Document Number	SDM855 RF INTF		Rev
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CAD NOTE: DEDICATED CONNECTION TO MAIN GND PLAIN

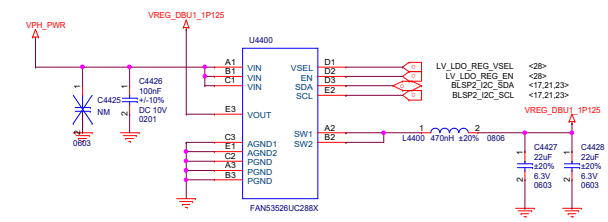
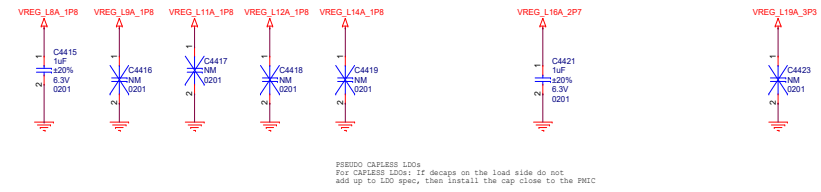
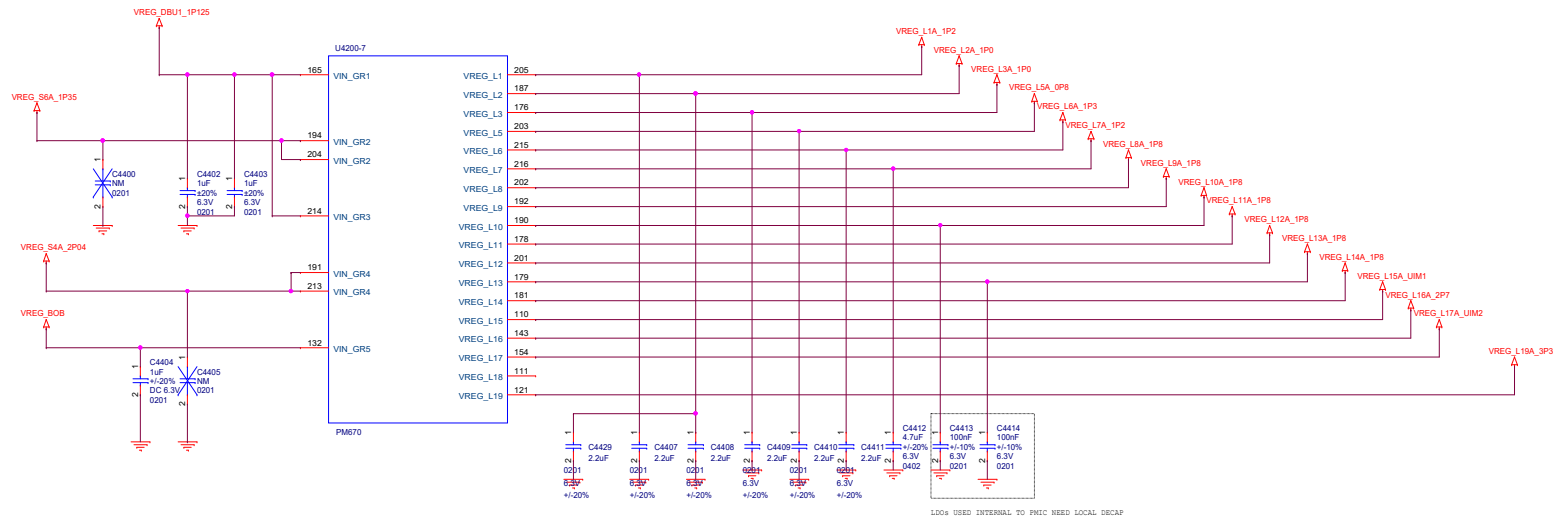
CAD NOTE: DEDICATED CONNECTION TO MAIN GND PLAIN

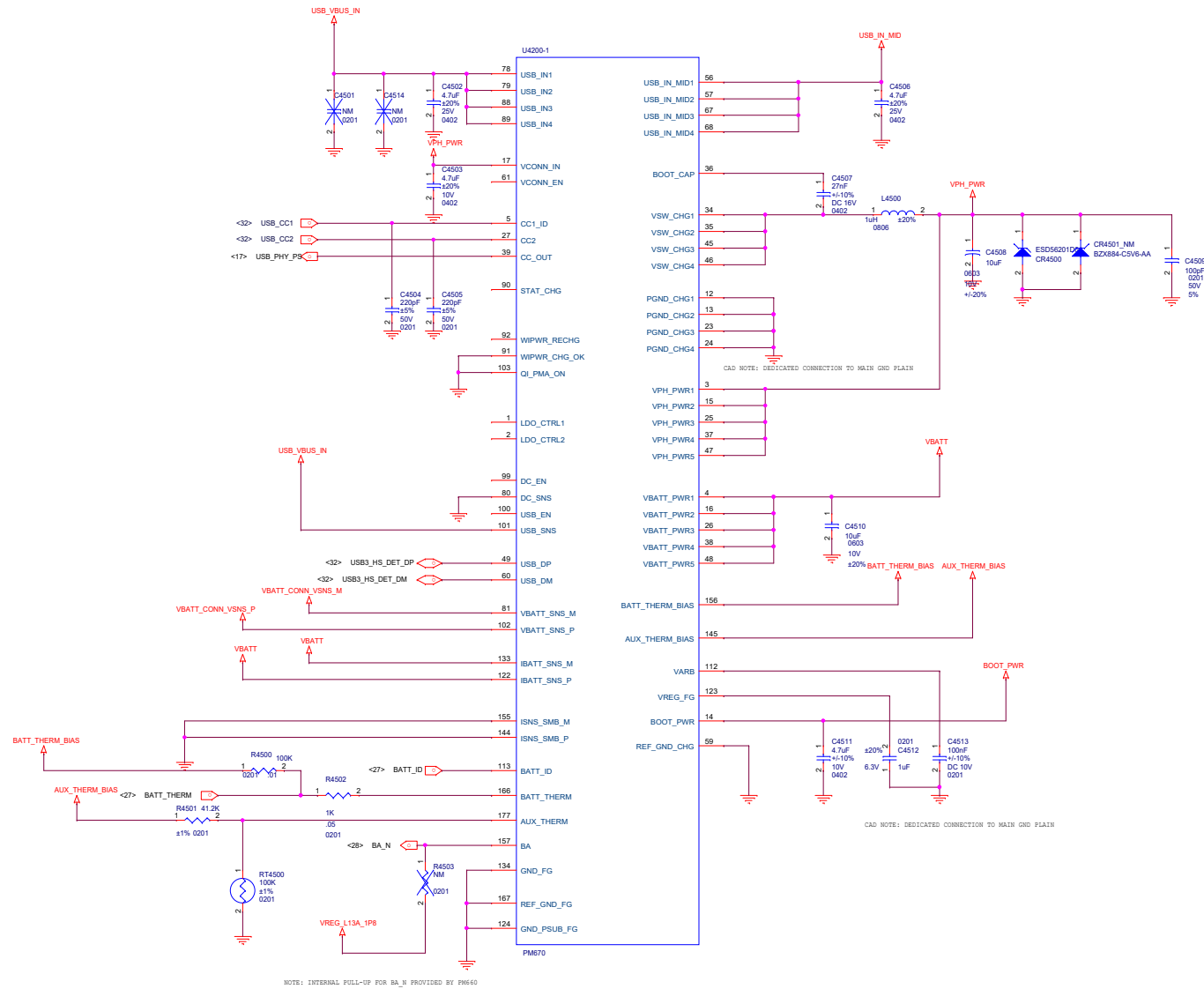
CAD NOTE: DEDICATED CONNECTION TO MAIN GND PLAIN

CAD NOTE: DEDICATED CONNECTION TO MAIN GND PLAIN

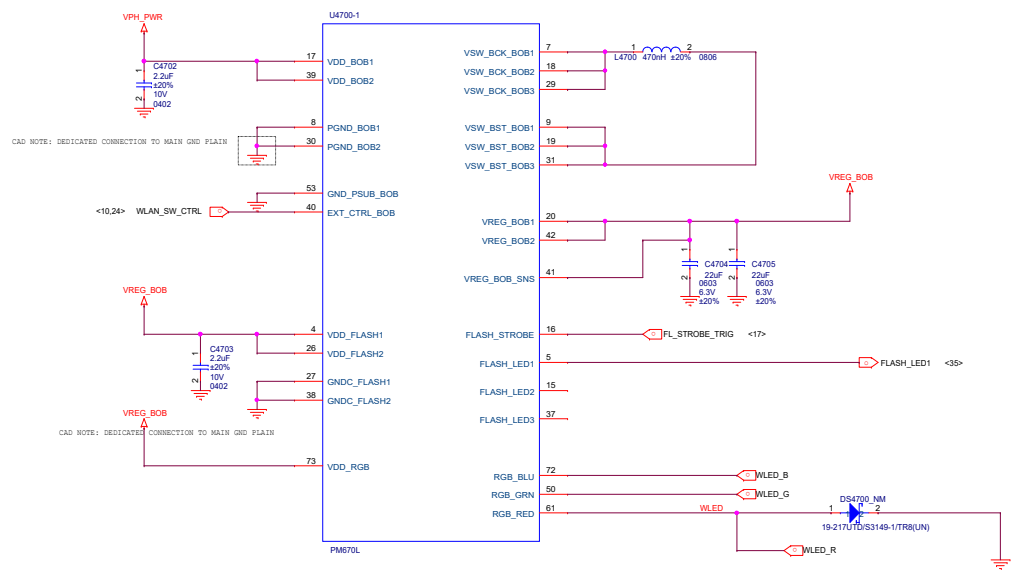
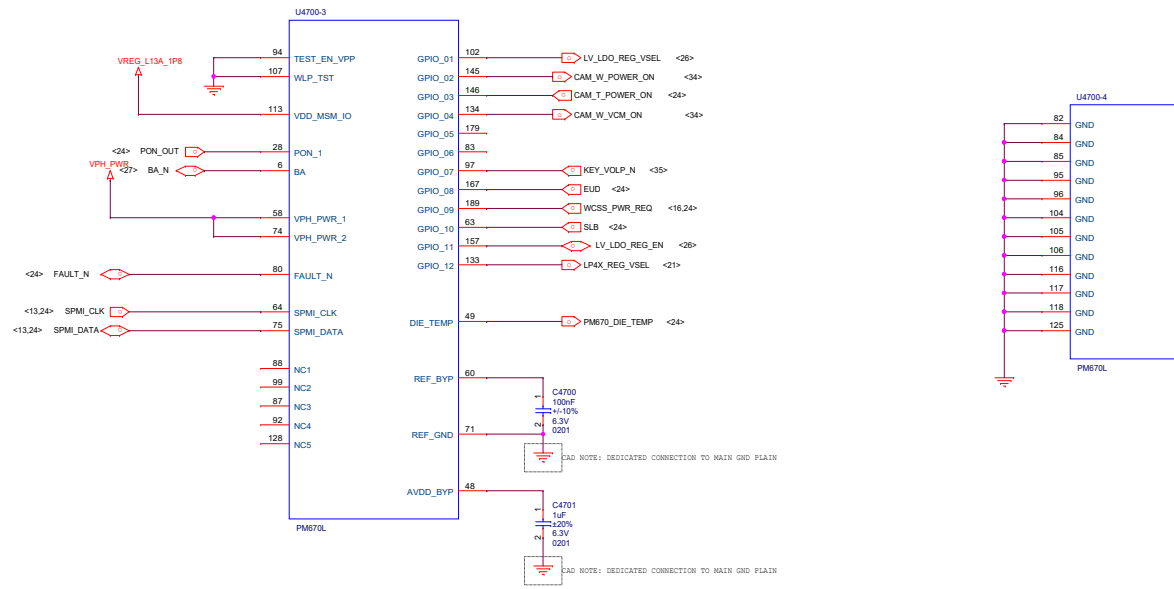
CAD NOTE: DEDICATED CONNECTION TO MAIN GND PLAIN

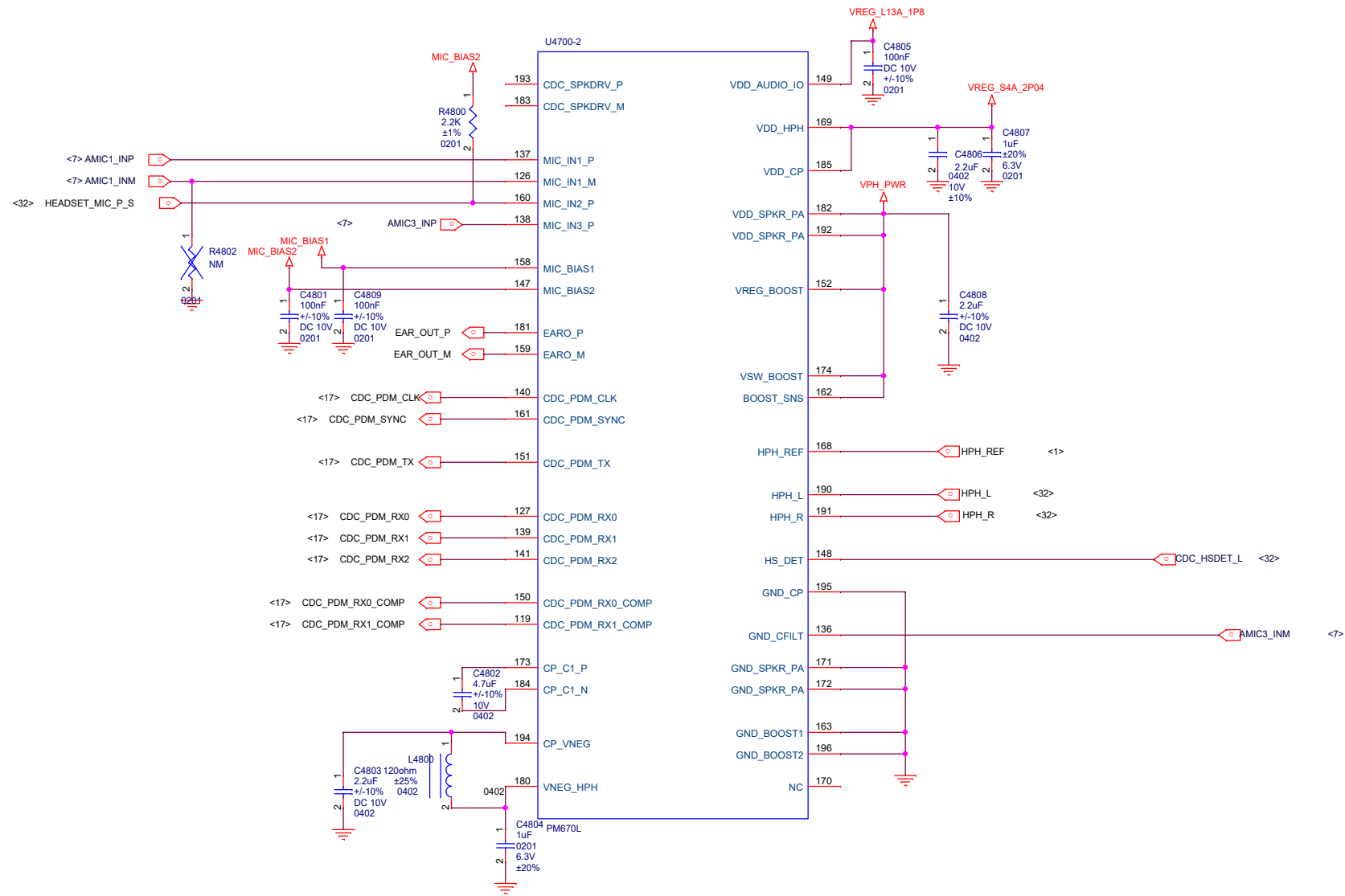
CAD NOTE: DEDICATED CONNECTION TO MAIN GND PLAIN

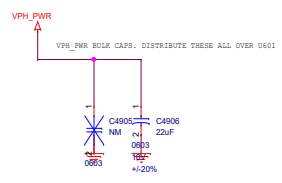
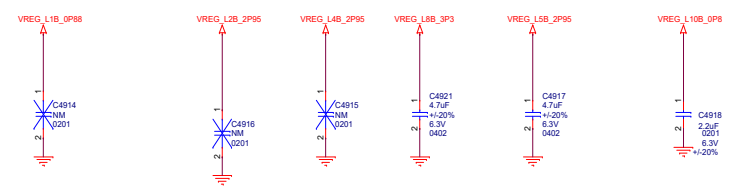
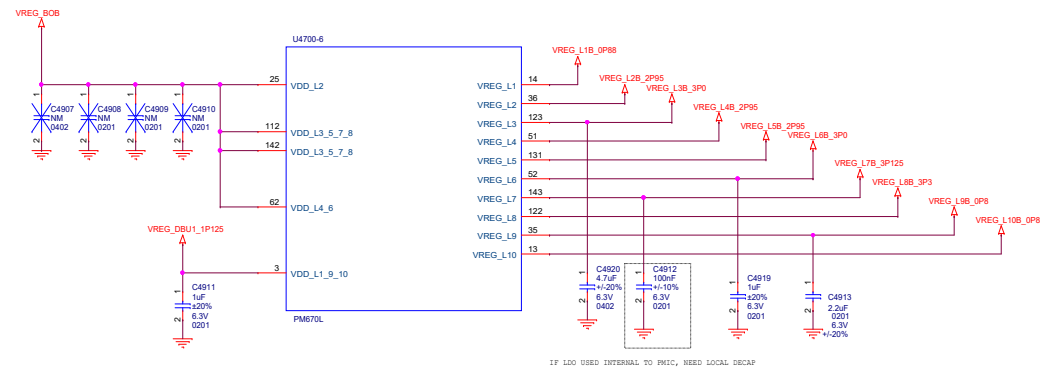
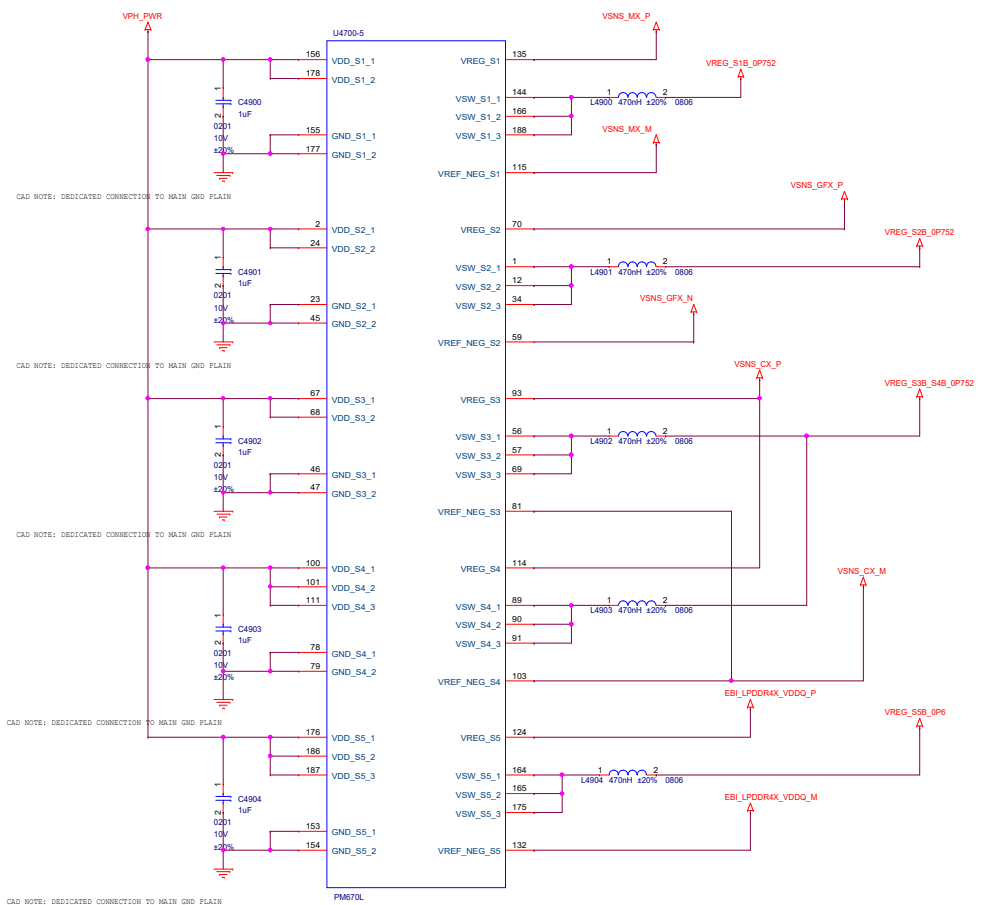


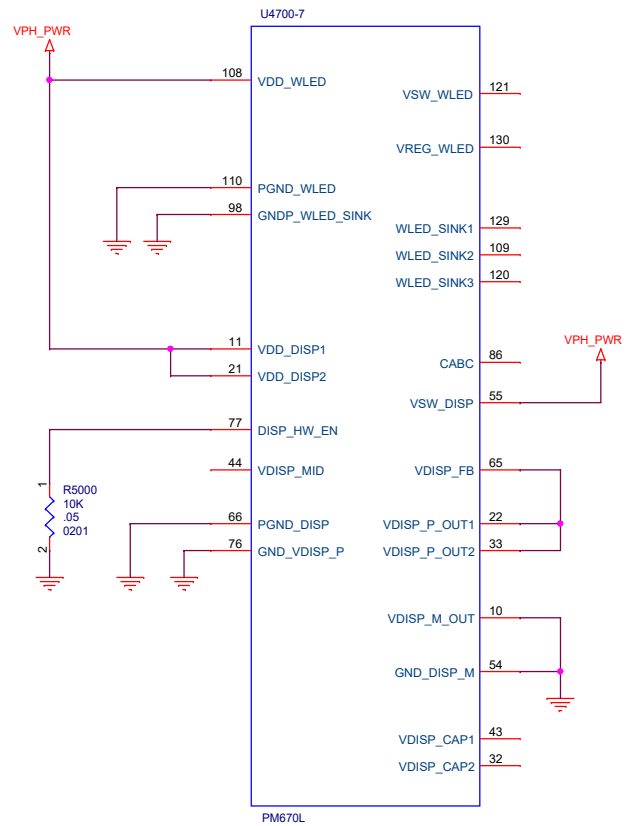


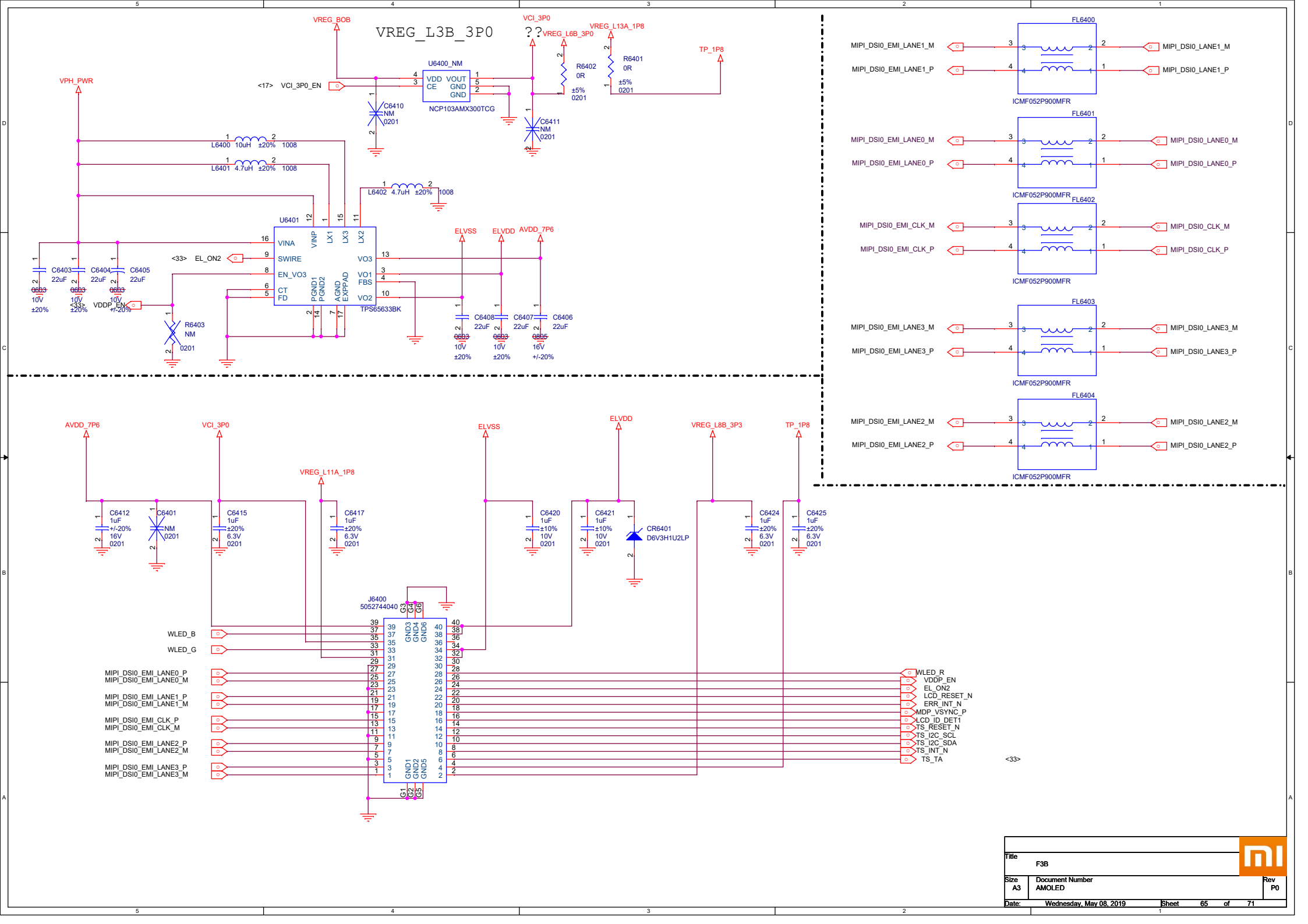
NOTE: INTERNAL PULL-UP FOR BA_N PROVIDED BY PM660








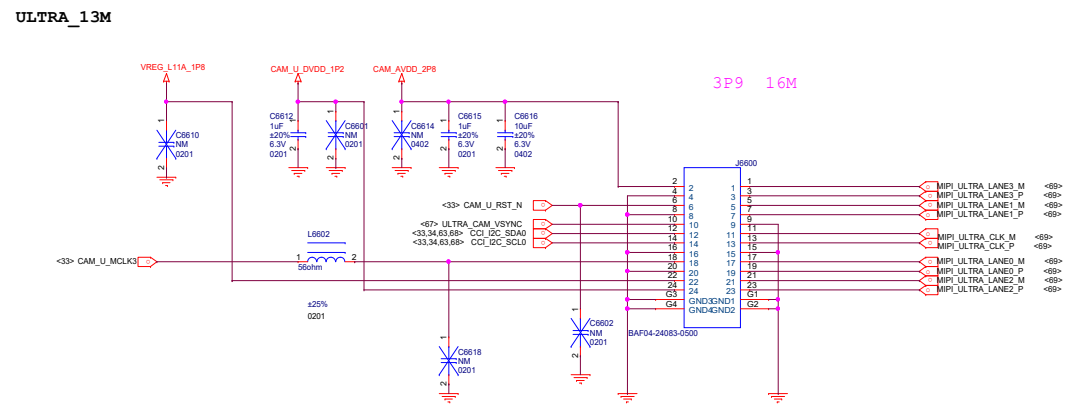
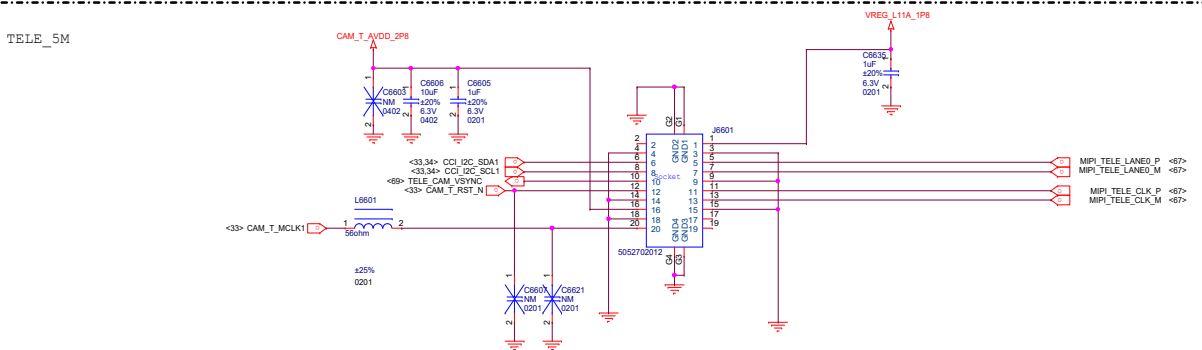
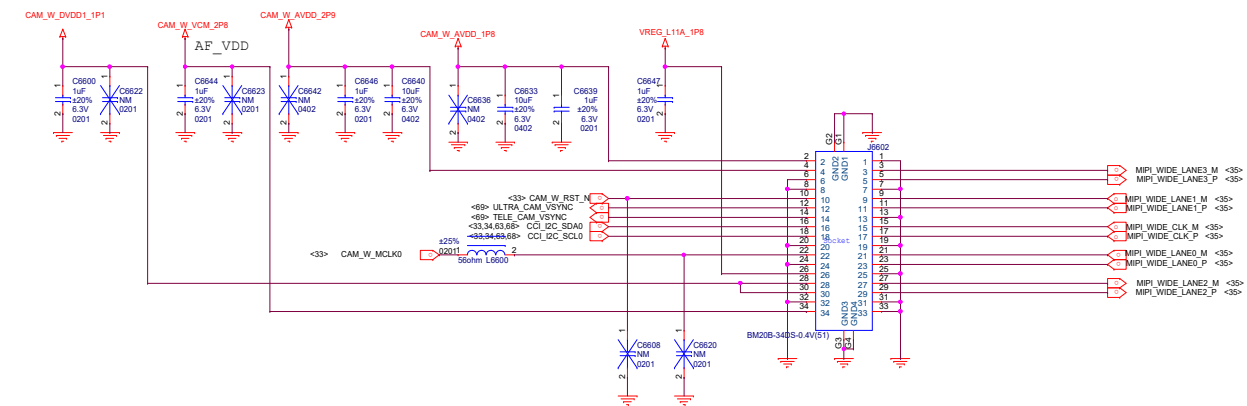




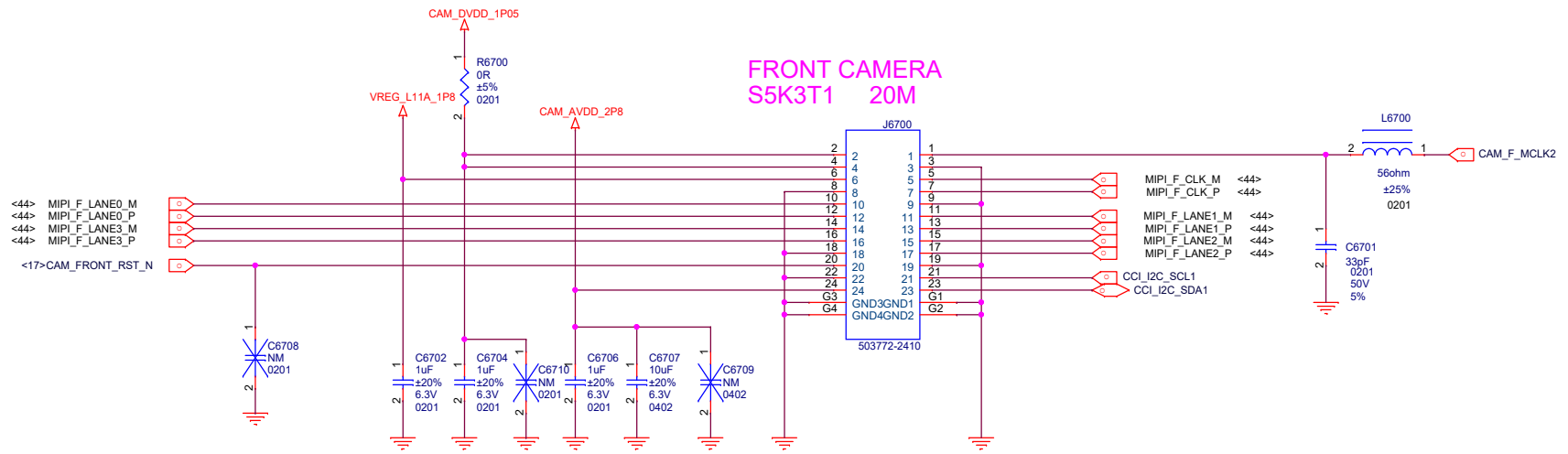
VREG_L3B_3P0

VREG_L11A_1P8

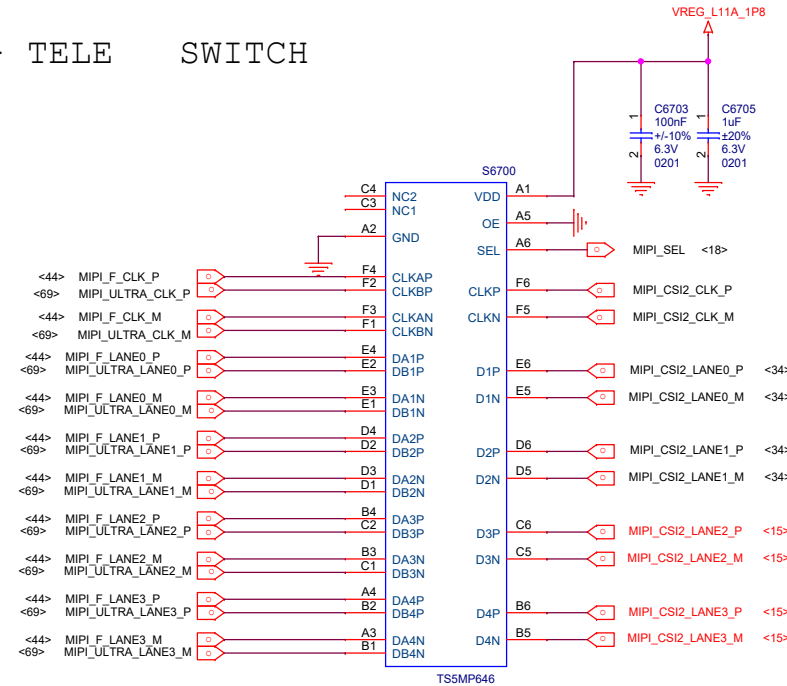
Title		
F3B		
Size	Document Number	Rev
A3	AMOLED	
Date:	Wednesday, May 08, 2019	Sheet
		65 of 71




FRONT CAMERA



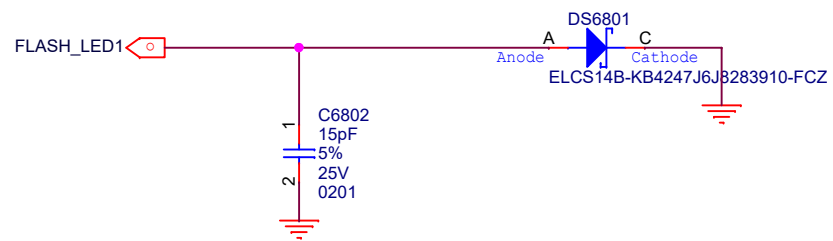
FRONT- TELE SWITCH




Title		F3B	
Size	Document Number Reserved(for Analog FM)		
Date:	Wednesday, May 08, 2019	Sheet 68	of 71

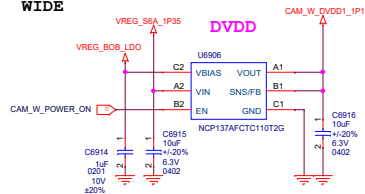
TOF

FLASH



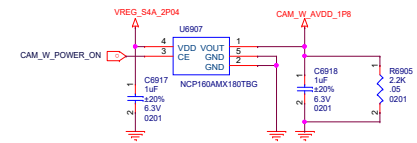
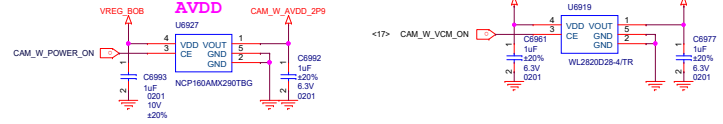
Title		F3B		
Size	Document Number	Rev		
A4	ULTRA CAMERA	P0		
Date:	Wednesday, May 08, 2019	Sheet	69 of 71	

WIDE



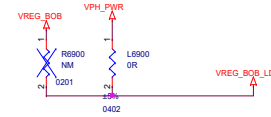
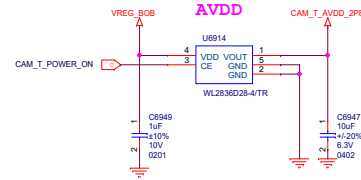
Camera Ldos

The 48M sensor IMX586 use 2.9V AVDD for some special reason



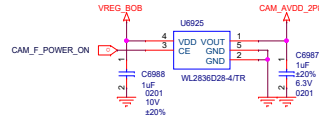
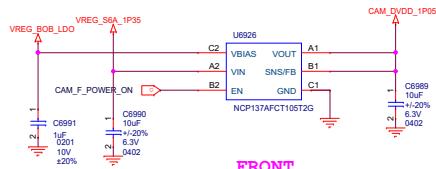
TELE

DVDD



FRONT

FRONT



ULTRA

