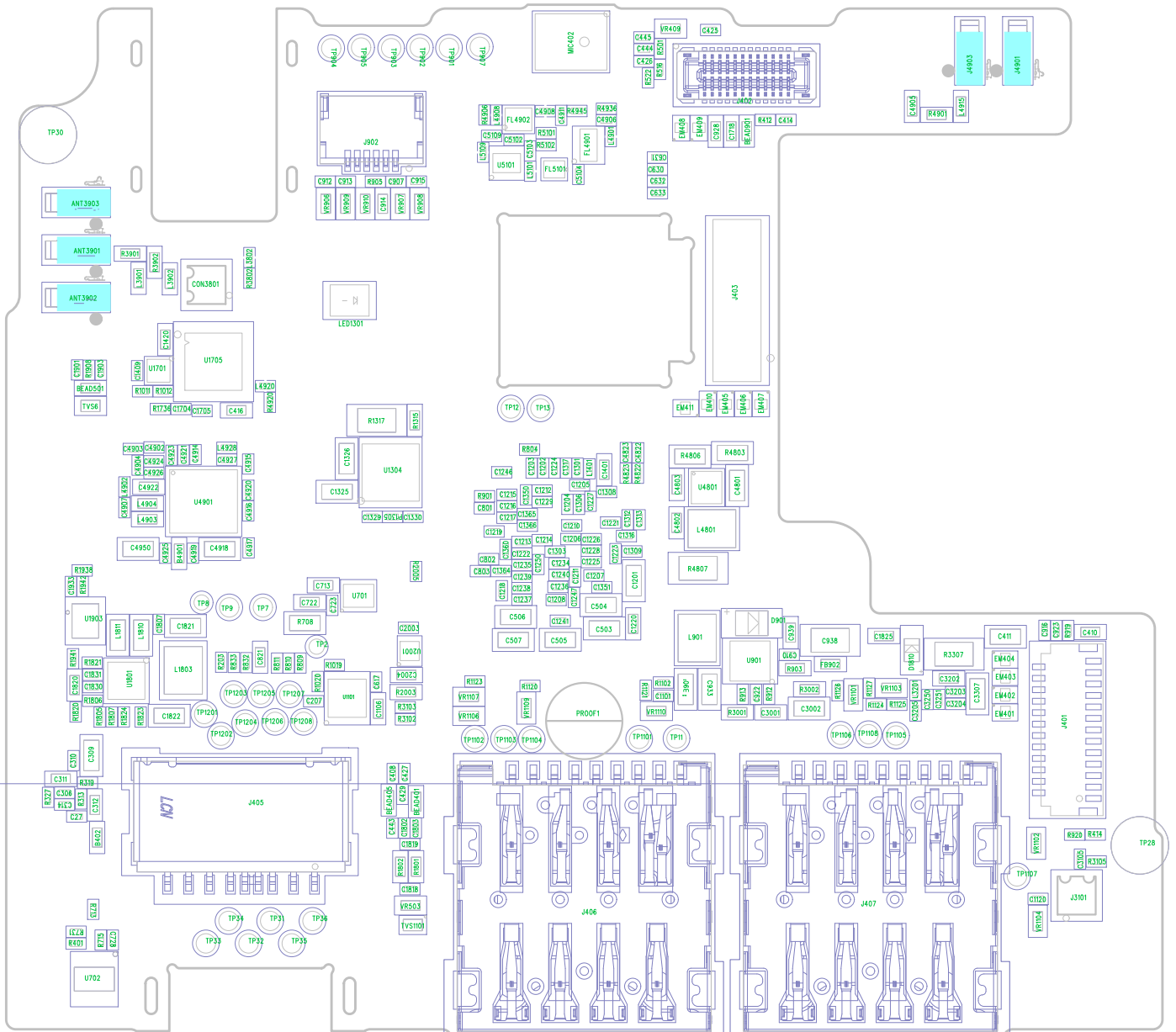
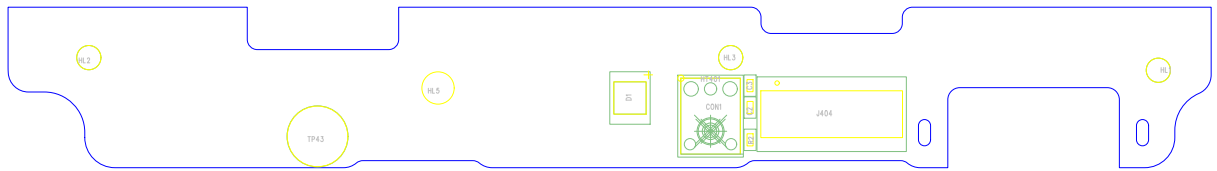


VIETMOBILE.VN



VIETMOBILE.VN





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01D.	GPIO Map	30.	WTR1605L POWER DISTRIBUTION
02.	PM8916 Control and MPP/Clock	31.	LTE/W/TD/GSM Antenna Switch
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09.	MSM8916: EBI	38.	DRX_Antenna Switch
10.	MSM8916: GPIO	39.	QFE1550 DRX Tunner
11.	MSM8916: MIPI and RF Control	40.	B7/40/41 DRX Switch
12.	MSM8916: POWER1	41.	B39/B1/B3 DRX Switch
13.	MSM8916: POWER2	42.	WTR2605_SECONDARY PATH
14.	MSM8916:GND	43.	WTR2605_POWER DISTRIBUTION
15.	MEMORY:LPDDR3+EMMC	44.	Antenna_SECONDARY PATH
16.	Battery Connector	45.	RESERVED
17.	Subboard Connector	46.	RESERVED
18.	Mic and Receiver	47.	CDMA BC0(Voice) TRX
19.	EARPHONE	48.	ET_APT
20.	LCD interface and backlight	49.	WCN3620
21.	Main/Slave Camera and Flash	50.	WIFI FEM
22.	Sensors	51.	GPS/XO DISTRIBUTOR
23.	SIM/TF card	52.	NFC
24.	Keypad/LED/Status indicator		
25.	Touch Interface		
26.	Test Point/GND/Shields		

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Revision History

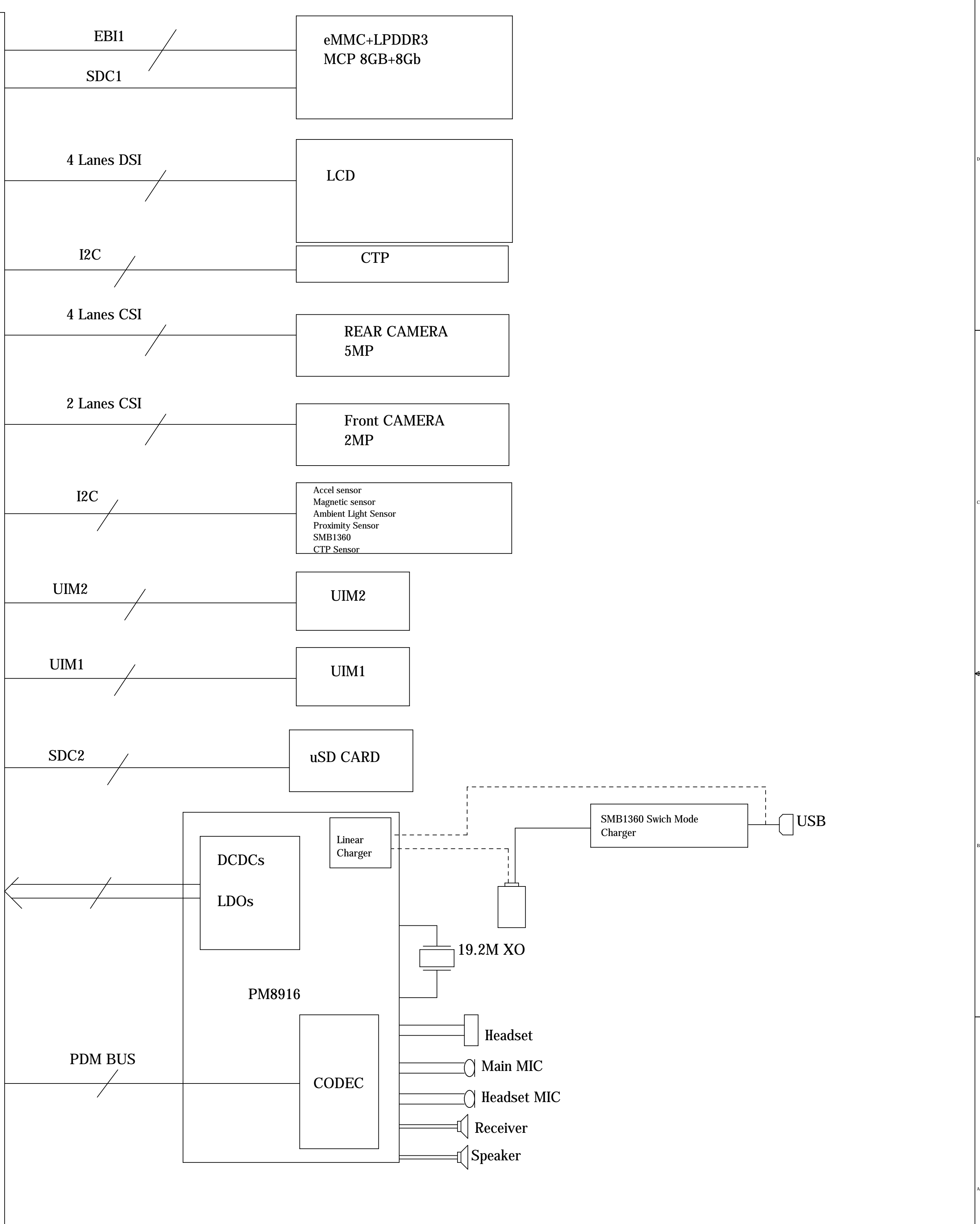
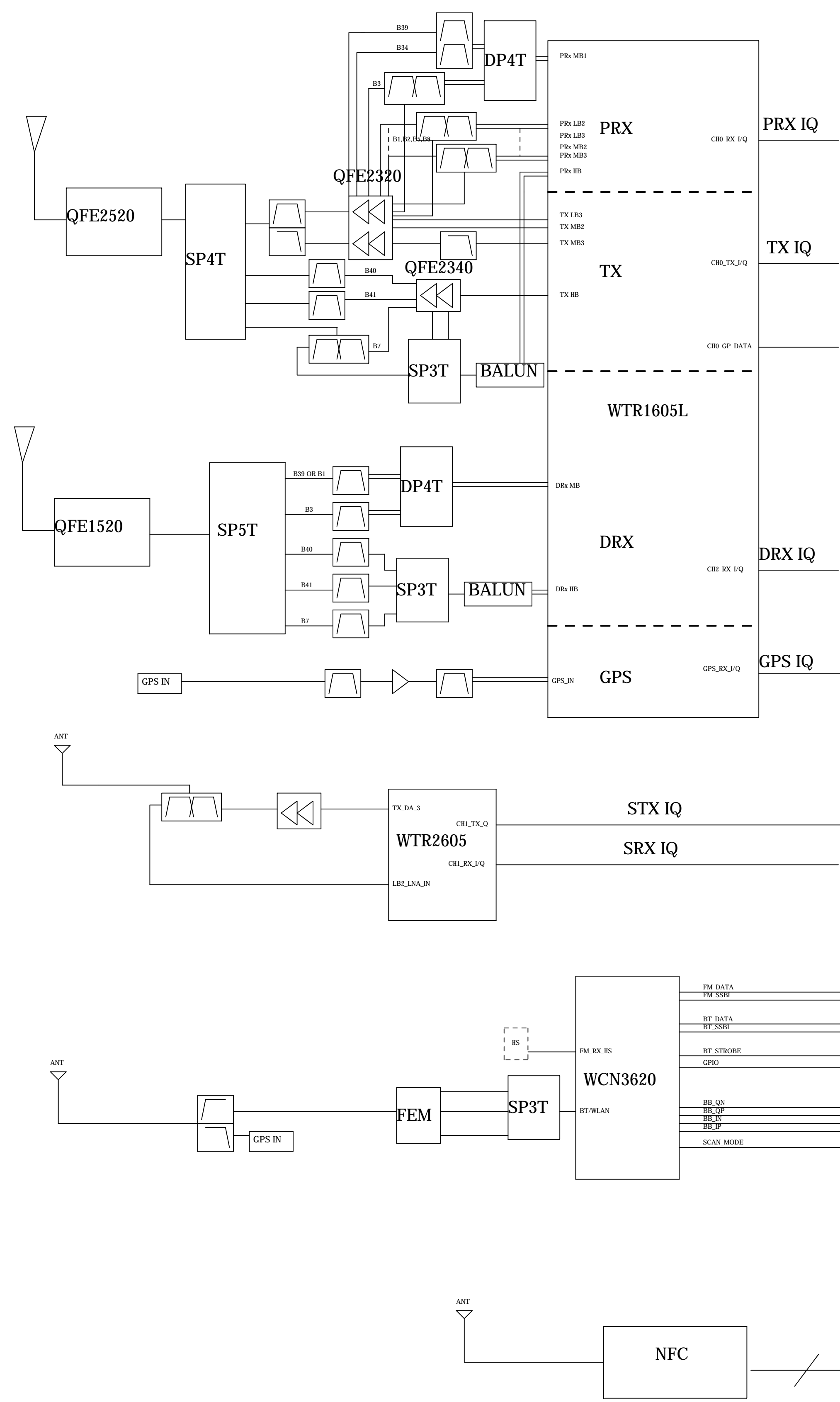
SCHEMATIC MCN	HW VERSION	VERSION	DESCRIPTION OF CHANGE
	EVT1.0	A	EVT1.0 Initial Release

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Date: _____	Rev: _____
Name: _____	Sheet: _____ of _____



MSM8916 GPIO Configuration For QRD8916

GPIO_0	NC	GPIO_41	WCSS_WLAN_DATA_1	GPIO_82	WTR1_SWITCH_CTL3
GPIO_1	NC	GPIO_42	WCSS_WLAN_DATA_0	GPIO_83	WTR1_GSM_PA_HB_EN
GPIO_2	NC	GPIO_43	WCSS_WLAN_SET	GPIO_84	WTR1_SWITCH_CTL0
GPIO_3	NC	GPIO_44	WCSS_WLAN_CLK	GPIO_85	DRX_MODE_SEL2
GPIO_4	UART_MSM_TX	GPIO_45	WCSS_FM_SSB1	GPIO_86	NC
GPIO_5	UART_MSM_RX	GPIO_46	WCSS_FM_SDI	GPIO_87	WTR0_PA_R1
GPIO_6	SENSOR_I2C_SDA	GPIO_47	WCSS_BT_CTRL	GPIO_88	WTR1_PA_R1
GPIO_7	SENSOR_I2C_SCL	GPIO_48	WCSS_BT_DAT_STB	GPIO_89	WTR1_PA_R0
GPIO_8	NC	GPIO_49	LCD_ID	GPIO_90	DRX_MODE_SEL0
GPIO_9	NC	GPIO_50	CTP_ID	GPIO_91	GRFC_17
GPIO_10	NC	GPIO_51	MCAM_ID	GPIO_92	WTR1_PA_BC0_EN
GPIO_11	NC	GPIO_52	NC	GPIO_93	DRX_MODE_SEL1
GPIO_12	TP_RST_N	GPIO_53	UIM2_DATA	GPIO_94	WTR0_PA_R0
GPIO_13	TP_INT_N	GPIO_54	UIM2_CLK	GPIO_95	WTR1_SWITCH_CTRL2
GPIO_14	BUCK_SMB_I2C_SDA	GPIO_55	UIM2_RESET	GPIO_96	EXT_GPS_LNA_EN
GPIO_15	BUCK_SMB_I2C_SCL	GPIO_56	NC	GPIO_97	NC
GPIO_16	NC	GPIO_57	UIM1_DATA	GPIO_98	NC
GPIO_17	NC	GPIO_58	UIM1_CLK	GPIO_99	CH0_GSM_TX_PHASE_D0
GPIO_18	TP_I2C_SDA	GPIO_59	UIM1_RESET	GPIO_100	CH0_GSM_TX_PHASE_D1
GPIO_19	TP_I2C_SCL	GPIO_60	NC	GPIO_101	CH1_GSM_TX_PHASE_D0
GPIO_20	NFC_DISABLE	GPIO_61	UIM_BATT_ALM	GPIO_102	CH1_GSM_TX_PHASE_D1
GPIO_21	NFC_INT_N	GPIO_62	SMB_INT	GPIO_103	CH0_SSB1_WTR_RX
GPIO_22	NFC_I2C_SDA	GPIO_63	CDC_PDM_CLK	GPIO_104	CH0_SSB1_WTR_TX
GPIO_23	NFC_I2C_SCL	GPIO_64	CDC_PDM_SYNC	GPIO_105	CH1_SSB1_WTR_RX
GPIO_24	LCD_TE0	GPIO_65	CDC_PDM_TX	GPIO_106	CH1_SSB1_WTR_TX
GPIO_25	LCD_RST_N	GPIO_66	CDC_PDM_RX0	GPIO_107	KEY_VOL_UP_N
GPIO_26	CAM0_MCLK	GPIO_67	CDC_PDM_RX1	GPIO_108	NC
GPIO_27	CAM1_MCLK	GPIO_68	CDC_PDM_RX2	GPIO_109	NC
GPIO_28	SCAM_RST_N	GPIO_69	NC	GPIO_110	USB_HS_ID
GPIO_29	CAM_I2C_SDA	GPIO_70	RFFE1_CLK	GPIO_111	NC
GPIO_30	CAM_I2C_SCL	GPIO_71	RFFE1_DATA	GPIO_112	ACCL_INT1_N
GPIO_31	CAM_FLASH_TORCH_EN	GPIO_72	RFFE2_CLK	GPIO_113	ALSP_INT_N
GPIO_32	CAM_FLASH_TORCH_MODE	GPIO_73	RFFE2_DATA	GPIO_114	ACCL_INT2_N
GPIO_33	SCAM_PWDN	GPIO_74	NC	GPIO_115	GYRO_INT_N
GPIO_34	MCAM_PWDN	GPIO_75	GRFC_1	GPIO_116	NC
GPIO_35	MCAM_RST_N	GPIO_76	GRFC_2	GPIO_117	NC
GPIO_36	NC	GPIO_77	WTR1_GSM_PA_LB_EN	GPIO_118	NC
GPIO_37	FORCE_USB_BOOT	GPIO_78	TDS_B34/B39_PA_ON	GPIO_119	CTP_LED_EN
GPIO_38	NC	GPIO_79	WTR1_SWITCH_CTL1	GPIO_120	CAM_VCM_PWDN
GPIO_39	WCSS_BT_SSB1	GPIO_80	WTR0_RX_ON	GPIO_121	NC
GPIO_40	WCSS_WLAN_DATA_2	GPIO_81	WTR0_RF_ON		

PM8916 GPIO/MPP Configuration For QRD8916

GPIO_1	UIM_BATT_ALM	MPP_1	VDD_PX_BIAS_MPP
GPIO_2	NFC_CLK_REQ	MPP_2	SCAM_ID
GPIO_3	NC	MPP_3	VREF_DAC_MPP
GPIO_4	NC	MPP_4	PWM_OUT

Sensors I2C Address List For QRD8916

I2C GPIO	Device	MPN	Write Address	Read Address
GPIO[6:7]	Accelerometer	BMA223	0x30	0x31
	Magnetometer	MMC34160PJ	0x60	0x61
	Gyroscope	MPU-6050C	0xD0	0xD1
	ALS/Proximity	TMD27723	0x72	0x73
GPIO[18:19]	Touch Screen	FT6346	0x70	0x71
GPIO[29:30]	Front Camera	OV2680	0x20	0x21
	Rear Camera	OV5648	0x6C	0x6D

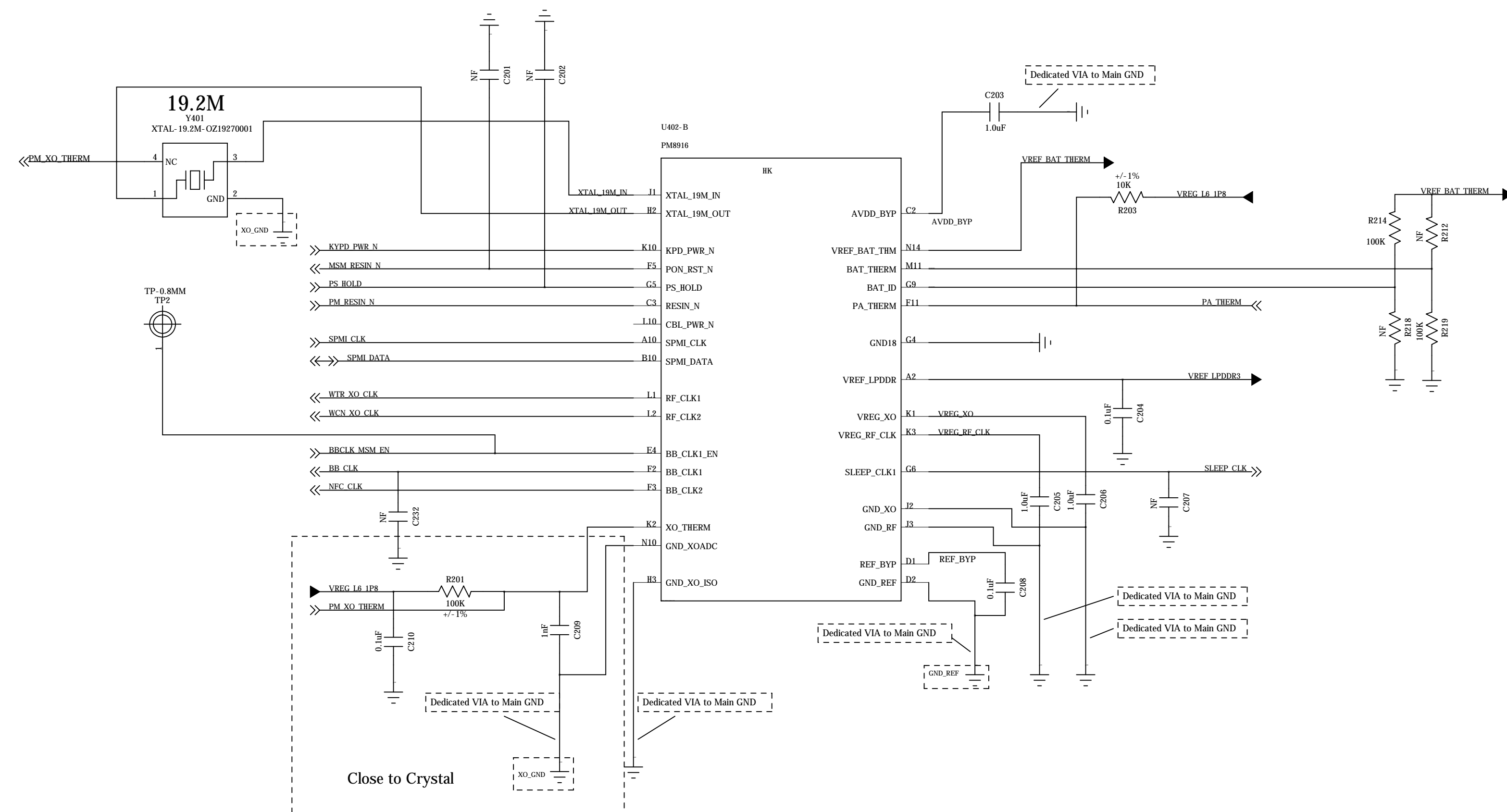
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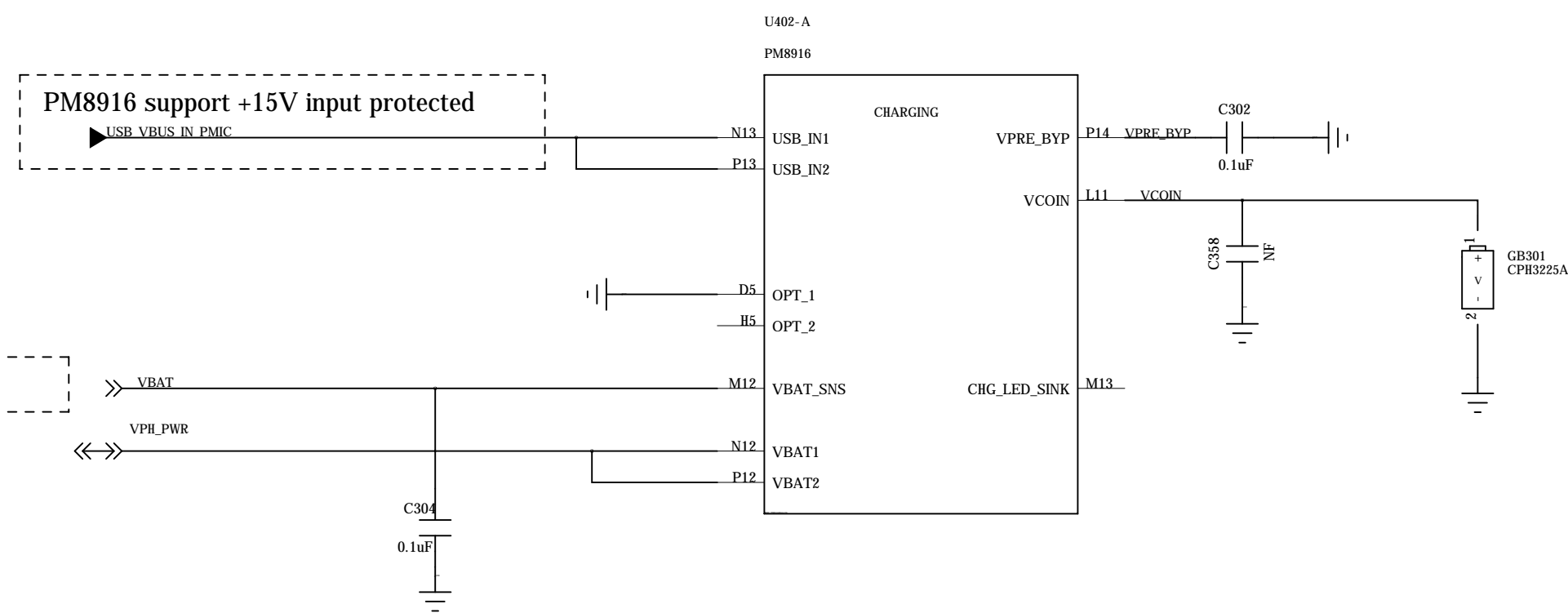
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Pins	Hi Z	GND
OPT_1	No SMB1360	SMB1360 present
OPT_2	No Ext APC buck (S5)	Ext APC buck (S5) present

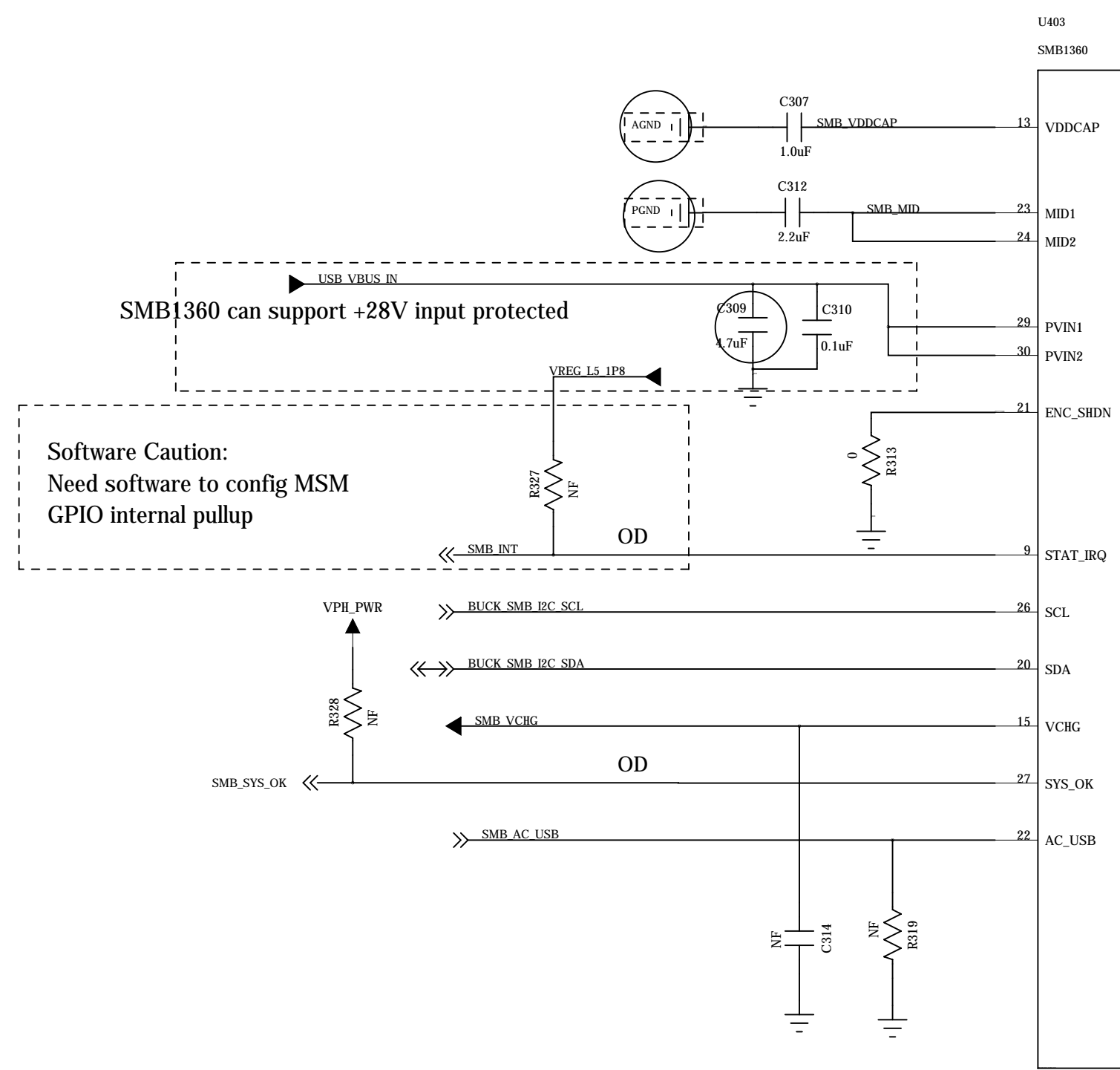
Dedicated sense trace to battery connector



PM8916 support +15V input protected
 USB_VBUS_IN_PMIC

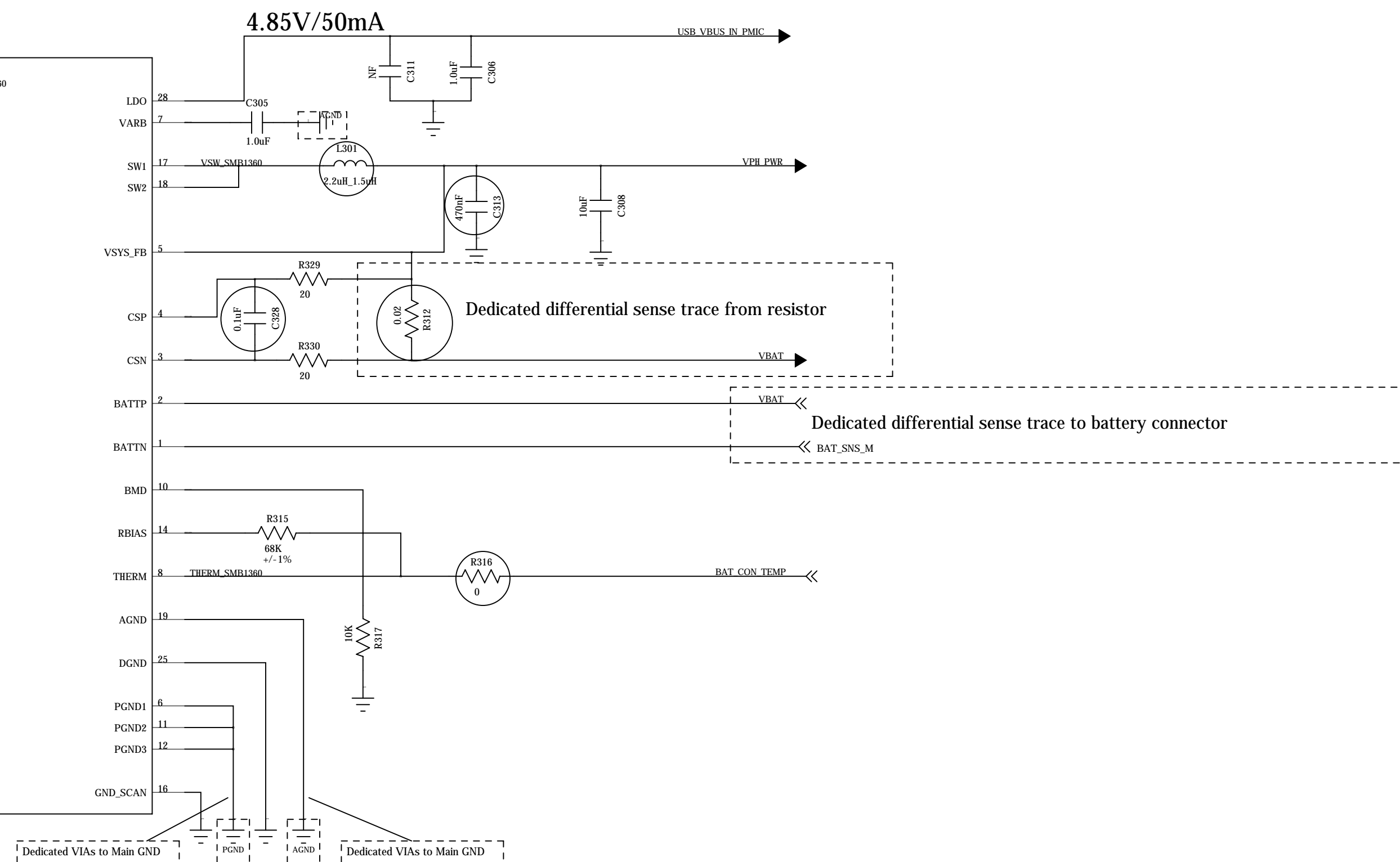
Close to connector
 if using PMIC's linear charger
 C0301 should assemble 4.7uF/16V capacitor
 R0307 should assemble 0 ohm
 R0302 should NC
 R0312 should be 0ohm
 BAT THERM switch to PM8916

Already have 47K resistor in subboard



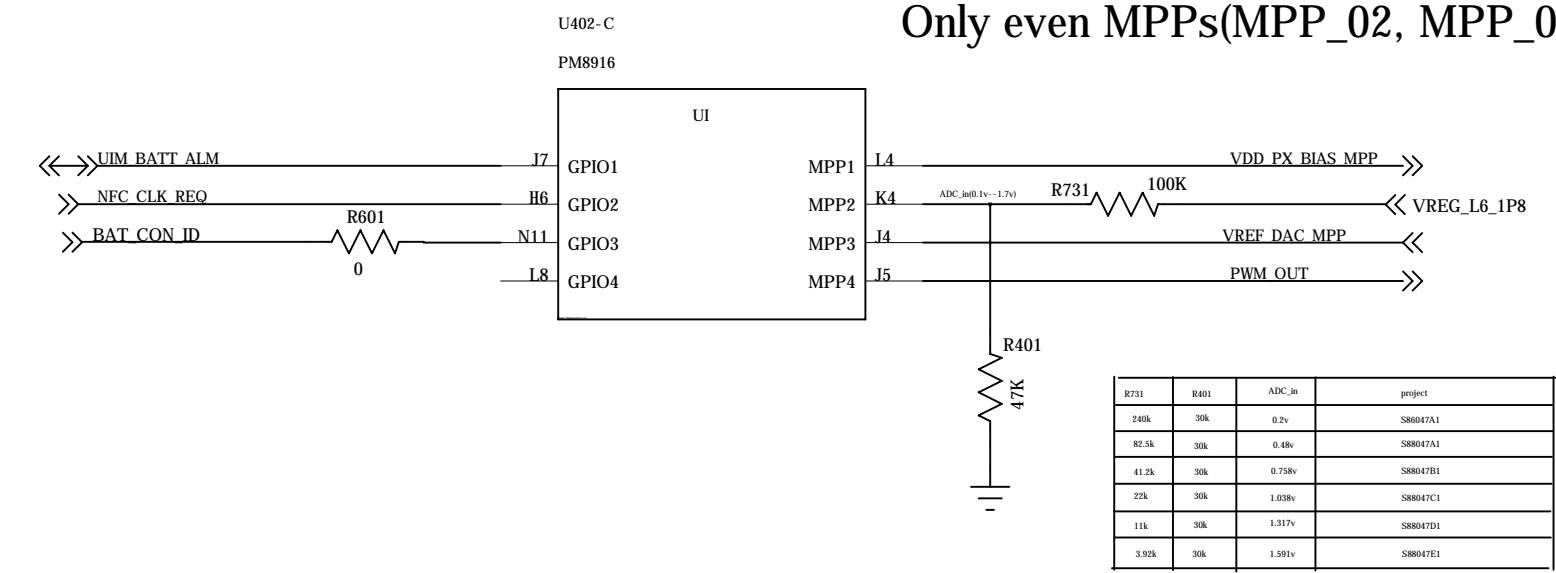
SMB1360 can support +28V input protected

Software Caution:
 Need software to config MSM
 GPIO internal pullup

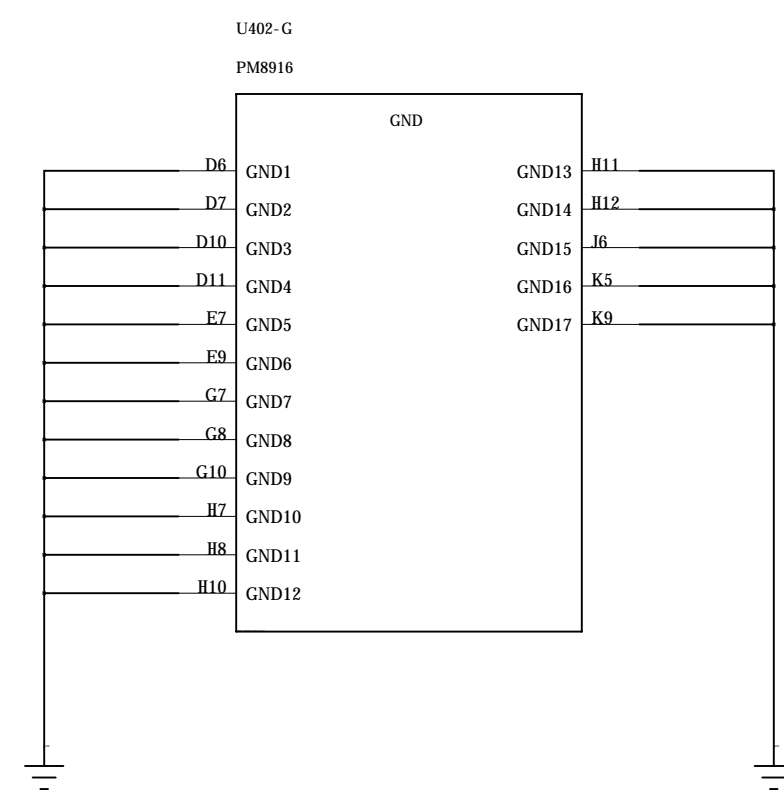


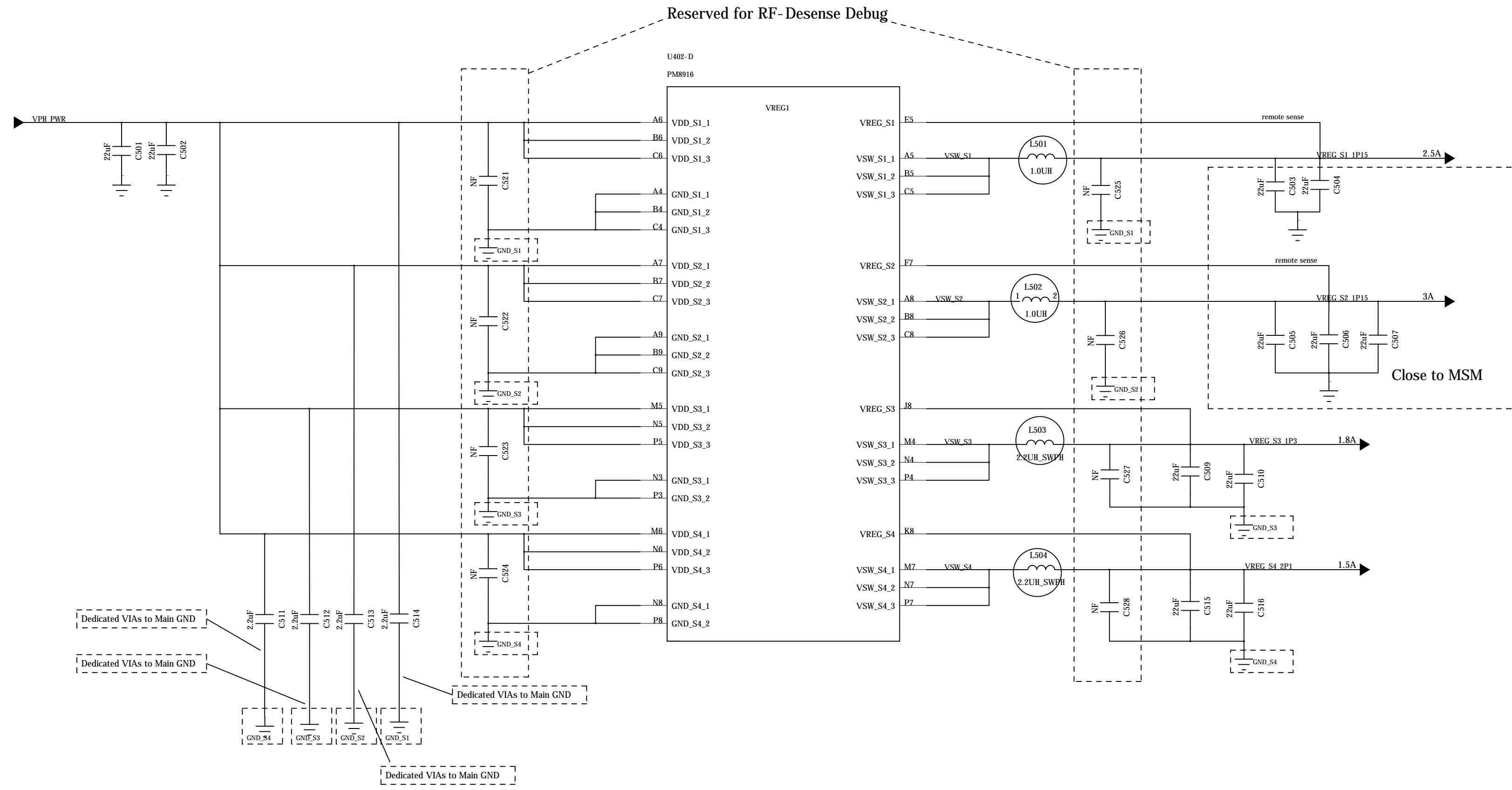
NOTE1: Separate the AGND from the PGND

Only odd MPPs (MPP_01, MPP_03) can be configured as analog outputs.
 Only even MPPs(MPP_02, MPP_04) have current sink capability.



REF	VALUE	UNIT	DESCRIPTION
R731	100K	Ω	MPP2
R801	10K	Ω	MPP3
R401	47K	Ω	MPP4
	10K	Ω	MPP1
	10K	Ω	MPP2
	10K	Ω	MPP3
	10K	Ω	MPP4





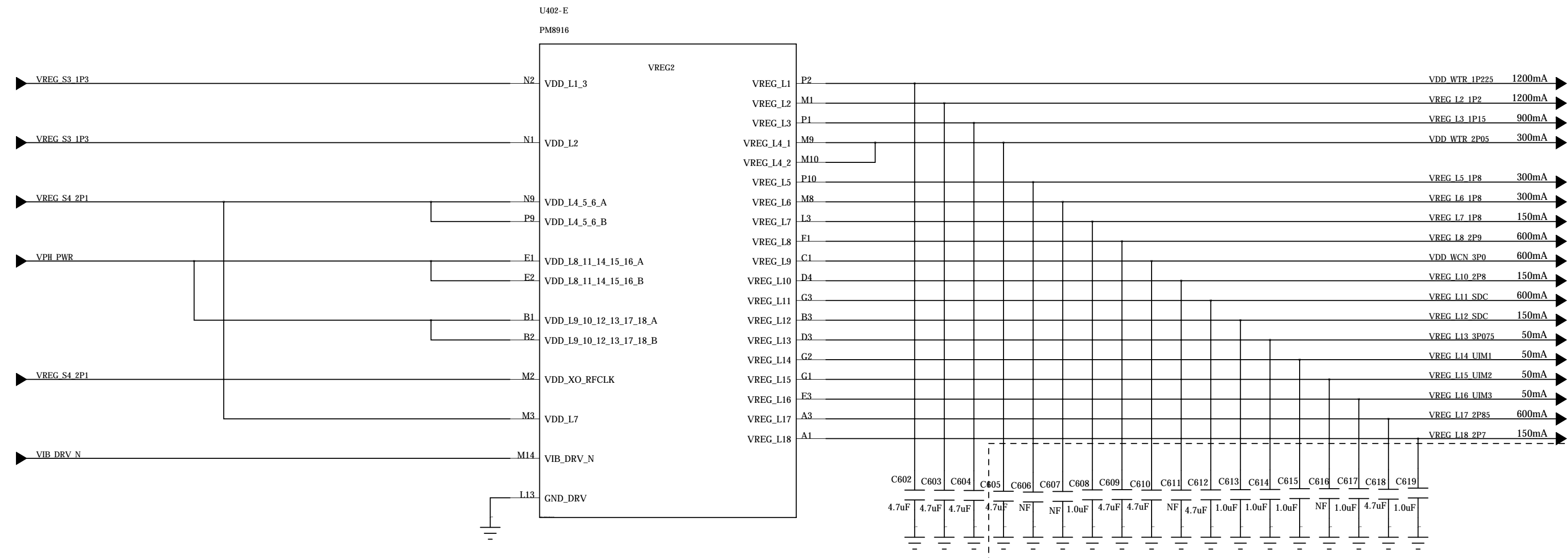
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Software Caution:
L16 Need to Config to 1.8V and turn on later than L17

PSEUDO CAPLESS LDOs
L4, L5, L6, L7, L8, L9, L10, L11, L12, L13, L14, L15, L16, L17, L18

Local output capacitor can NC/Remove depends on remote load capacitors and PCB layout
LDOs L8, L9, L11, L17 each shall have total effective output capacitance of 4.7uF.
Please refer to PM8916 training slides(80-NK808-21) for limits on LDO output trace resistance and inductance
Rest of the LDOs shall each have total effective output capacitance of 1.0uF.

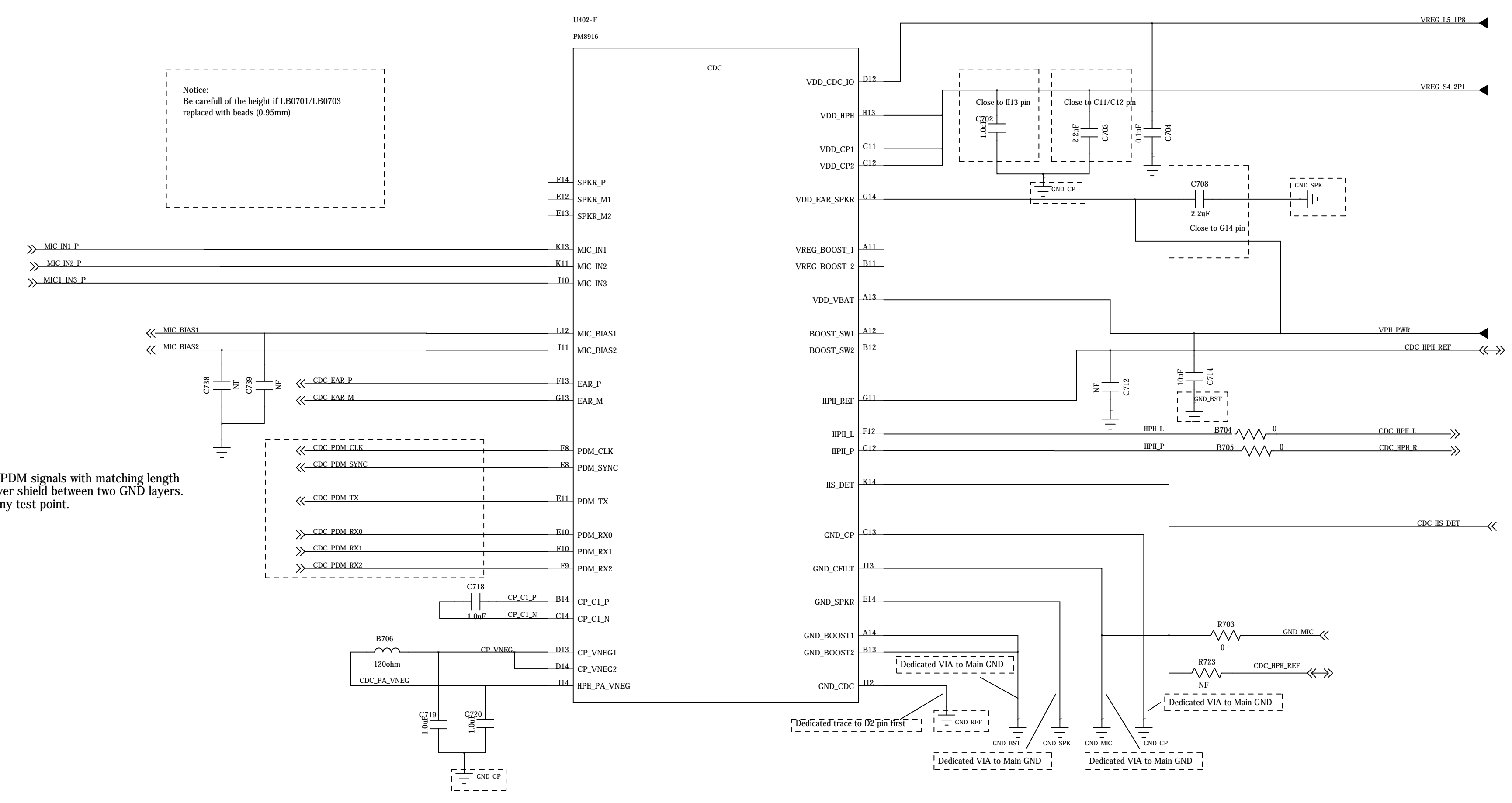
The Pseudo capless LDOs must comply to following tolerable routing requirement:
P50: R<400mOhms, L<45nH
P150: R<200mOhms, L<20nH
P300: R<150mOhms, L<18nH
P600: R<150mOhms, L<18nH

Please ensure when you DNI the output capacitor of Pseudo capless LDOs
the effective total capacitance at load side shall match above requirement

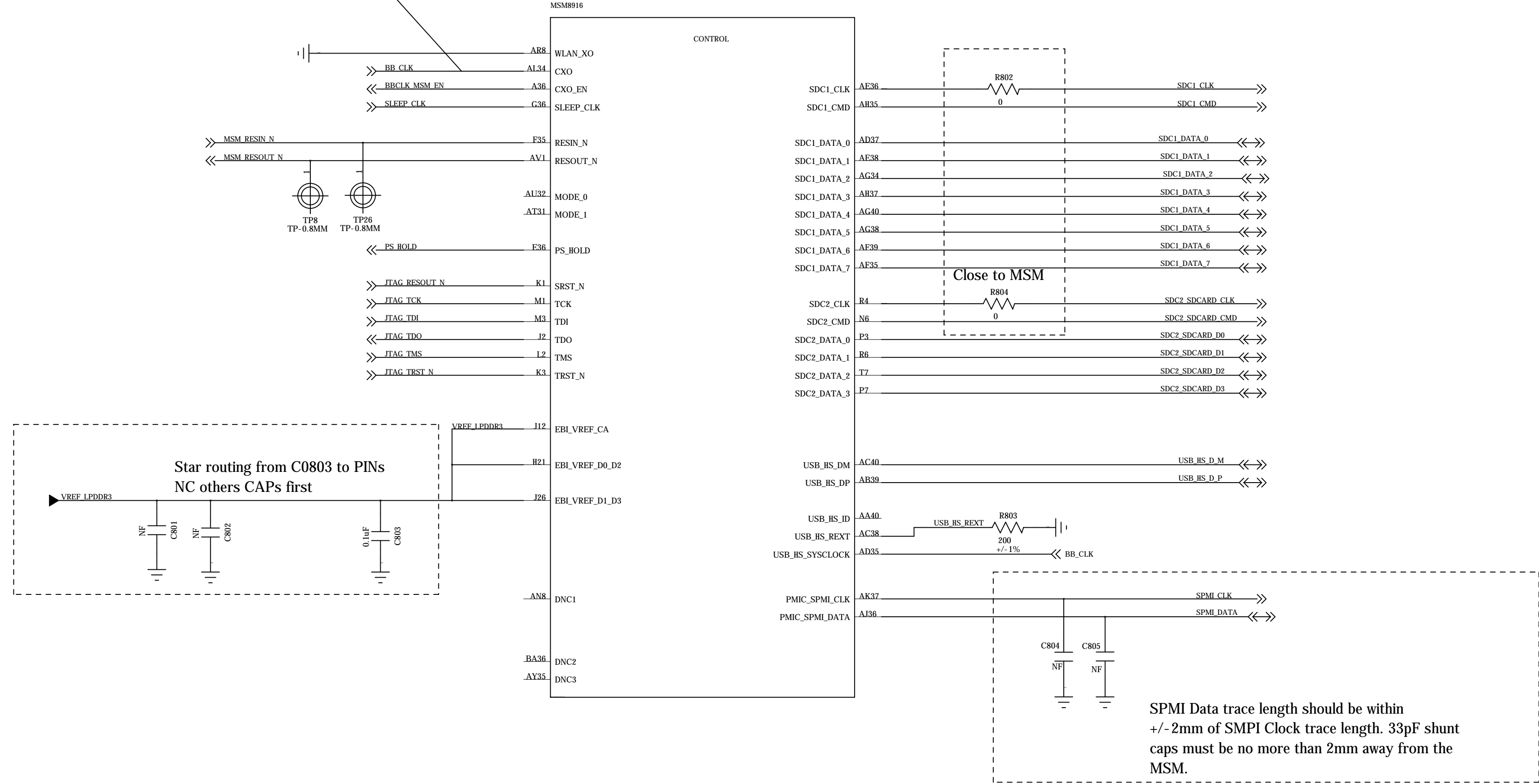
NC first

Notice:
Be careful of the height if LB0701/LB0703
replaced with beads (0.95mm)

Route codec PDM signals with matching length
on a inner layer shield between two GND layers.
Do not add any test point.



For BBCLK1,AD35 will be the first load and AL34 will be the final load
Should be a daisy chain route

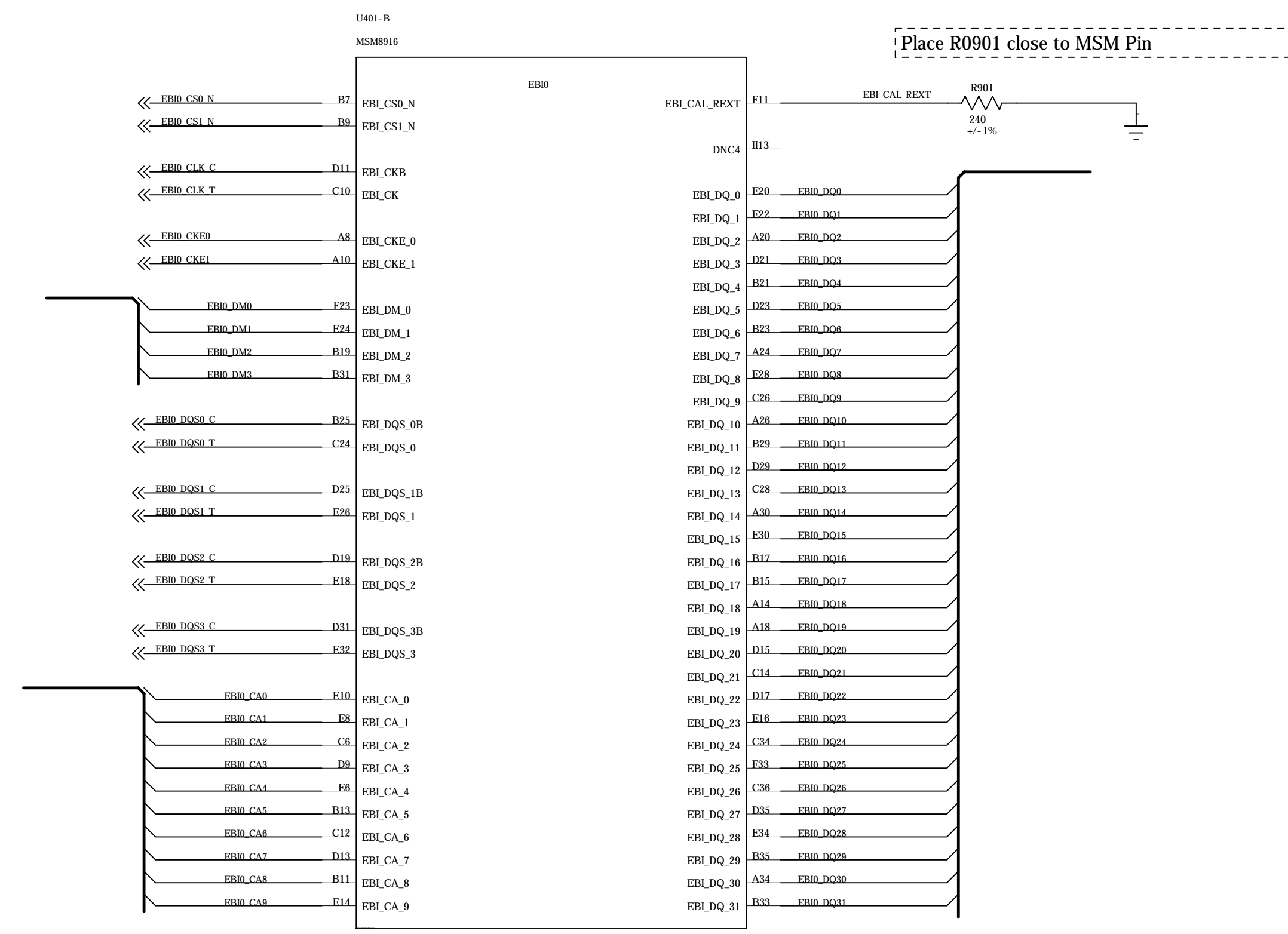


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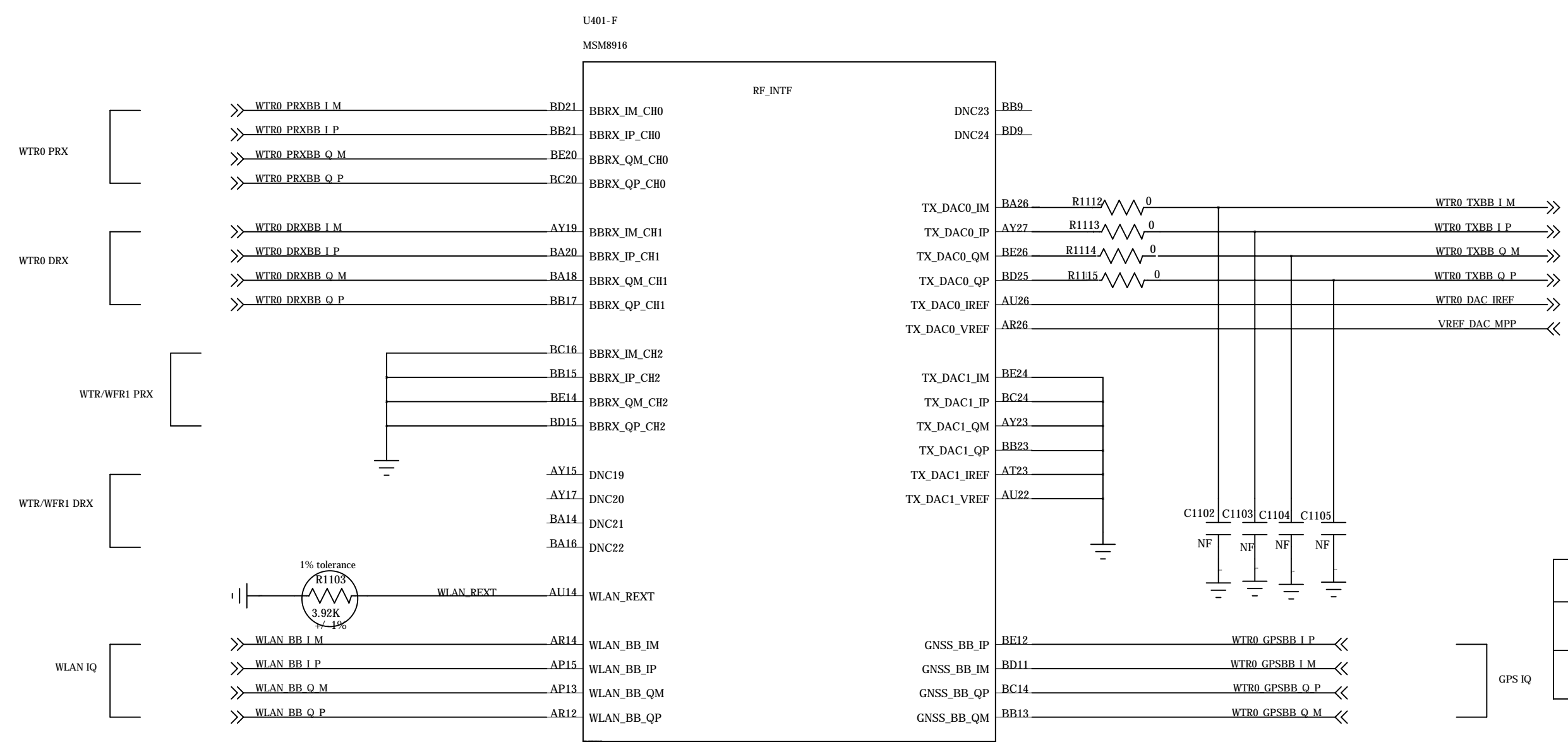
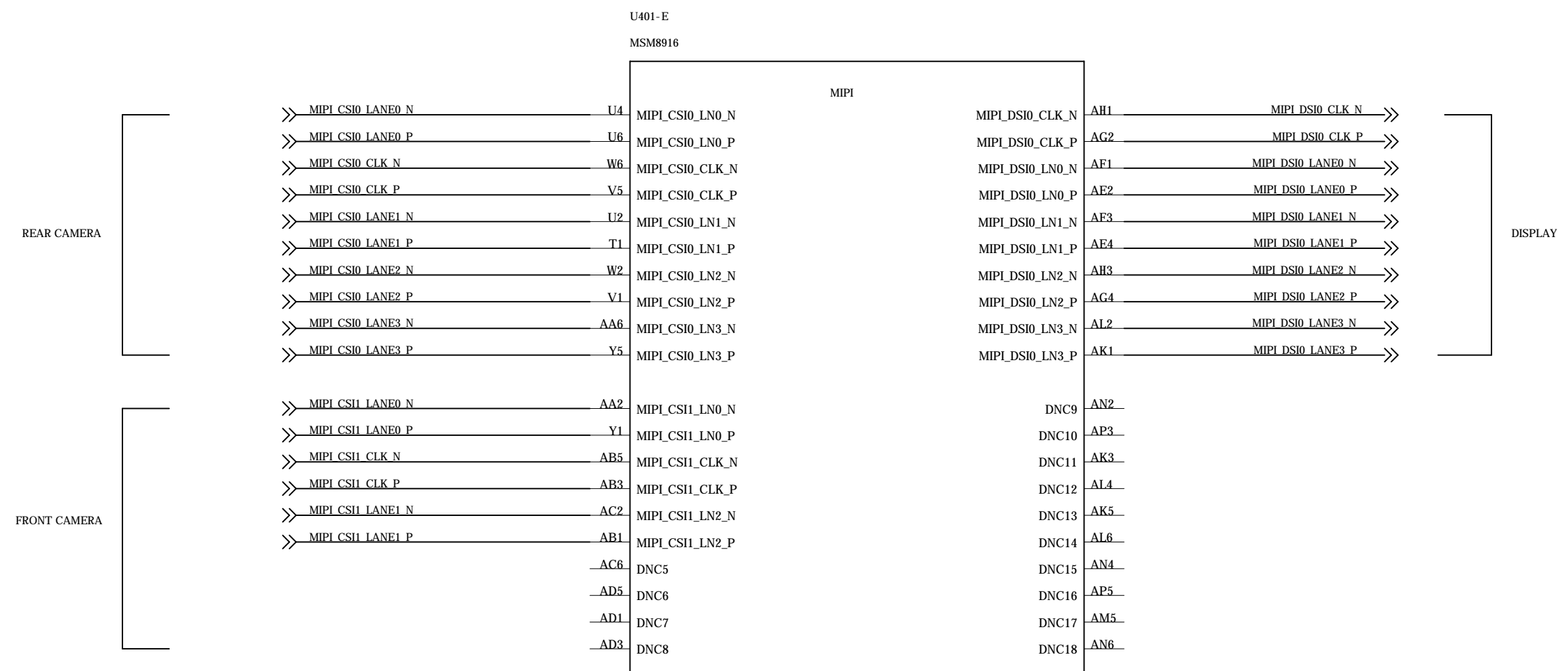
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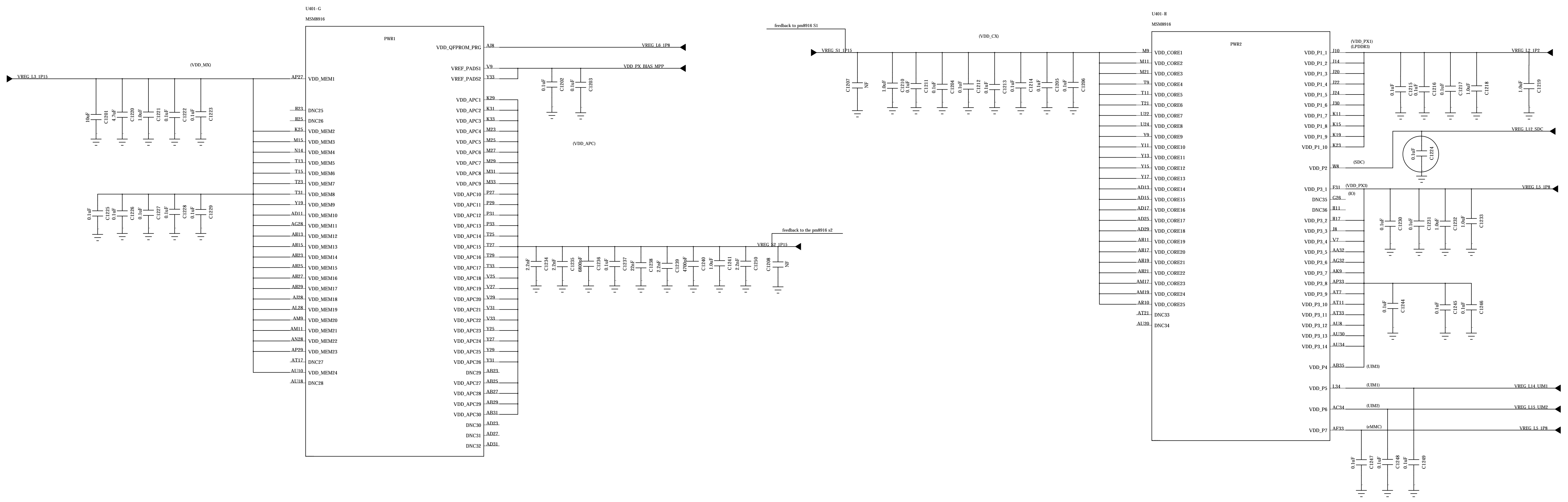
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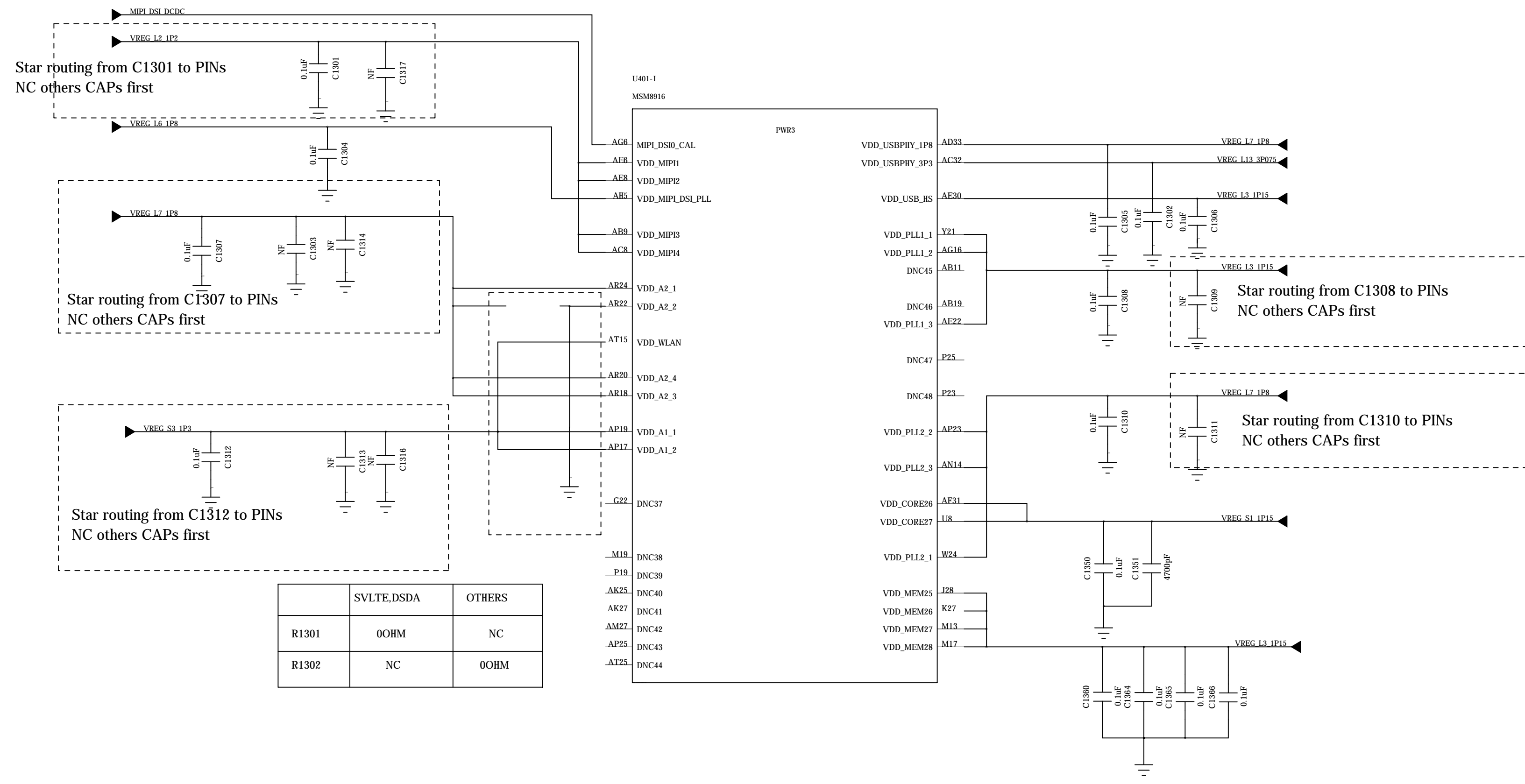
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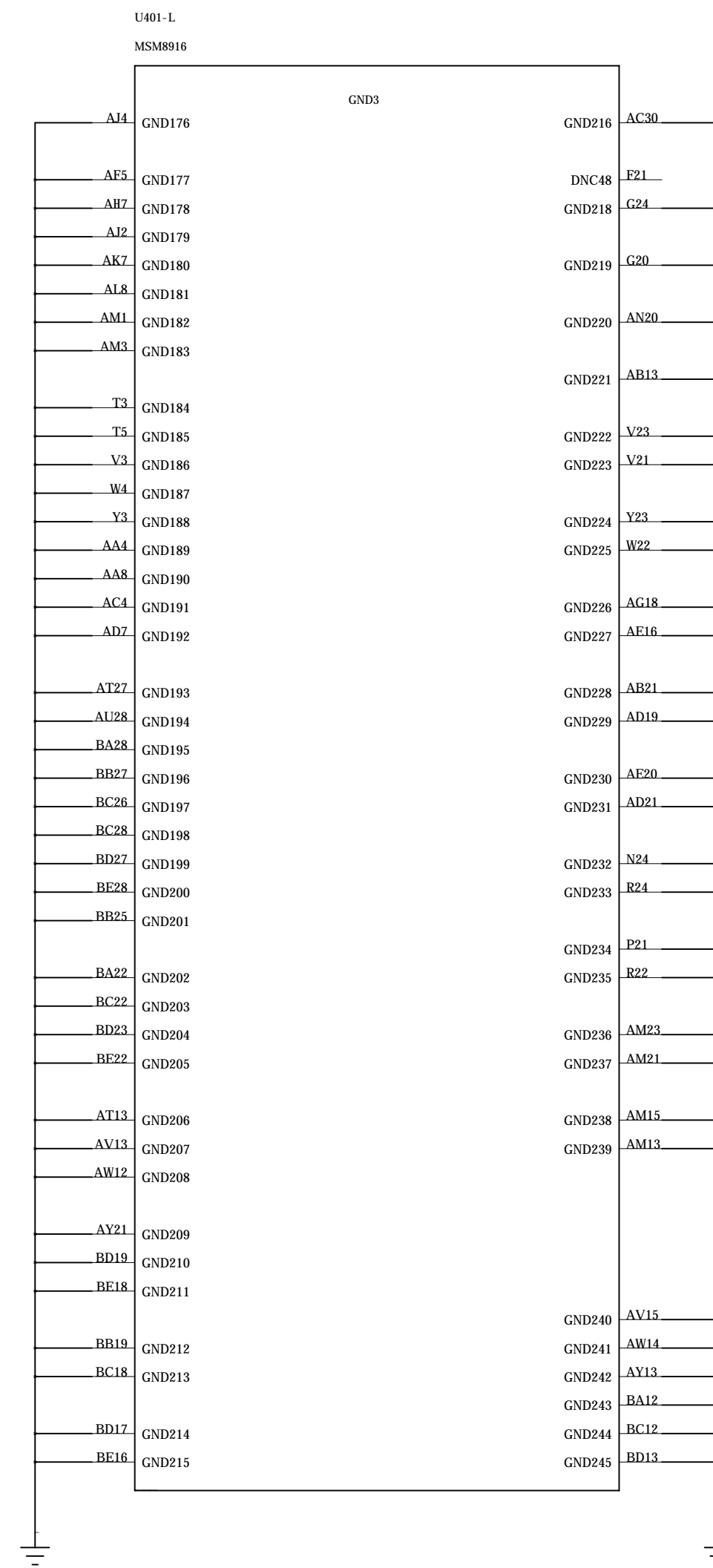
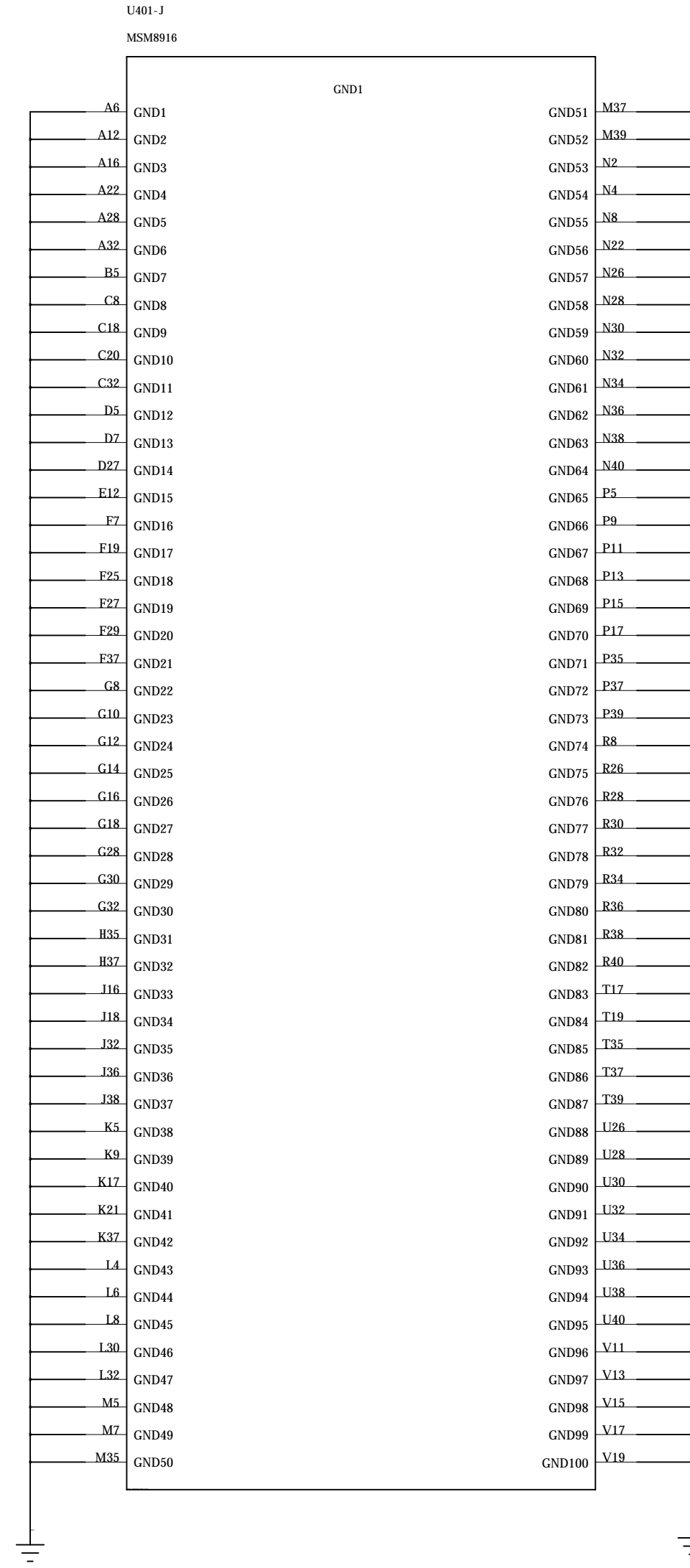
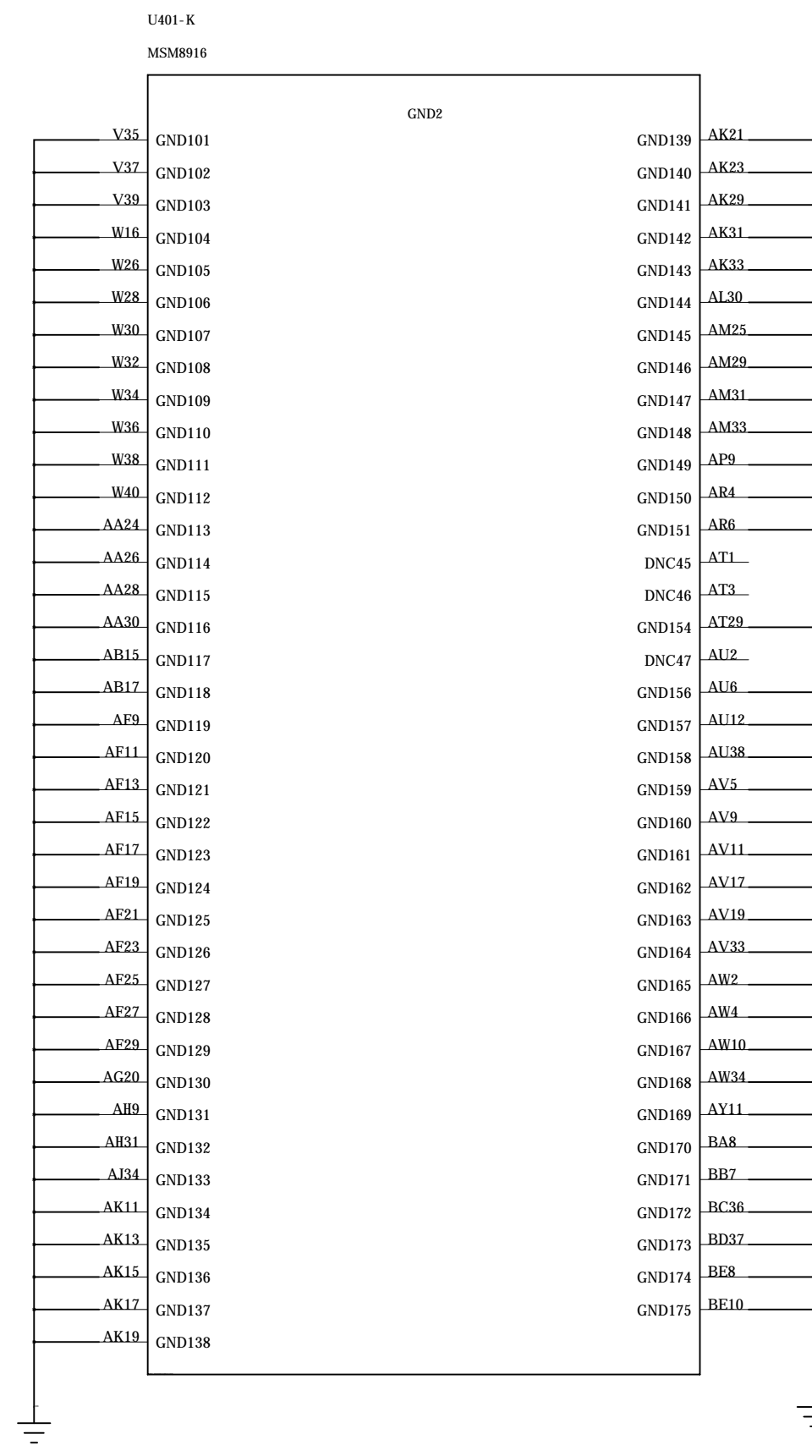


	SVLTE, DSDA	OTHERS
R1102	00HM	NC
R1120	NC	00HM

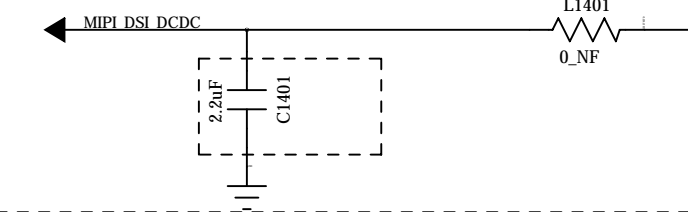
These NC resistor is for SVLTE or DSDA. Change them to 0ohm on other modes.



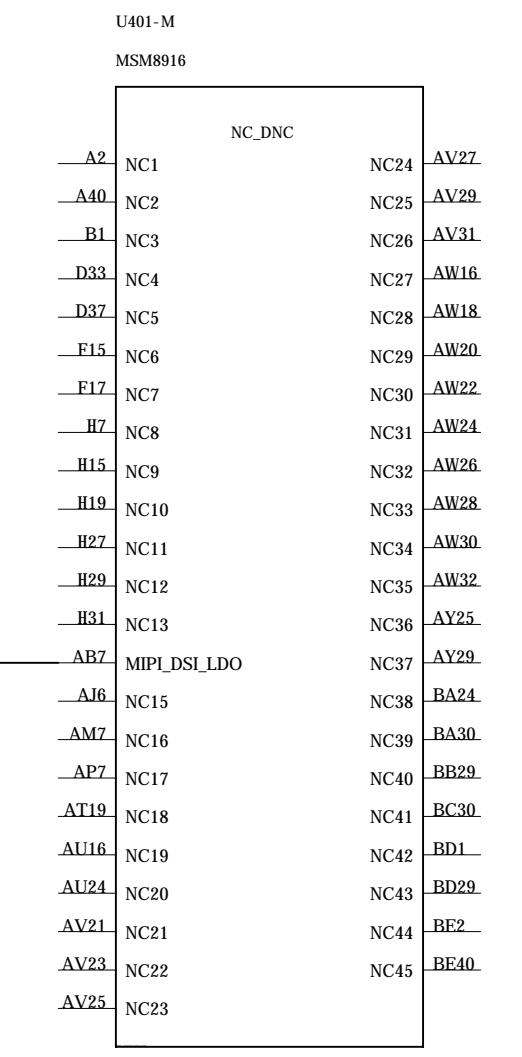




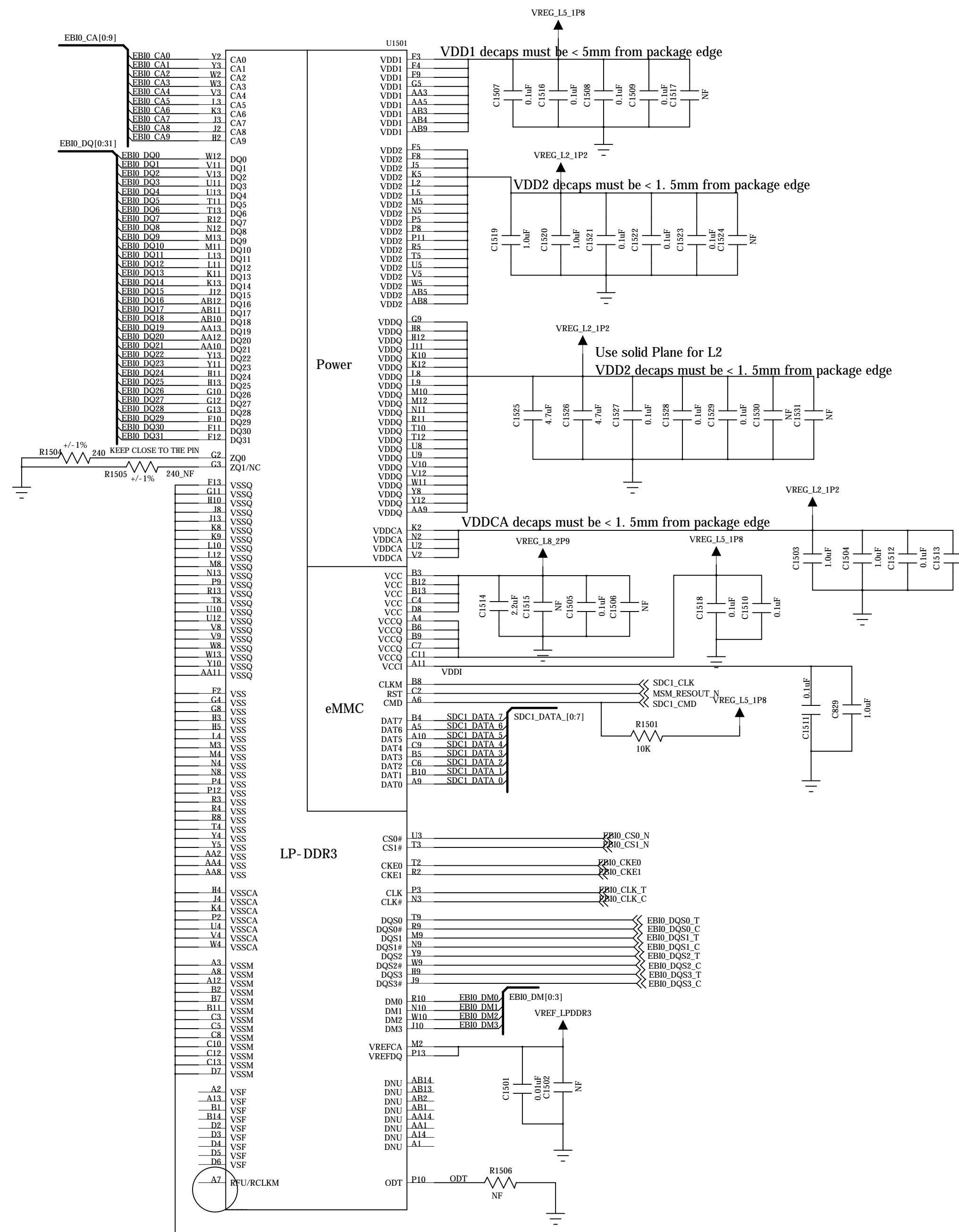
MIPL_DSL_LDO is configured to be LDO mode defaultly,
if using DC/DC mode,
L1401 need to be replaced with 2.2uH inductor



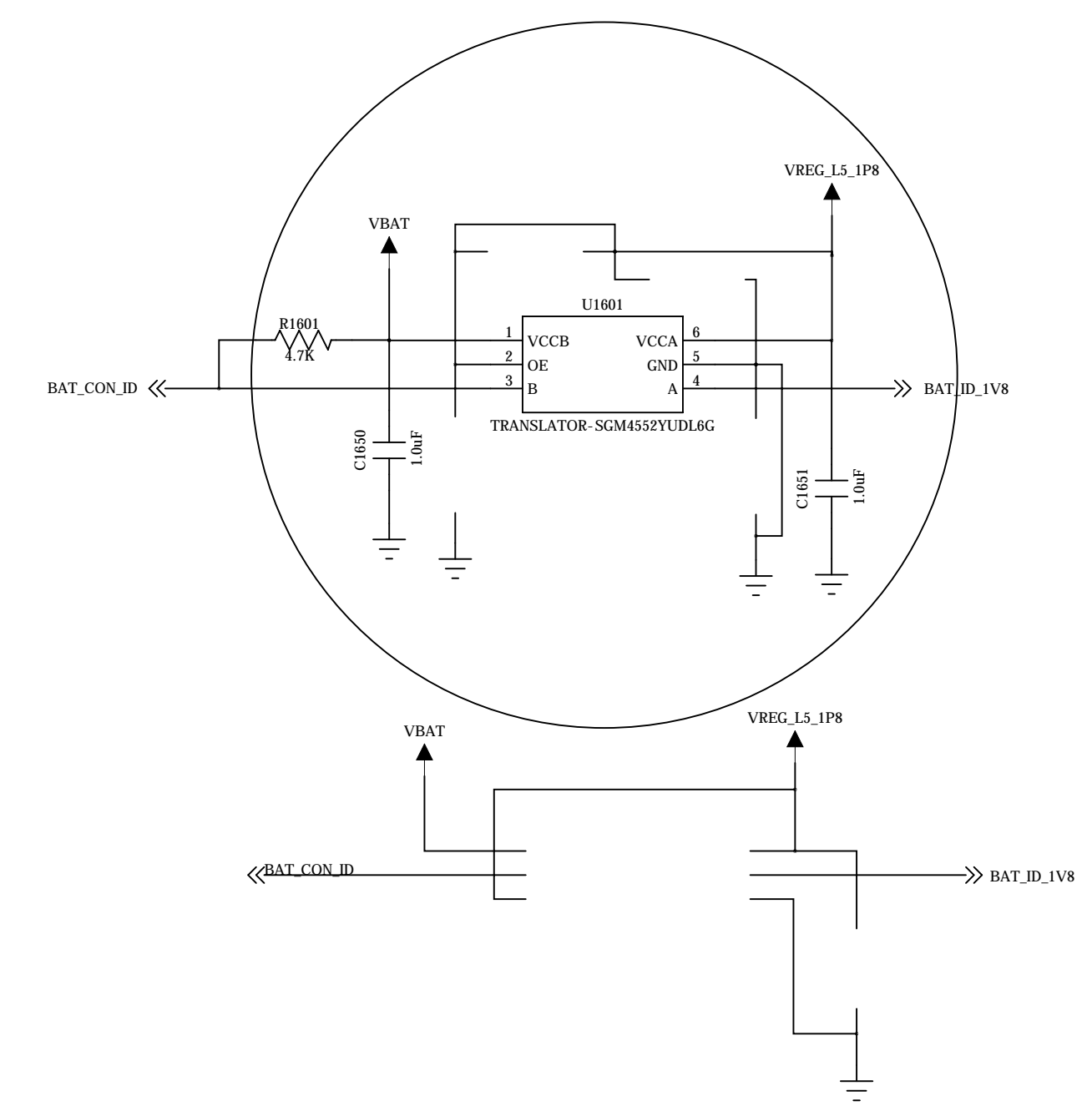
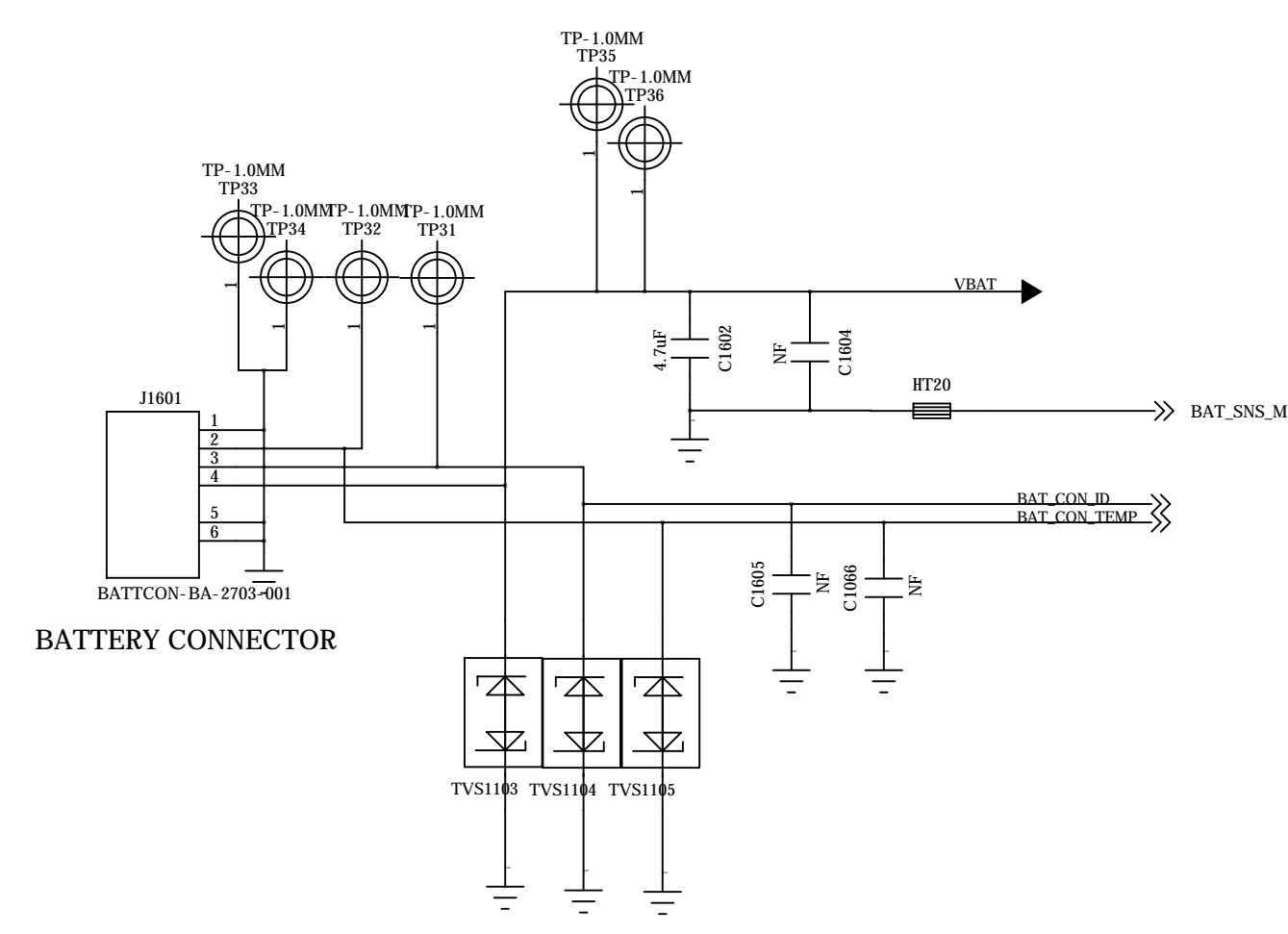
CAD NOTE: Loop to be shorted with MSM AB7 pin



	MICRO/HYNIX	SAMSUNG
U1501.G3	ZQ1	NC
U1501.A7	RFU	RCLK



Note: Pull-up resistors on SDC1_DATA are PCB and eMMC vender dependent

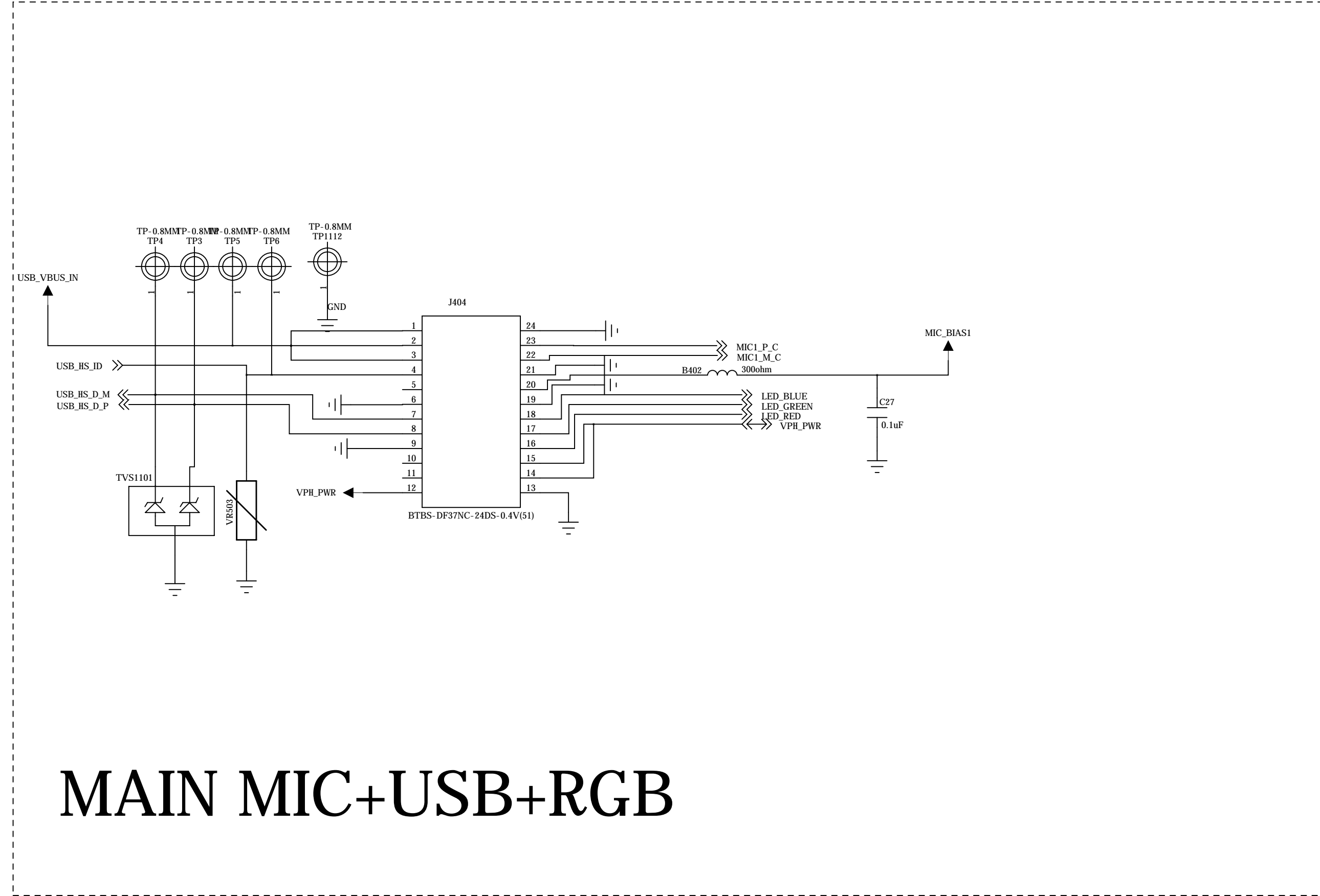


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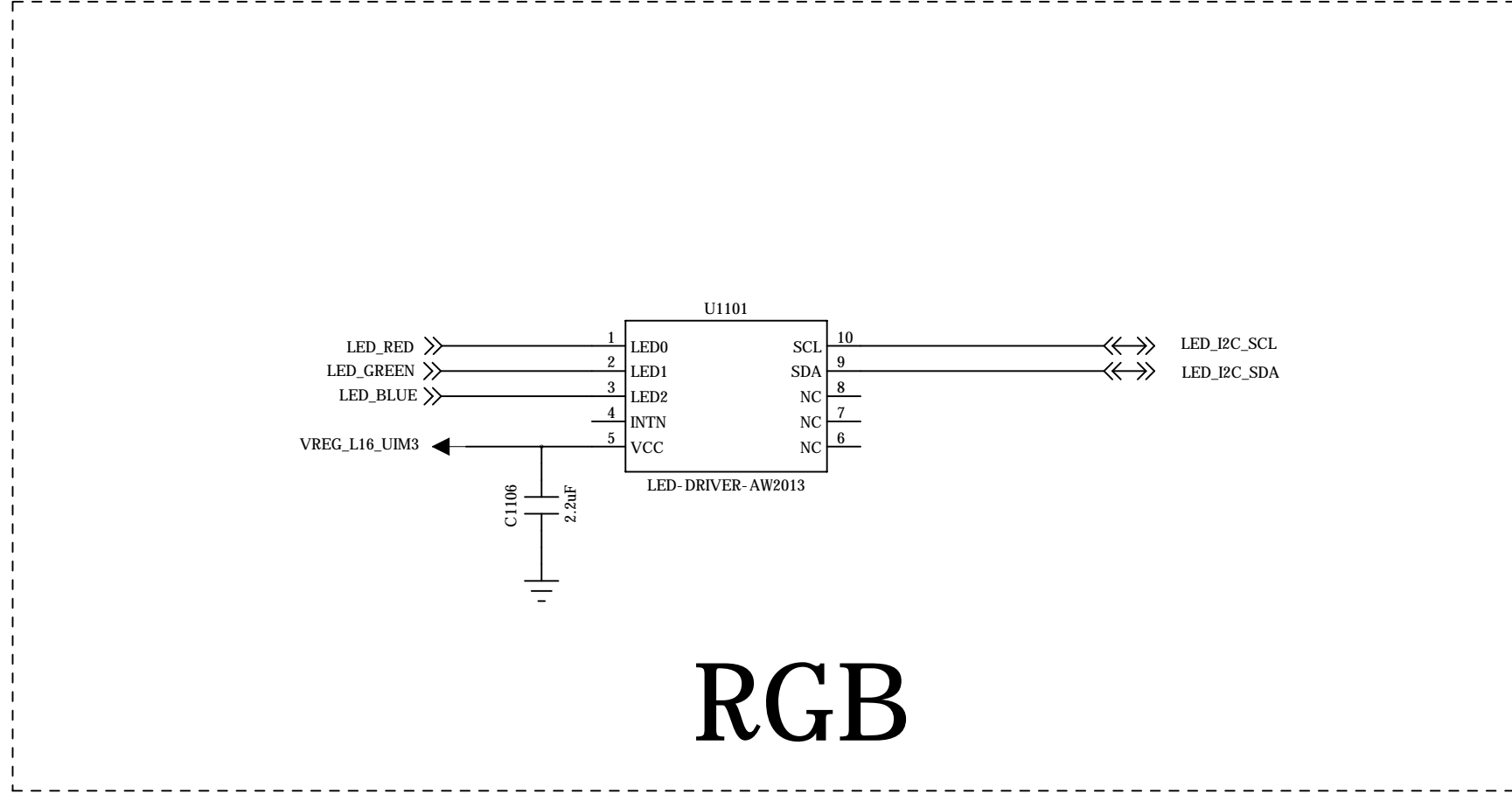
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Sub Board Connector



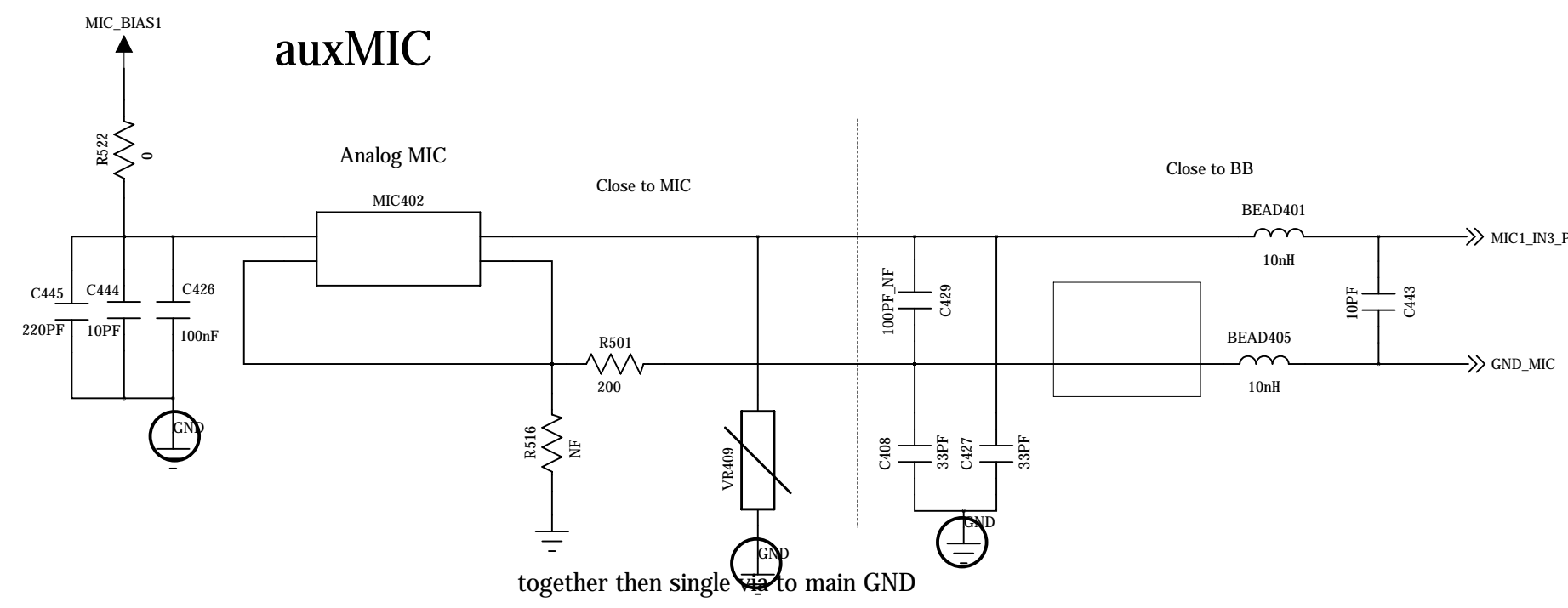
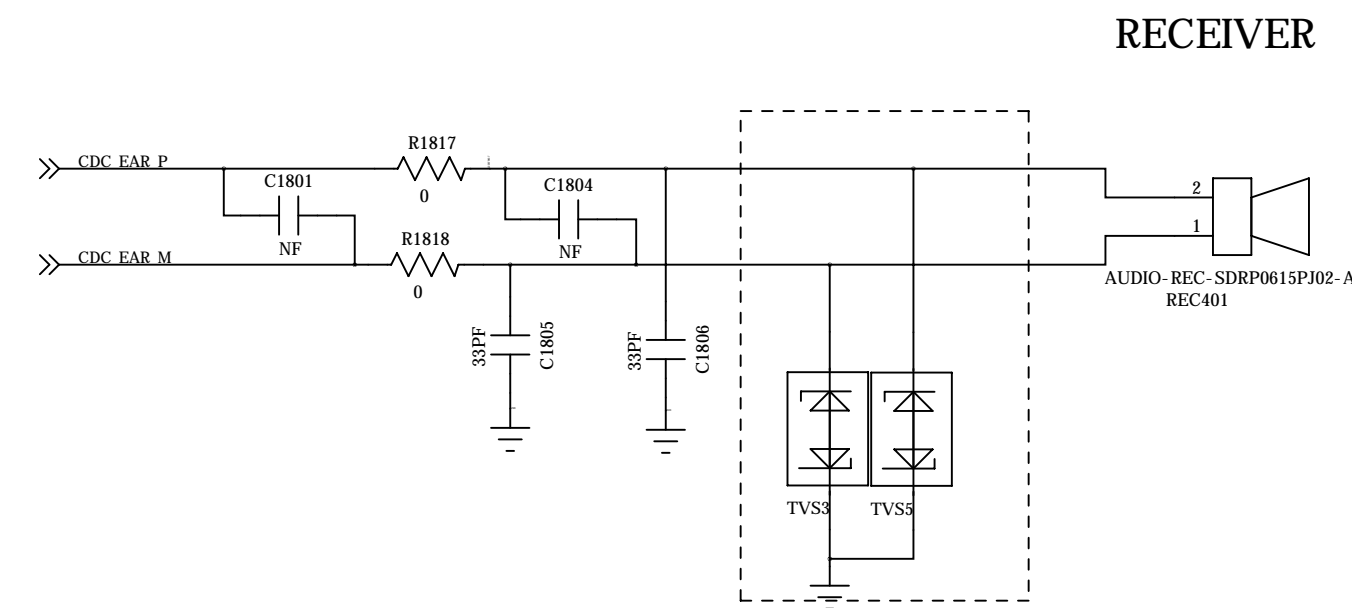
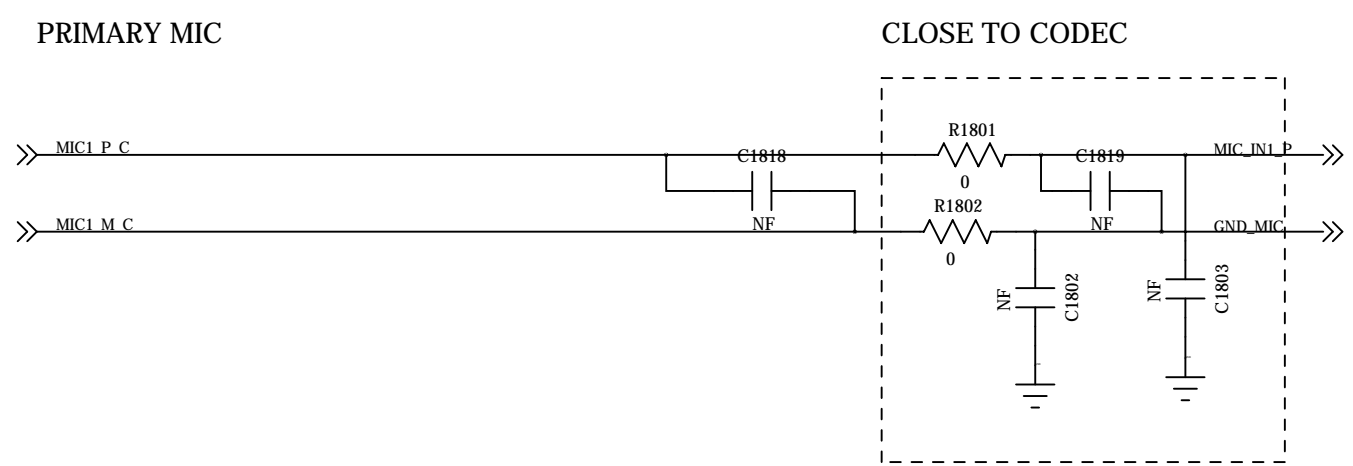
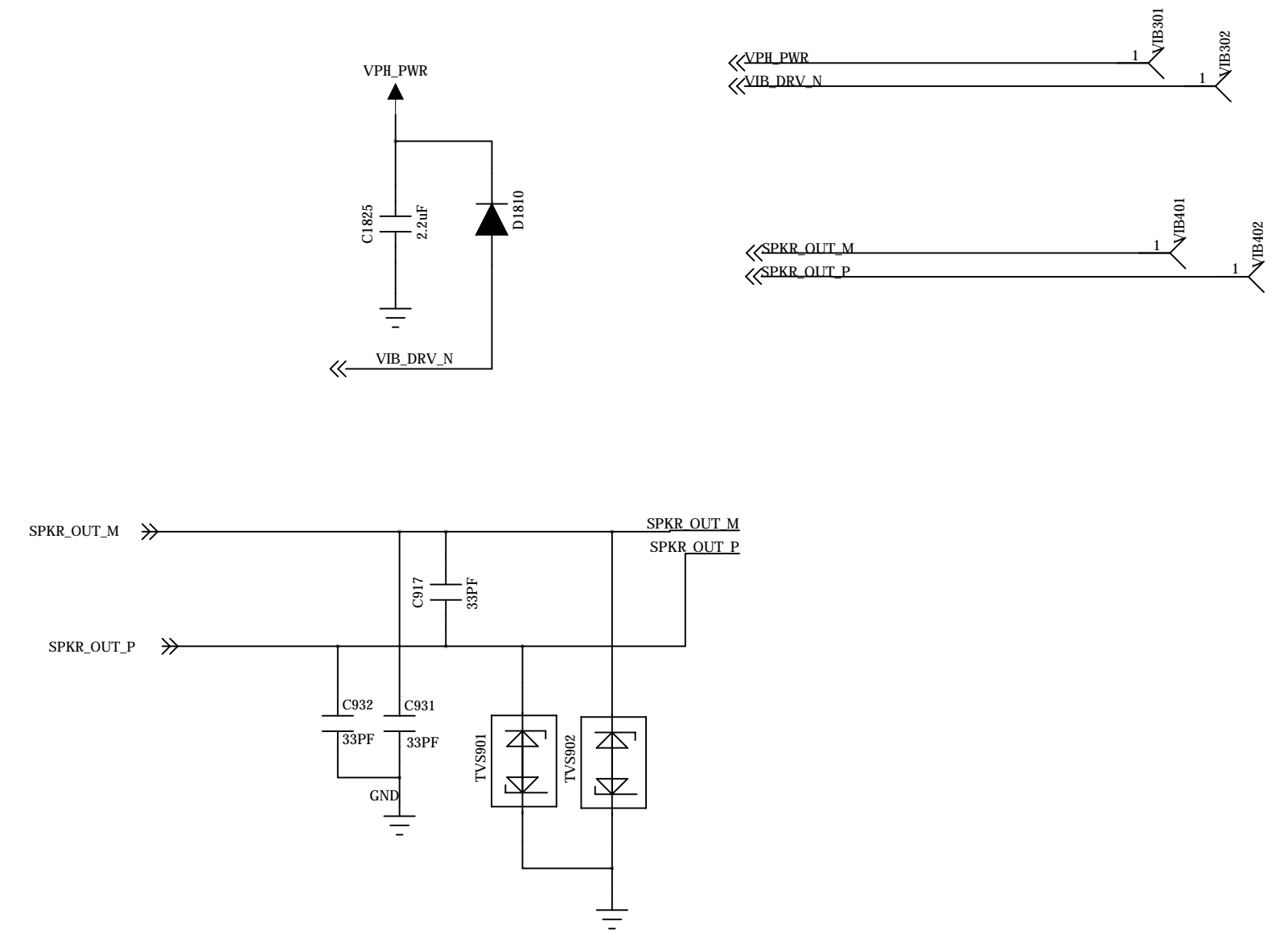
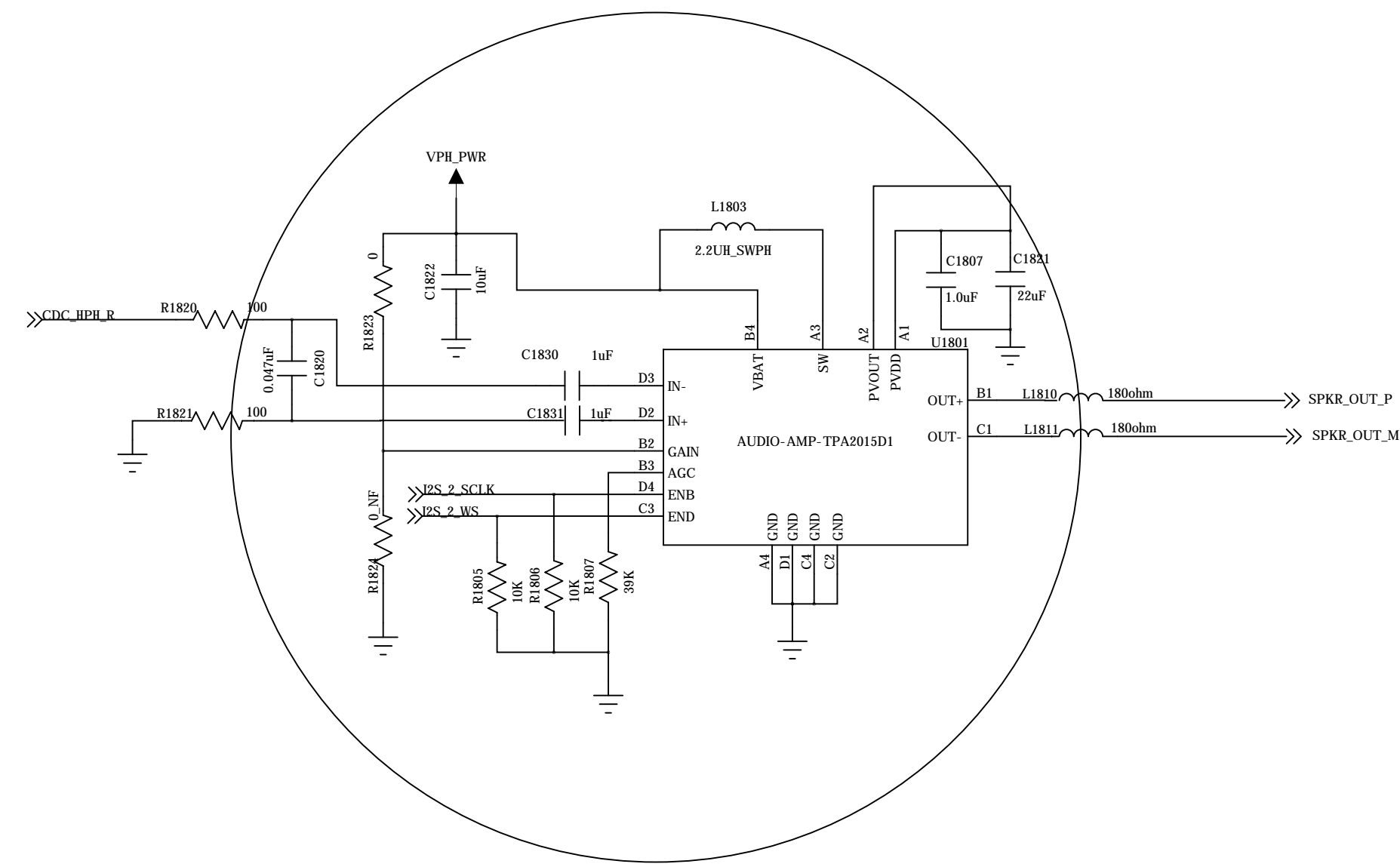
MAIN MIC+USB+RGB

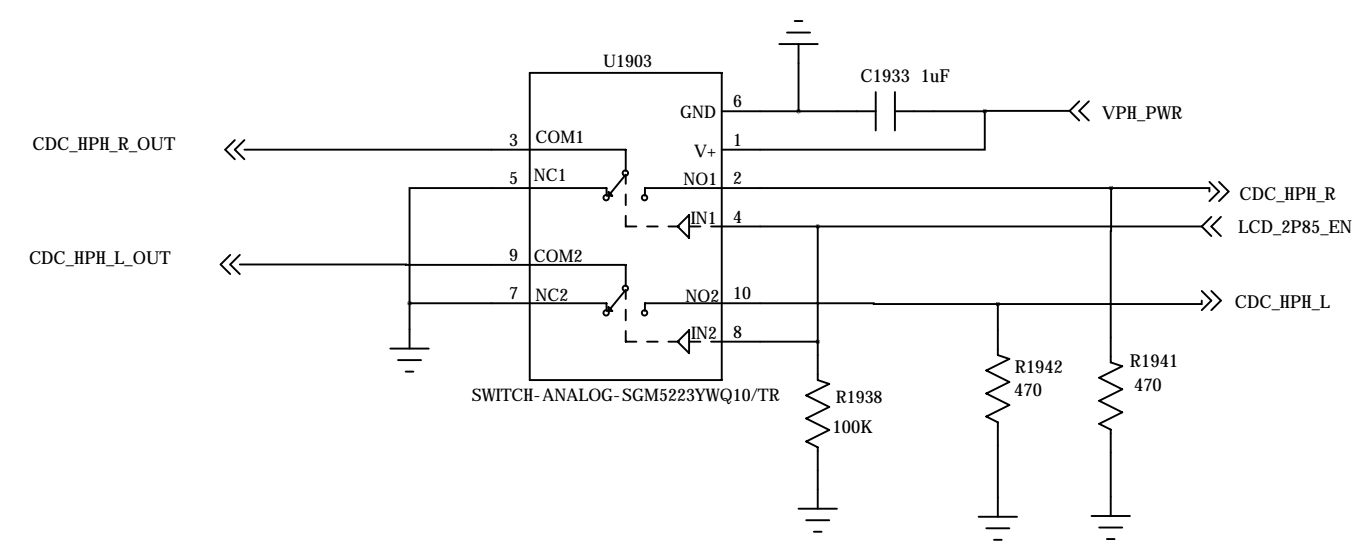
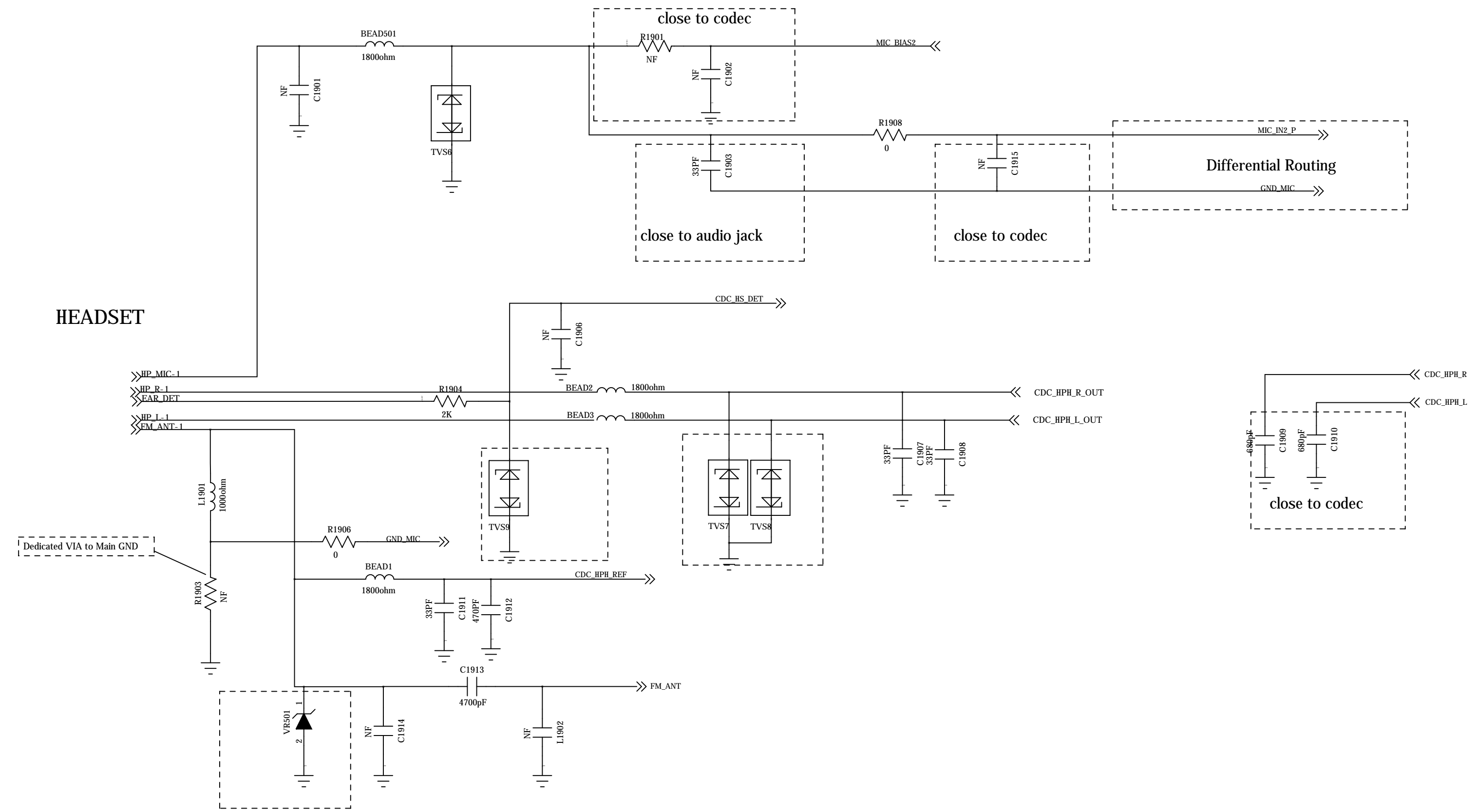
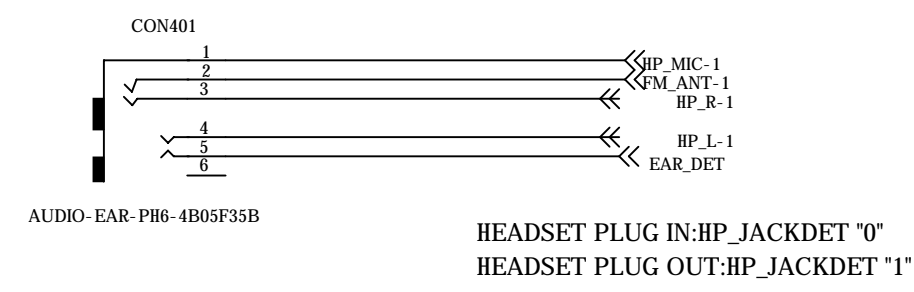


RGB

I2C Address	
Write	8B0x8A
Read	8B0x8B

Power on sequence: can be applied in any order





IN=0, COM Connected to NC;
IN=1, COM Connected to NO;

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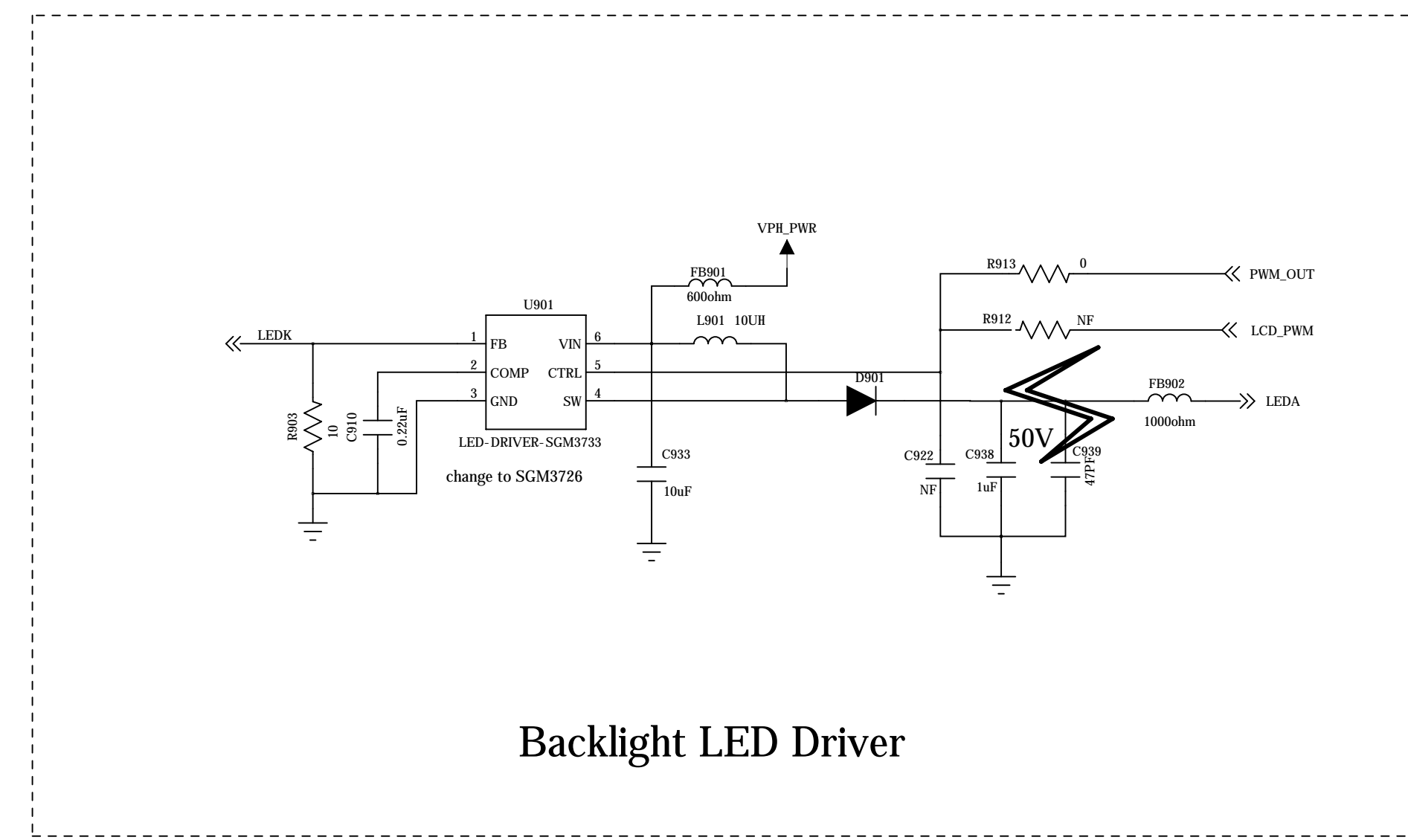
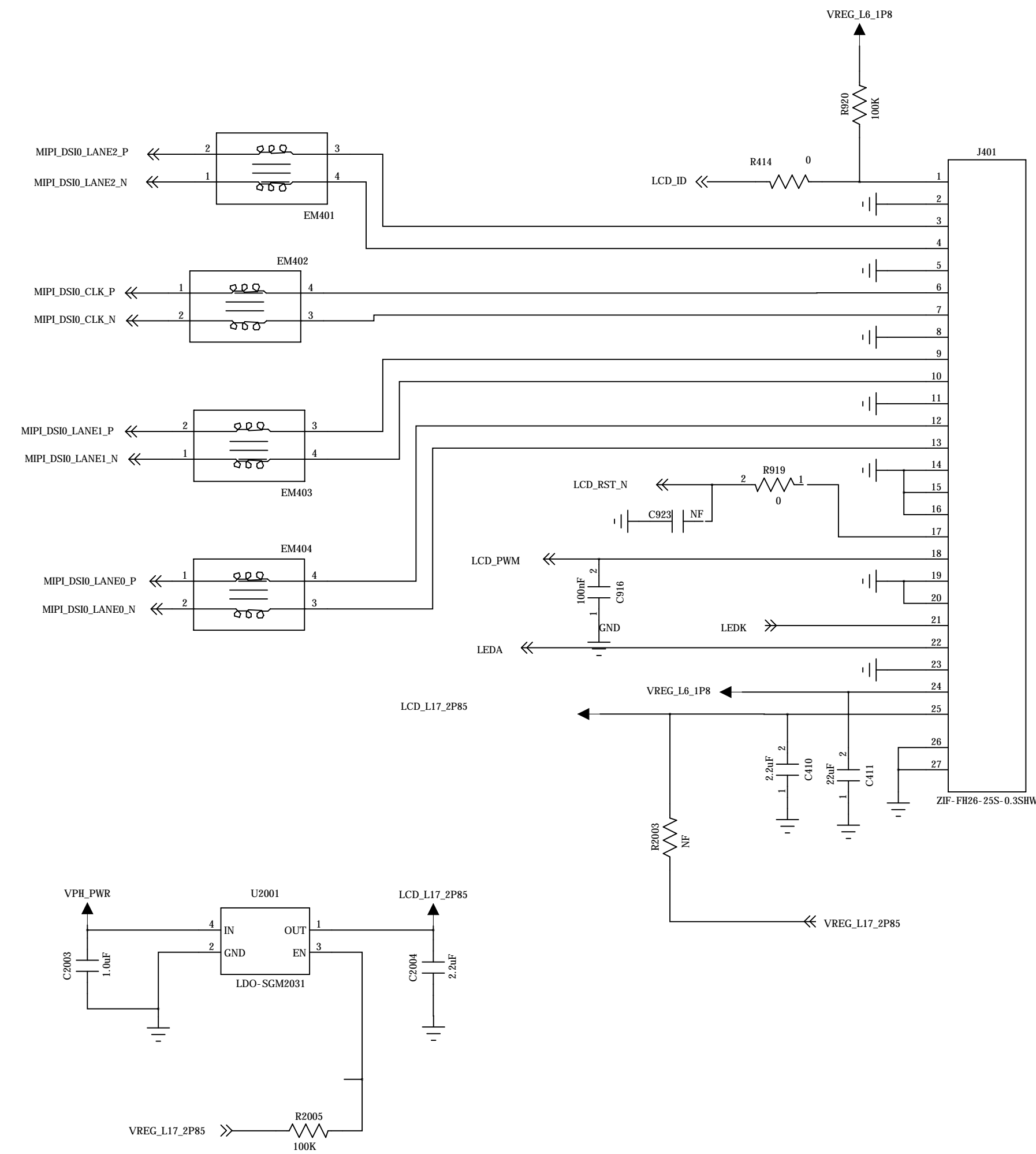
LCD

5.0' FWVGA

Module: KTF-050-D5498OF

Driver: OTM8019A

Power on sequence: L6(IOVCC)/L17(VCC) can be applied in any order



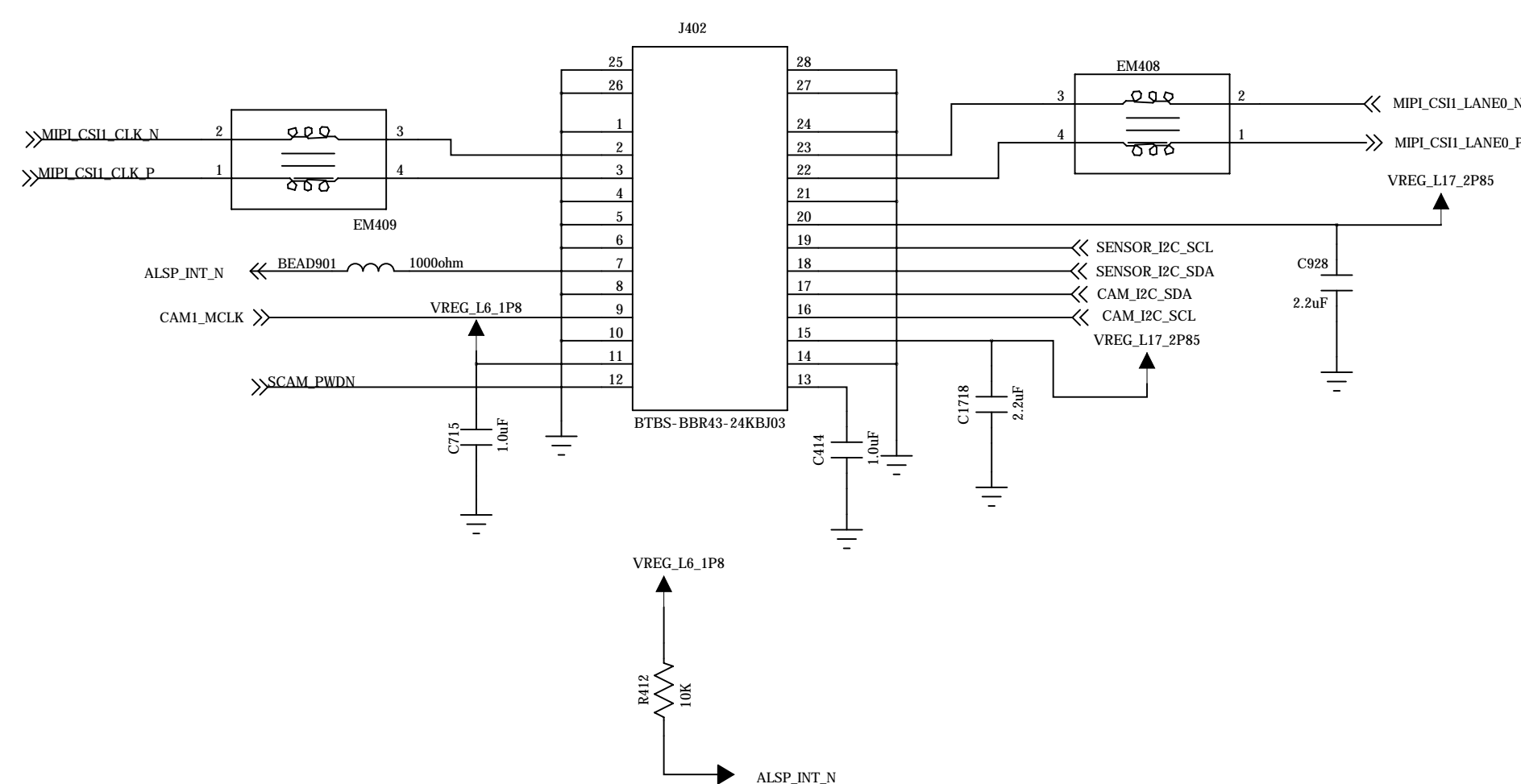
Backlight LED Driver

Sub Camera

FRONT_camera+LP Sensor+SPK

- J1
- DGND 1
- MCN 2
- MCP 3
- DGND 4
- DGND 5
- DGND 6
- ALS-INT 7
- DGND 8
- MCLK 9
- DGND 10
- DOVDD1.8 11
- XSHUTDOWN 12
- DVDD1.58 13
- AGND 14
- AVDD2.8 15
- SIO_C 16
- SIO_D 17
- ALS-SDA 18
- ALS-SCL 19
- ALS_VDD2.8 20
- DGND 21
- MDP0 22
- MDN0 23
- DGND 24

BBR13-24K6701

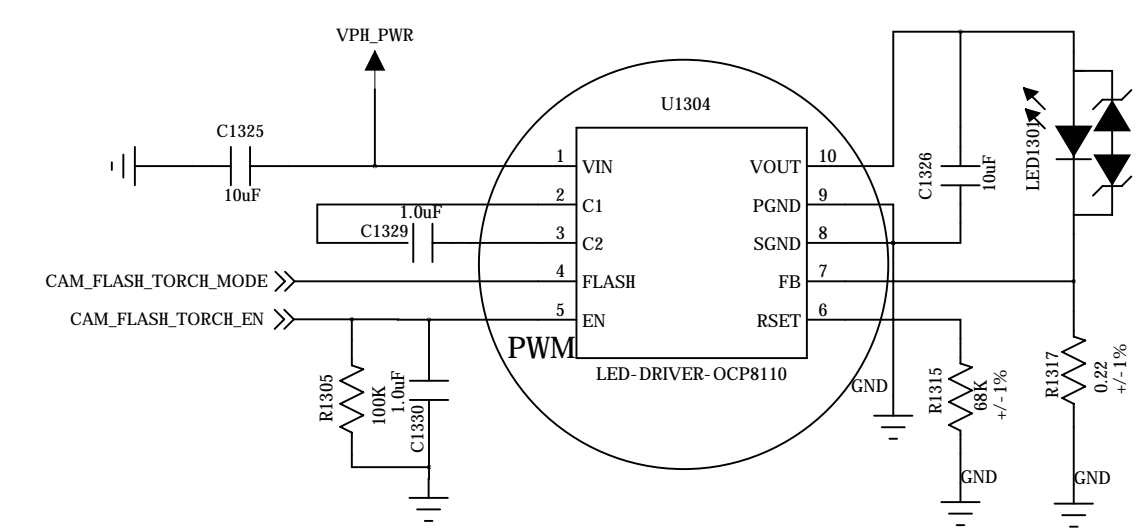
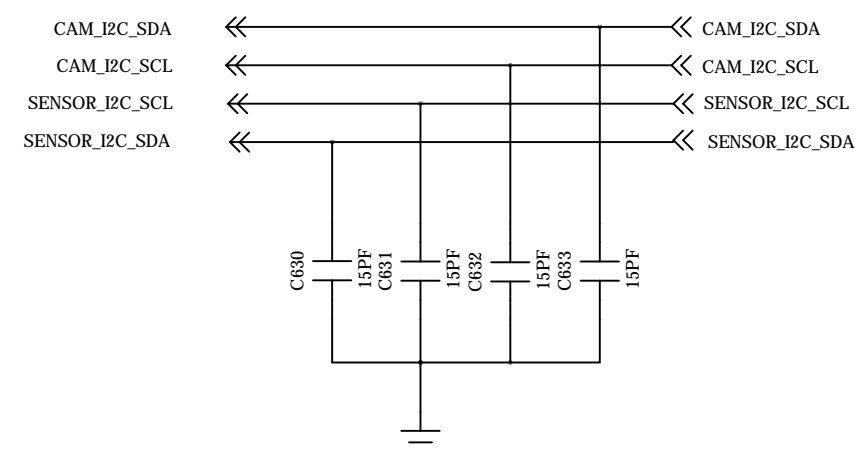


Main Camera / Sub Camera share power domain design should double check the voltage level is compatible

light sensor 0x90 Write(STK3171)
light sensor 0x46 Write (LTR-558ALS)

Front Camera: 2M	I2C Address	
Module:	Write	0x20
Sensor: OV2680	Read	0x21

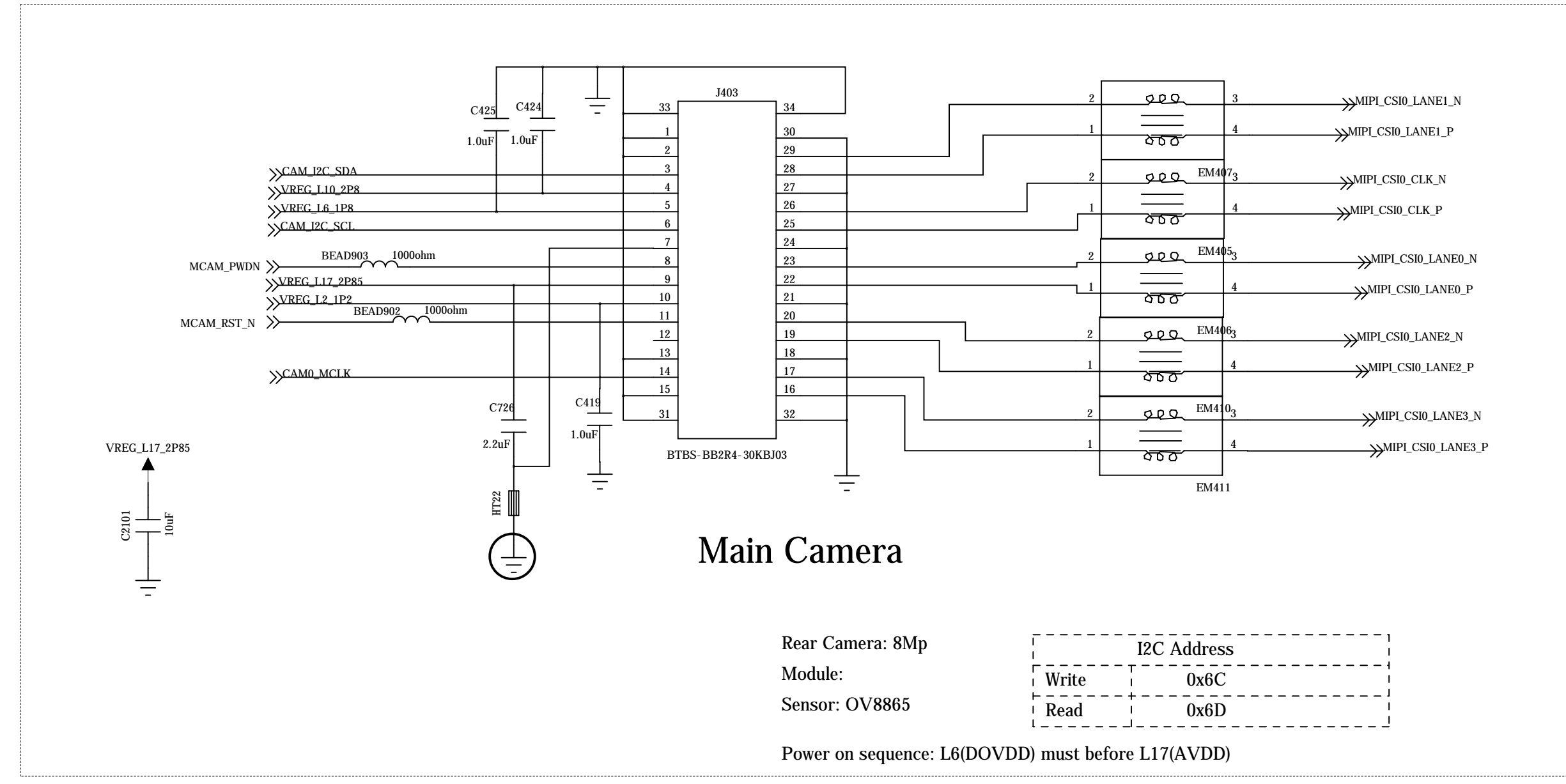
Power on sequence: L6(DOVDD)/L17(AVDD) can be applied in any order



OCP8111 R1315=68K R1317=0.22
OCP8110 R1315=82K R1317=0.47
FLASH LED

light sensor 0x90 Write(STK3171)
light sensor 0x46 Write (LTR-558ALS)

Main Camera / Sub Camera share power domain design should double check the voltage level is compatible



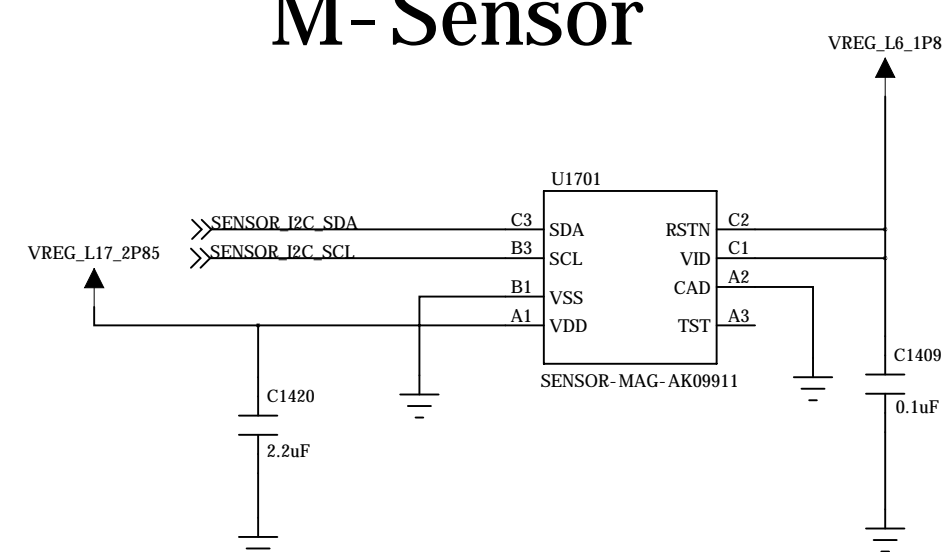
Main Camera

Rear Camera: 8Mp
Module:
Sensor: OV8865

I2C Address	
Write	0x6C
Read	0x6D

Power on sequence: L6(DOVDD) must before L17(AVDD)

M-Sensor



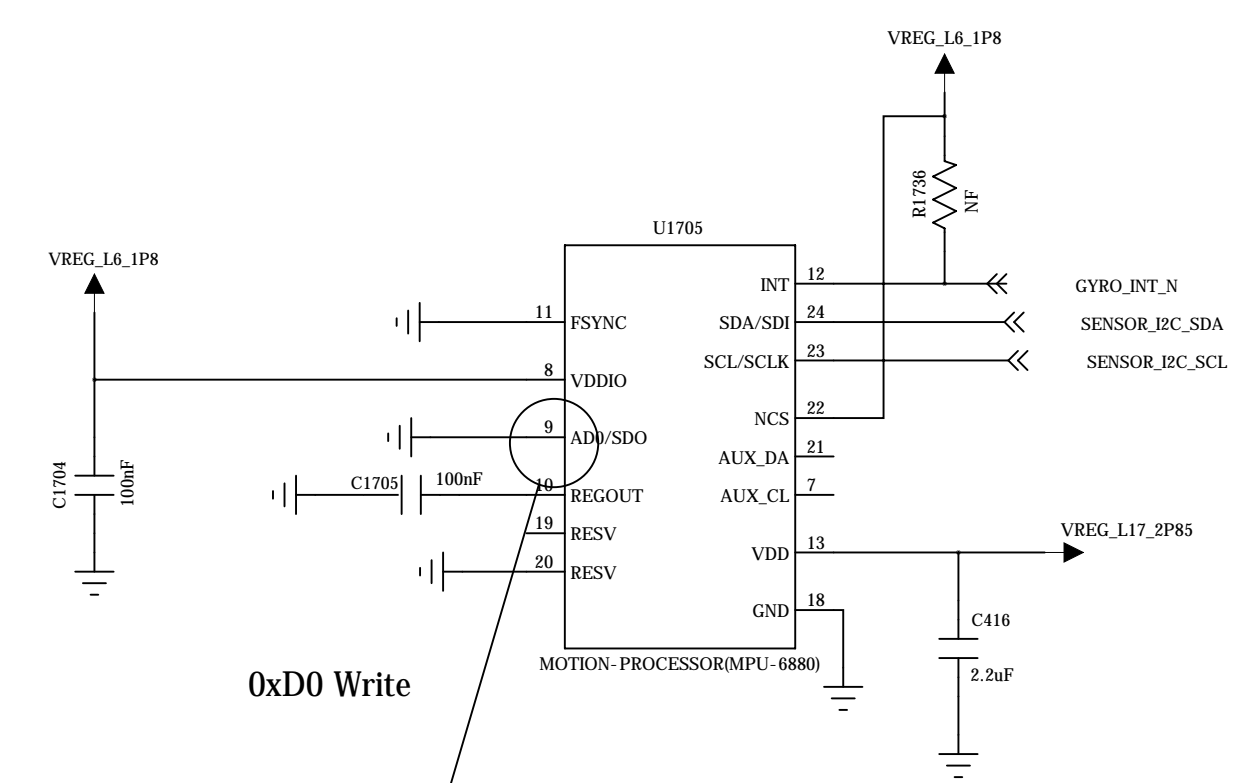
AKM: AK09911

I2C Address@CAD=0	
Write	0x18
Read	0x19

I2C Address@CAD=1	
Write	0x1A
Read	0x1B

Power on sequence: L6/L17 can be applied in any order

Gyroscope



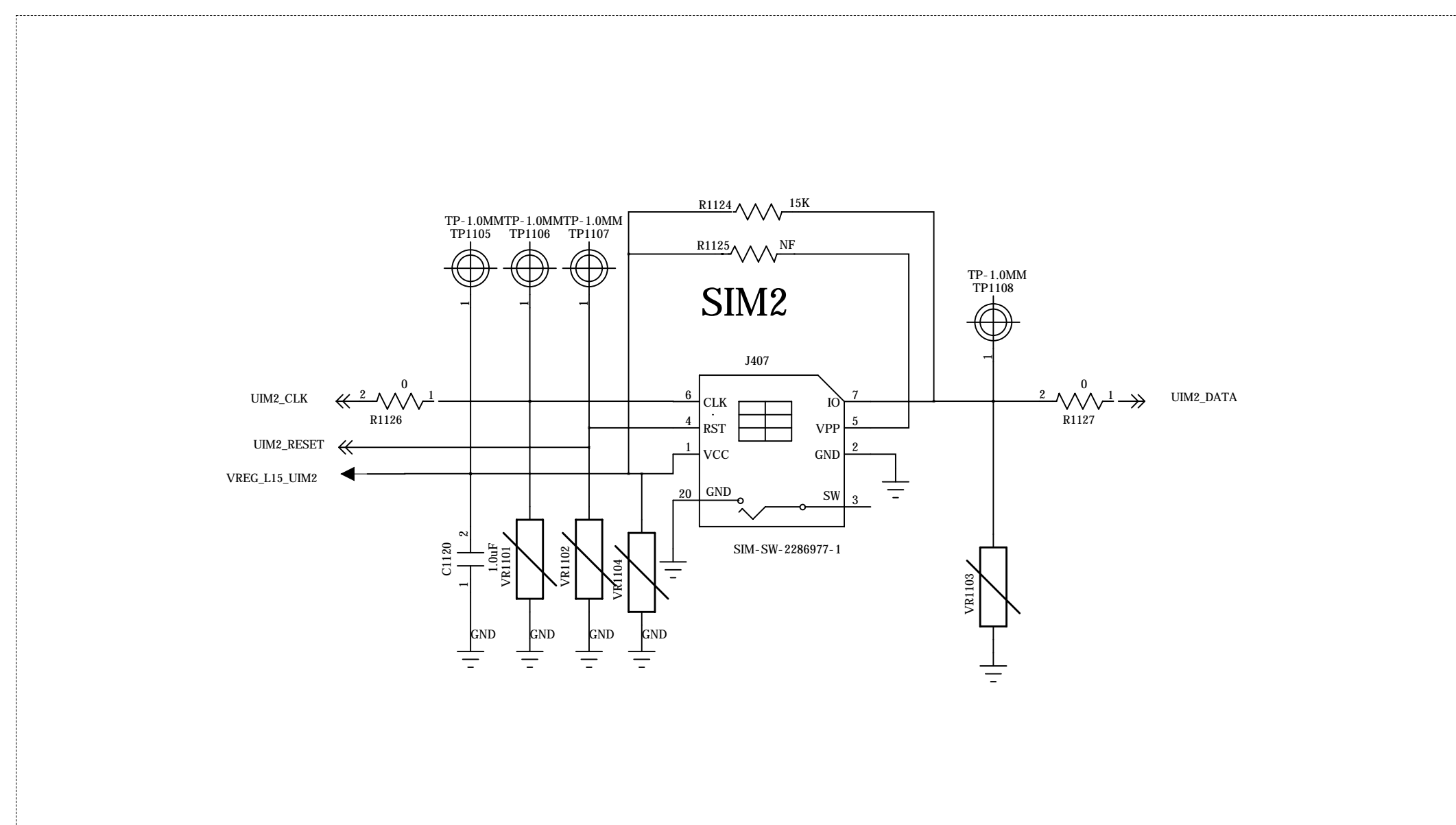
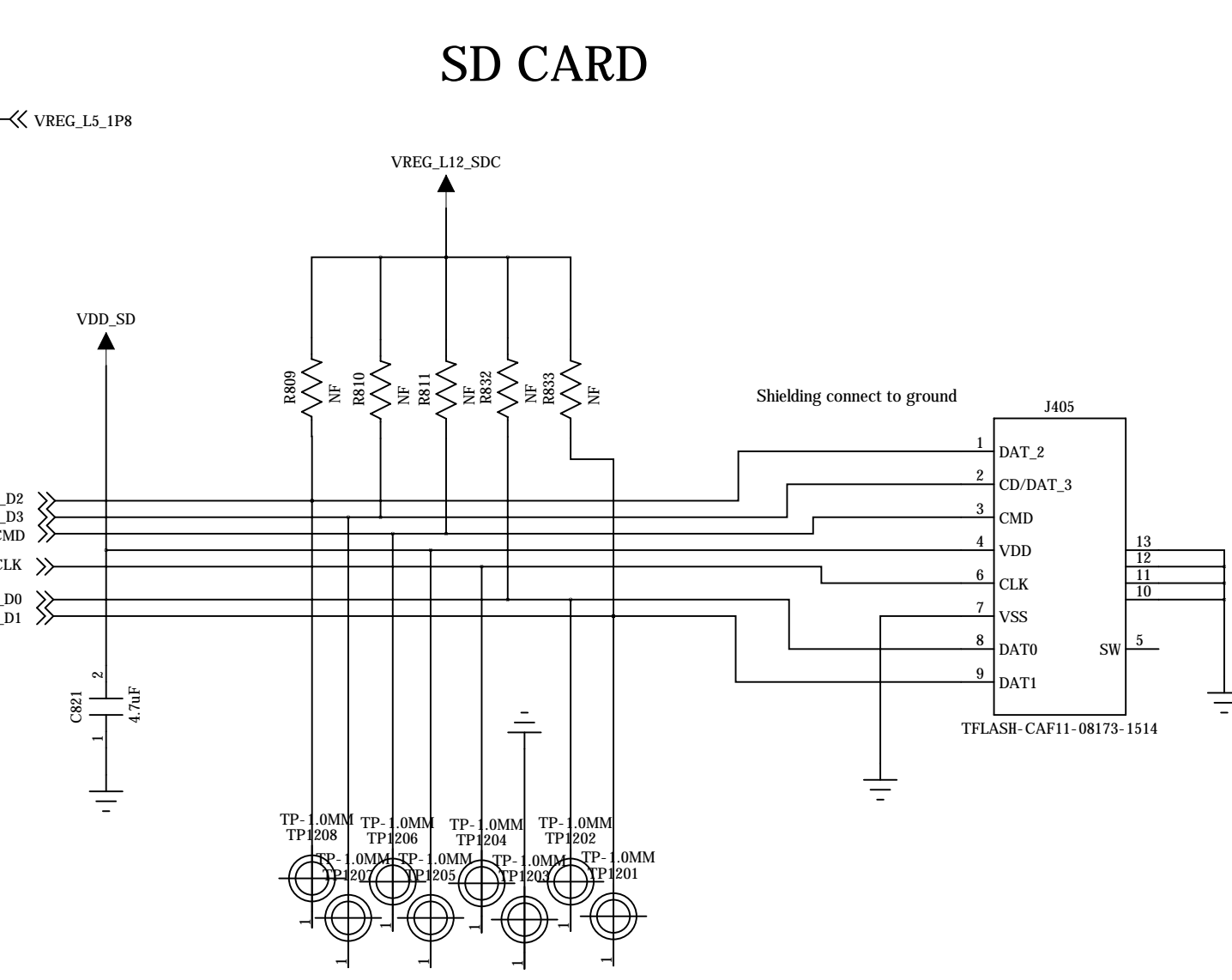
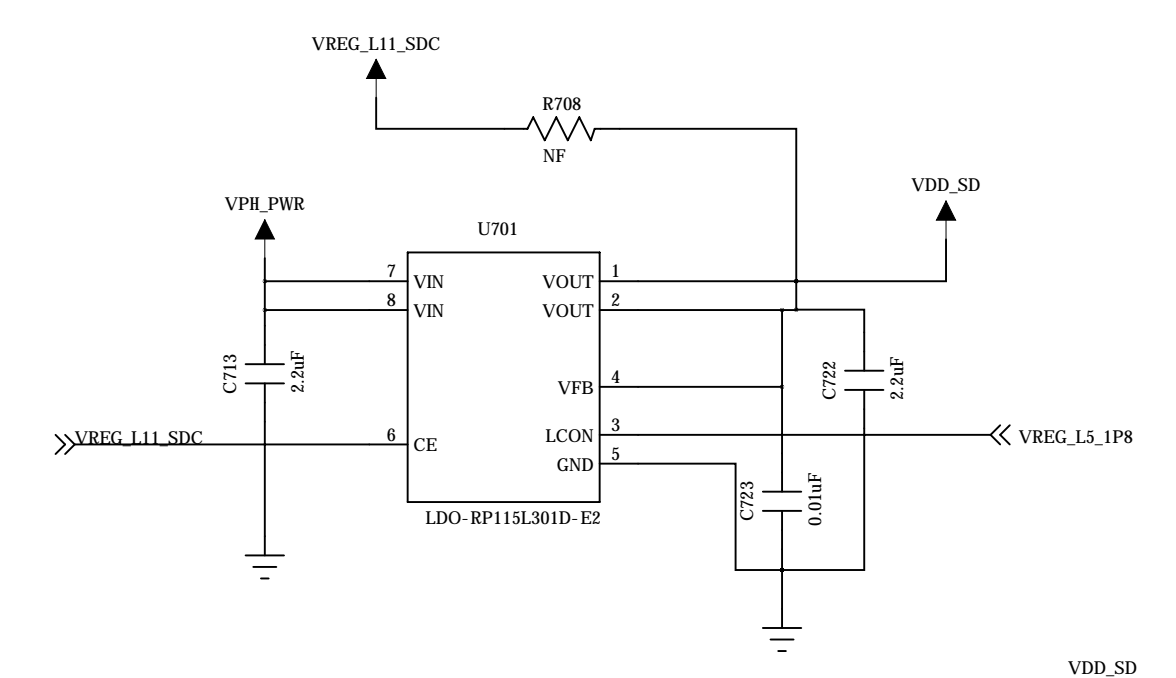
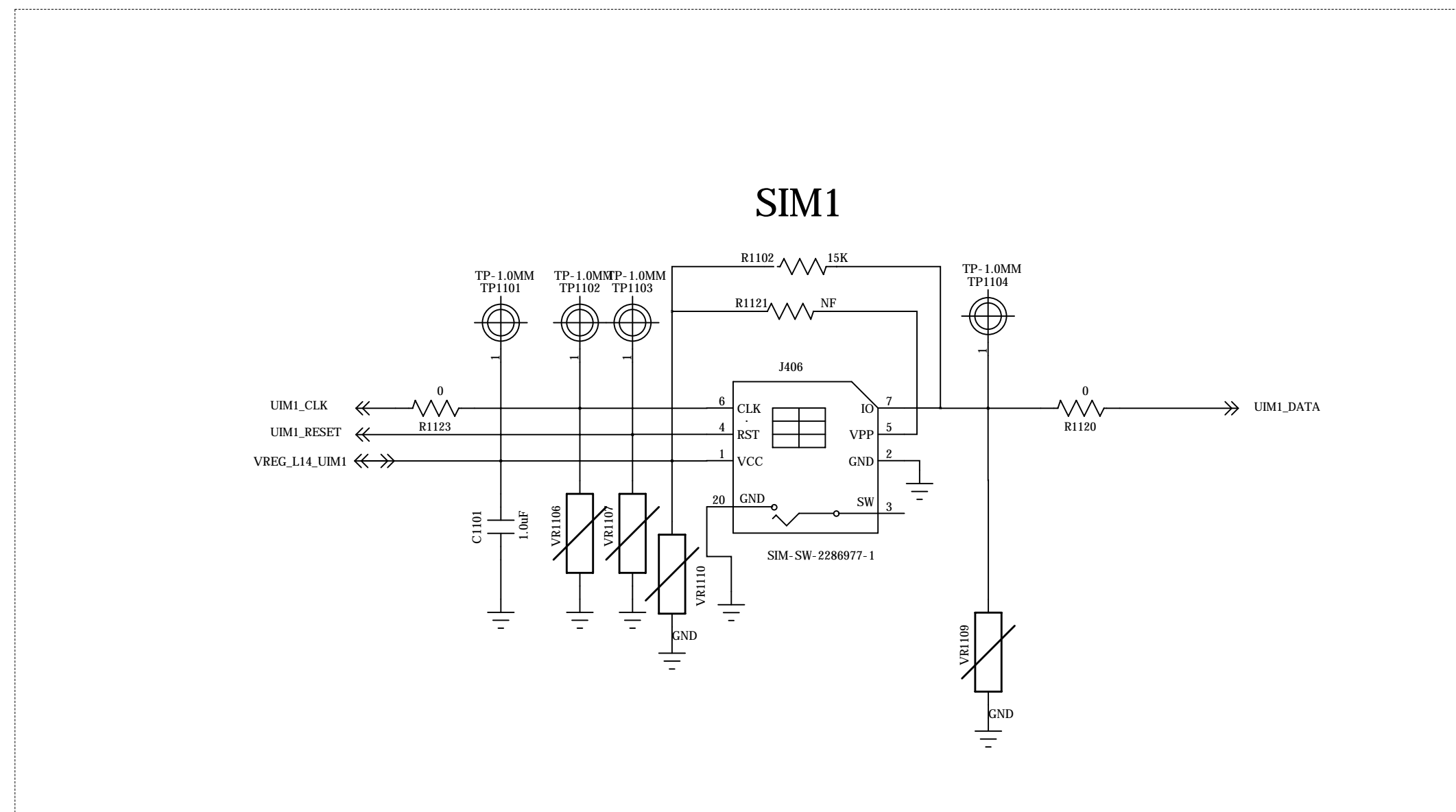
AD0=0 7b1101000 0x68
 AD0=1 7b1101001 0x69

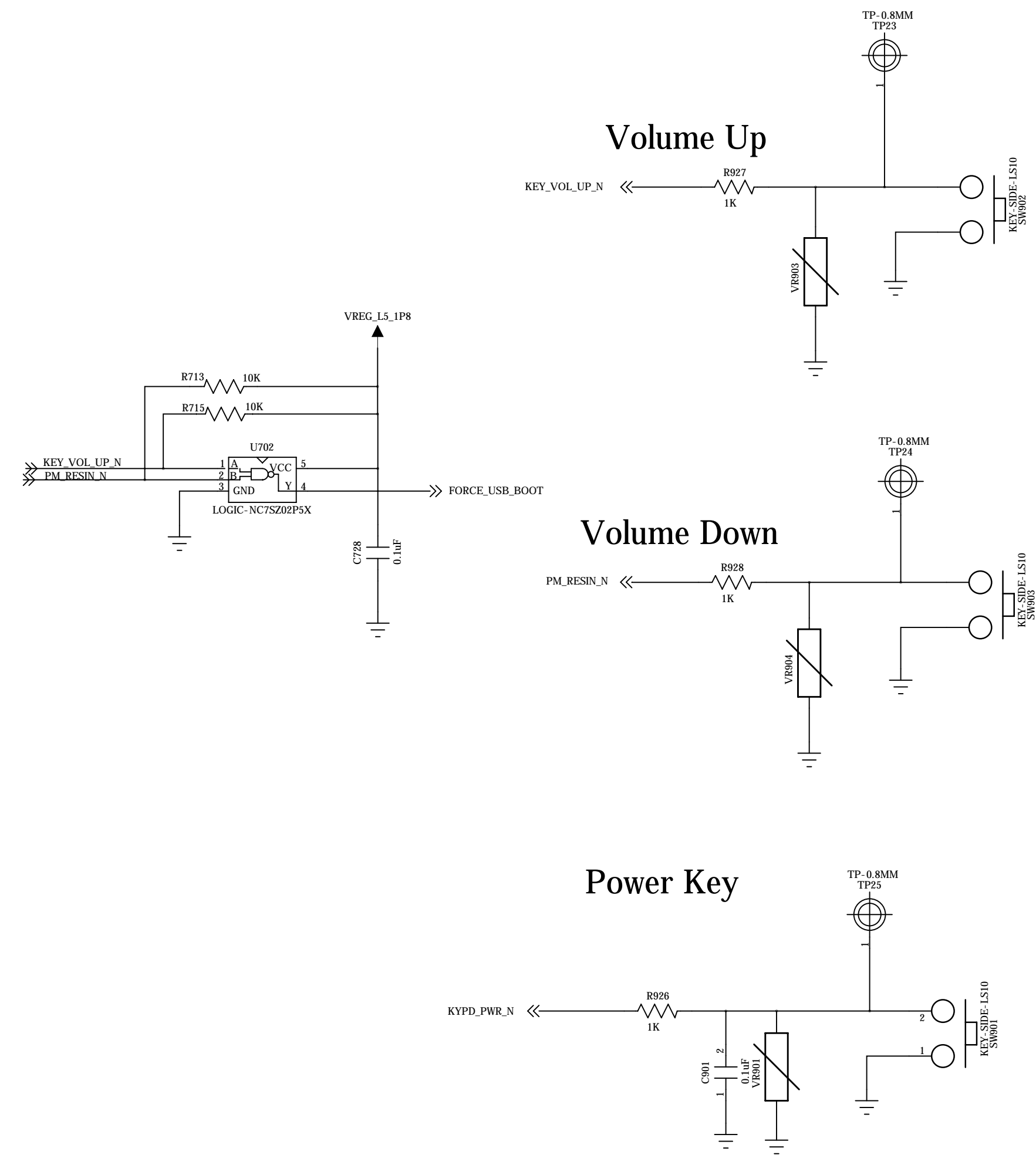
InvenSense: MPU6880

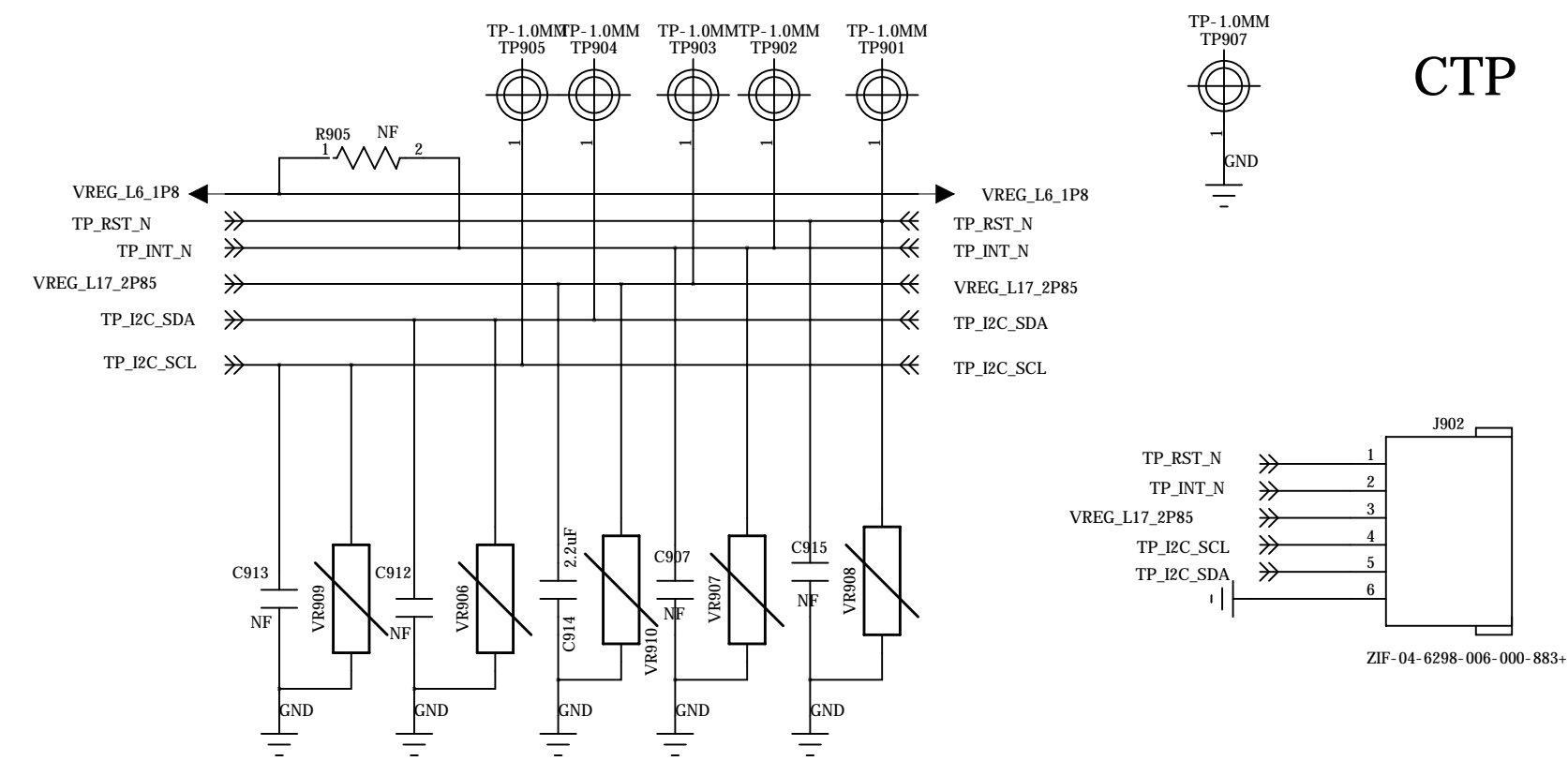
I2C Address@AD0=0	
Write	0xD0
Read	0xD1

I2C Address@AD0=1	
Write	0xD2
Read	0xD3

Power on sequence: L6/L17 can be applied in any order







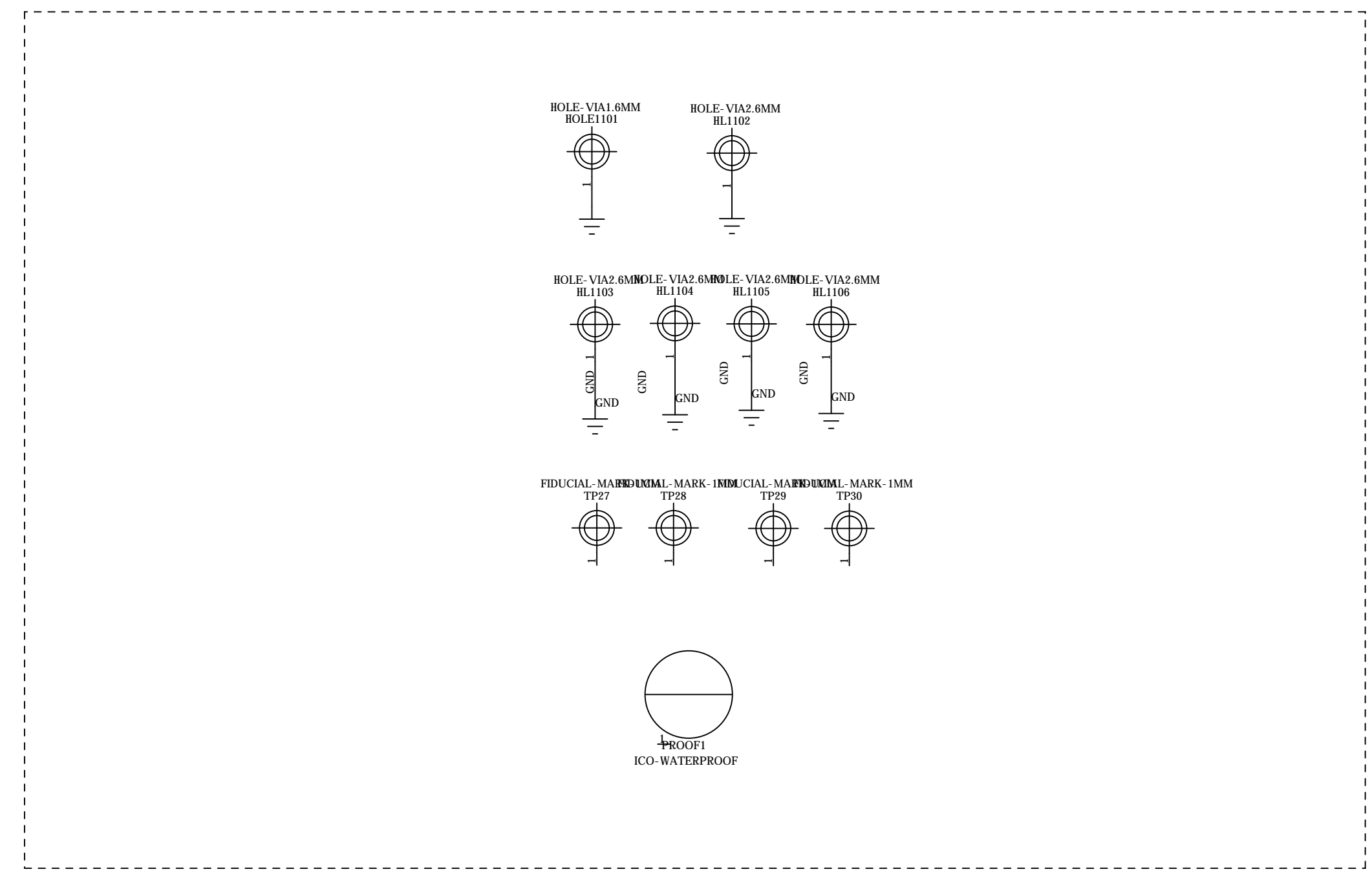
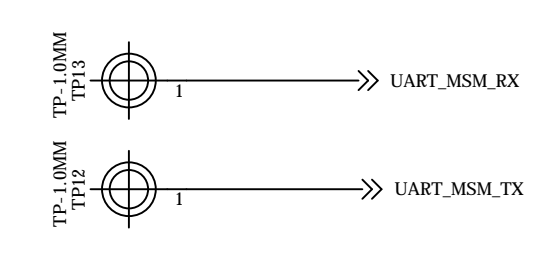
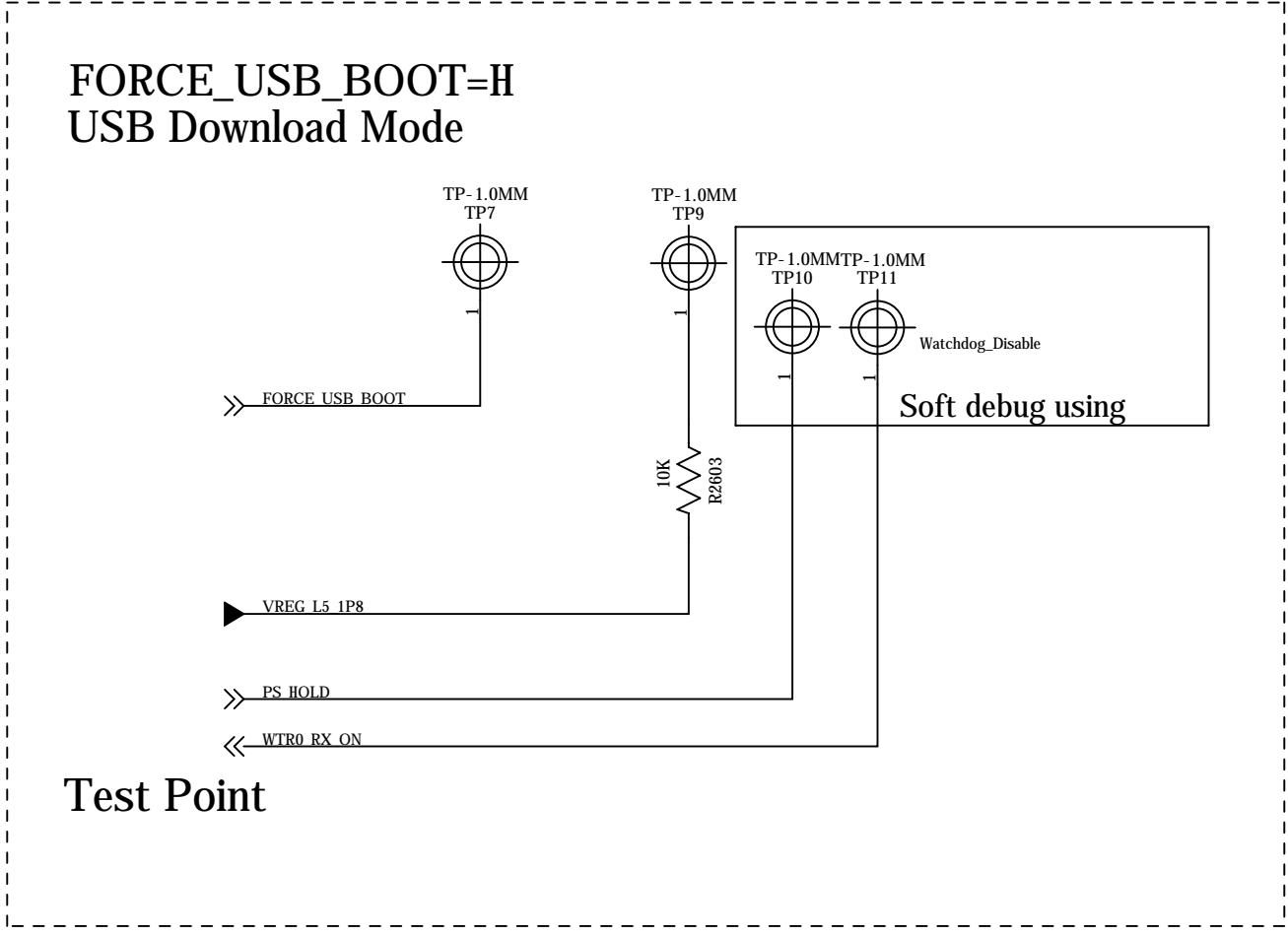
Power on sequence: Single Supply

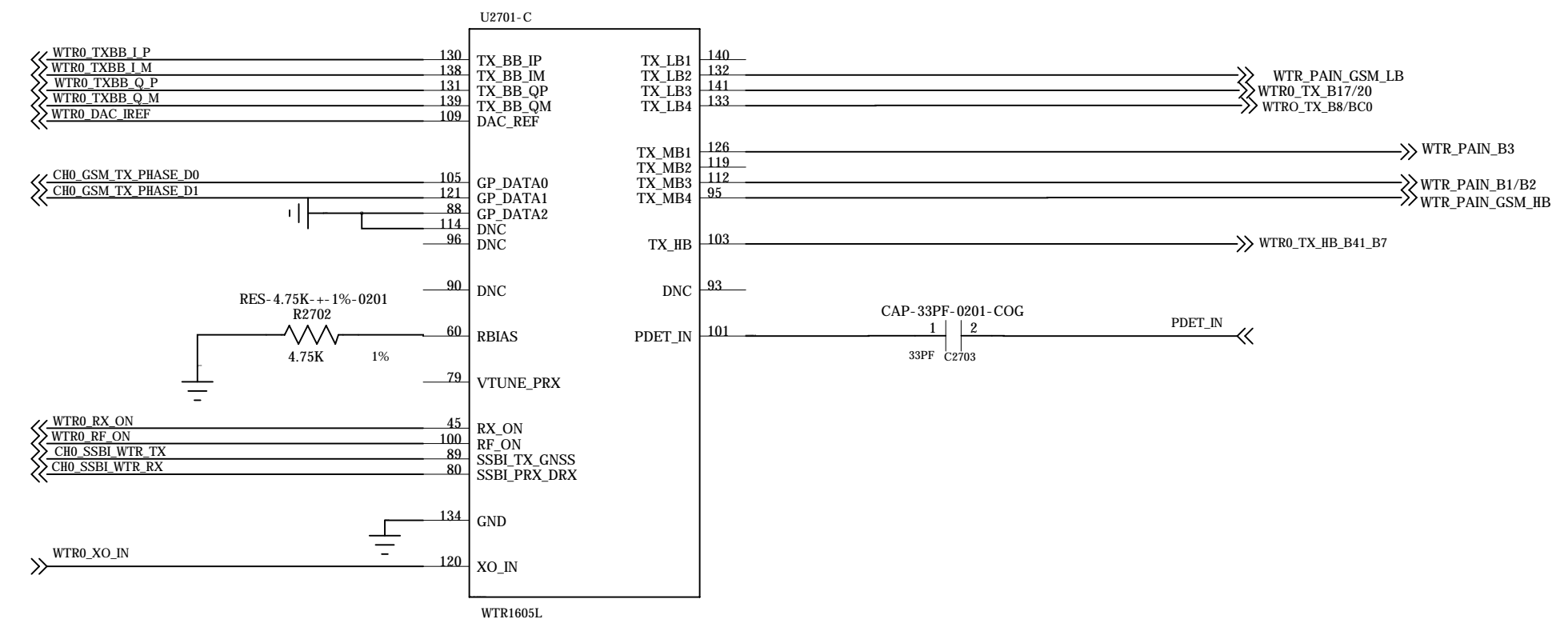
FocalTech: FT5336	
I2C Address	
Write	0x70 It can be changed by FACTORY.
Read	0x71

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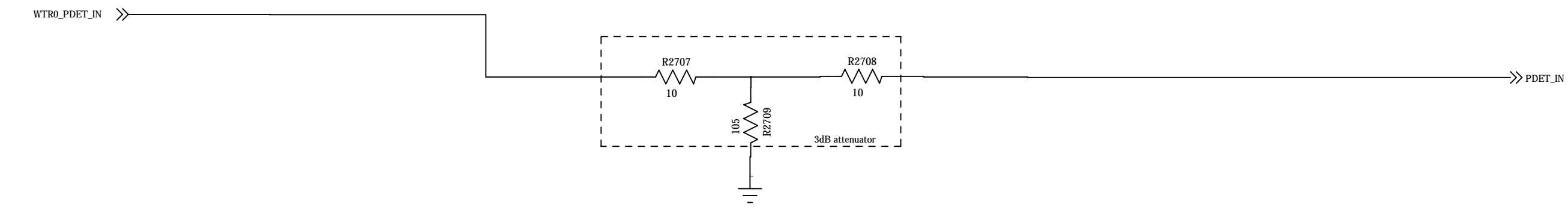
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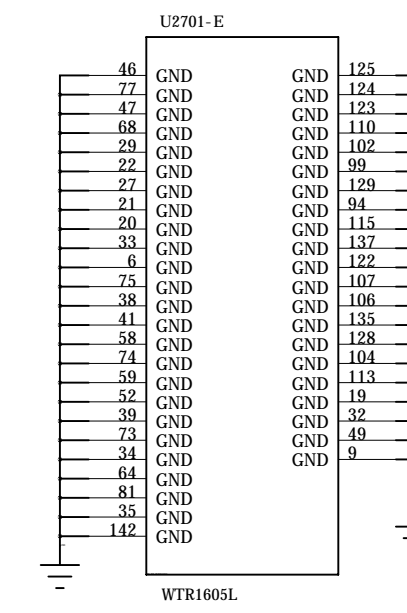
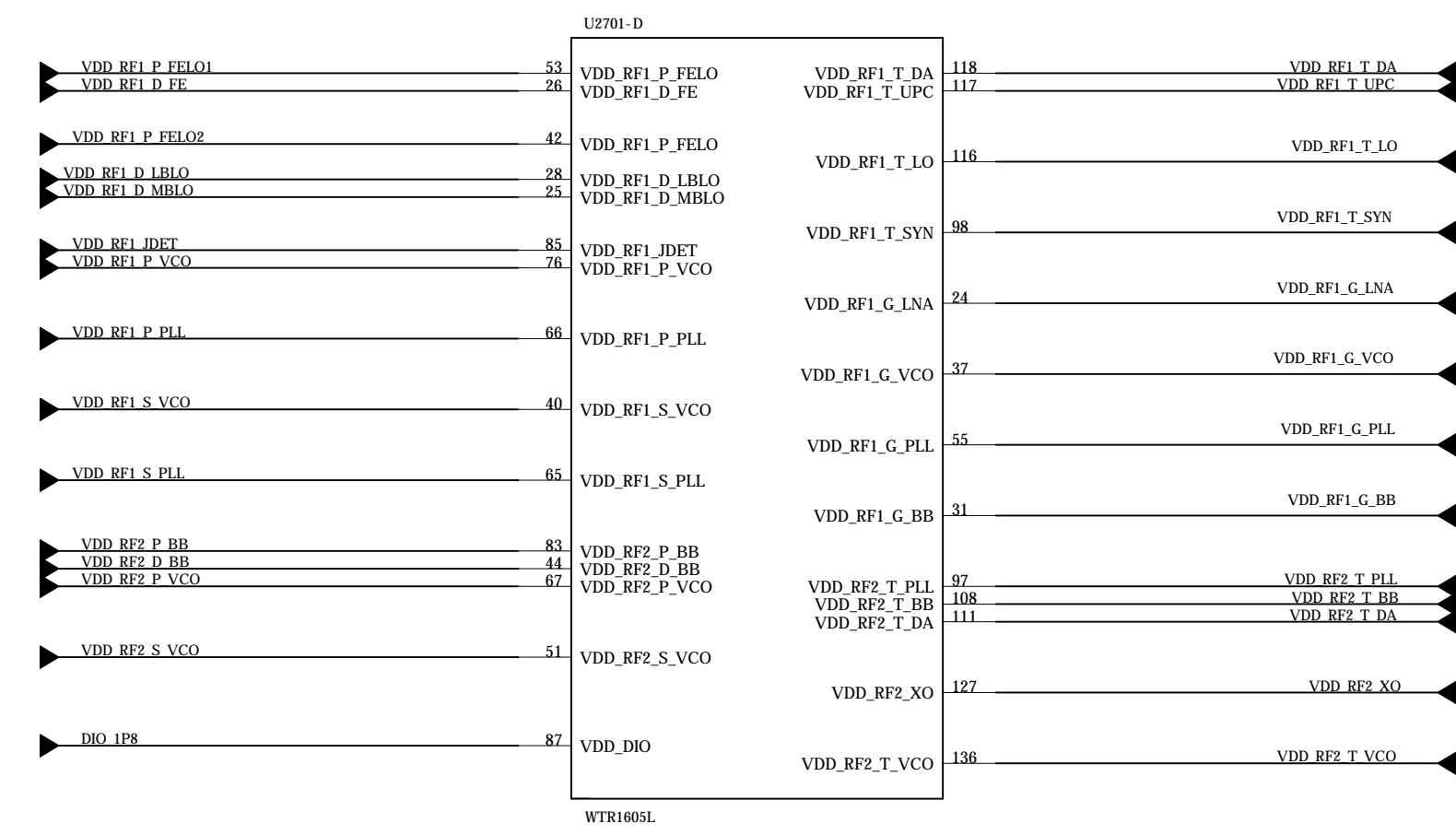
use C2701 for both SPDT power supply, and use C2705 for both SPDT control line

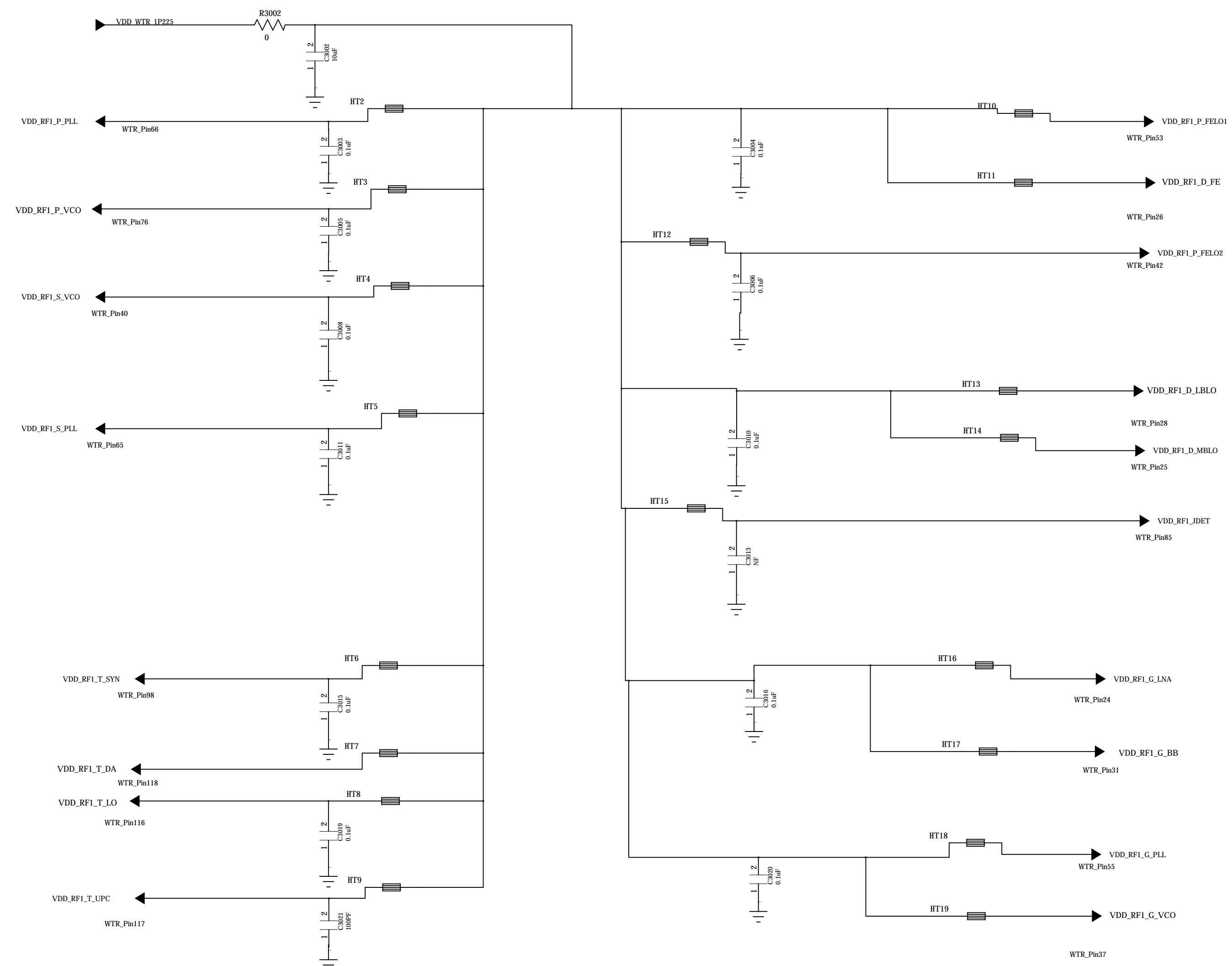
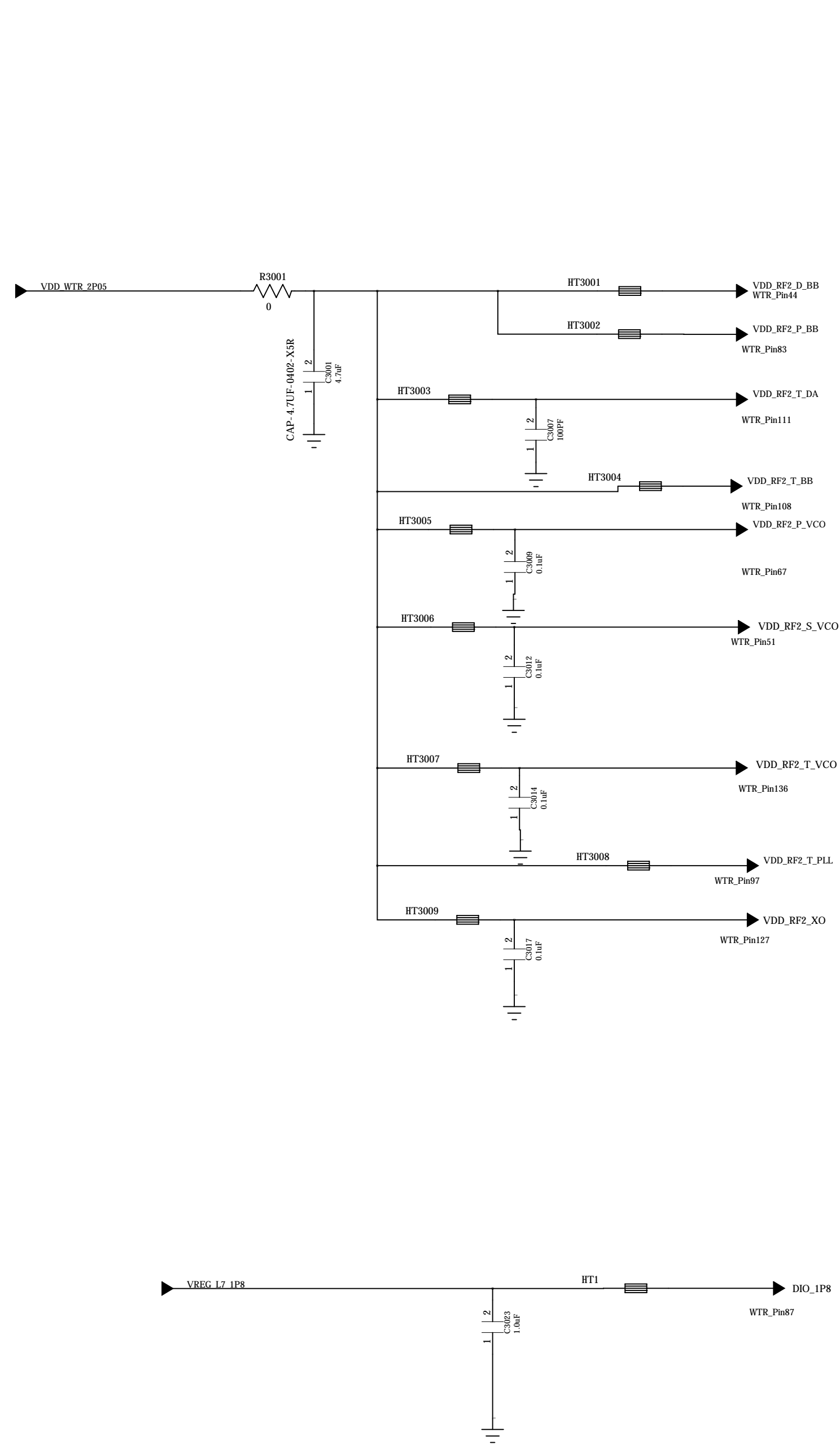


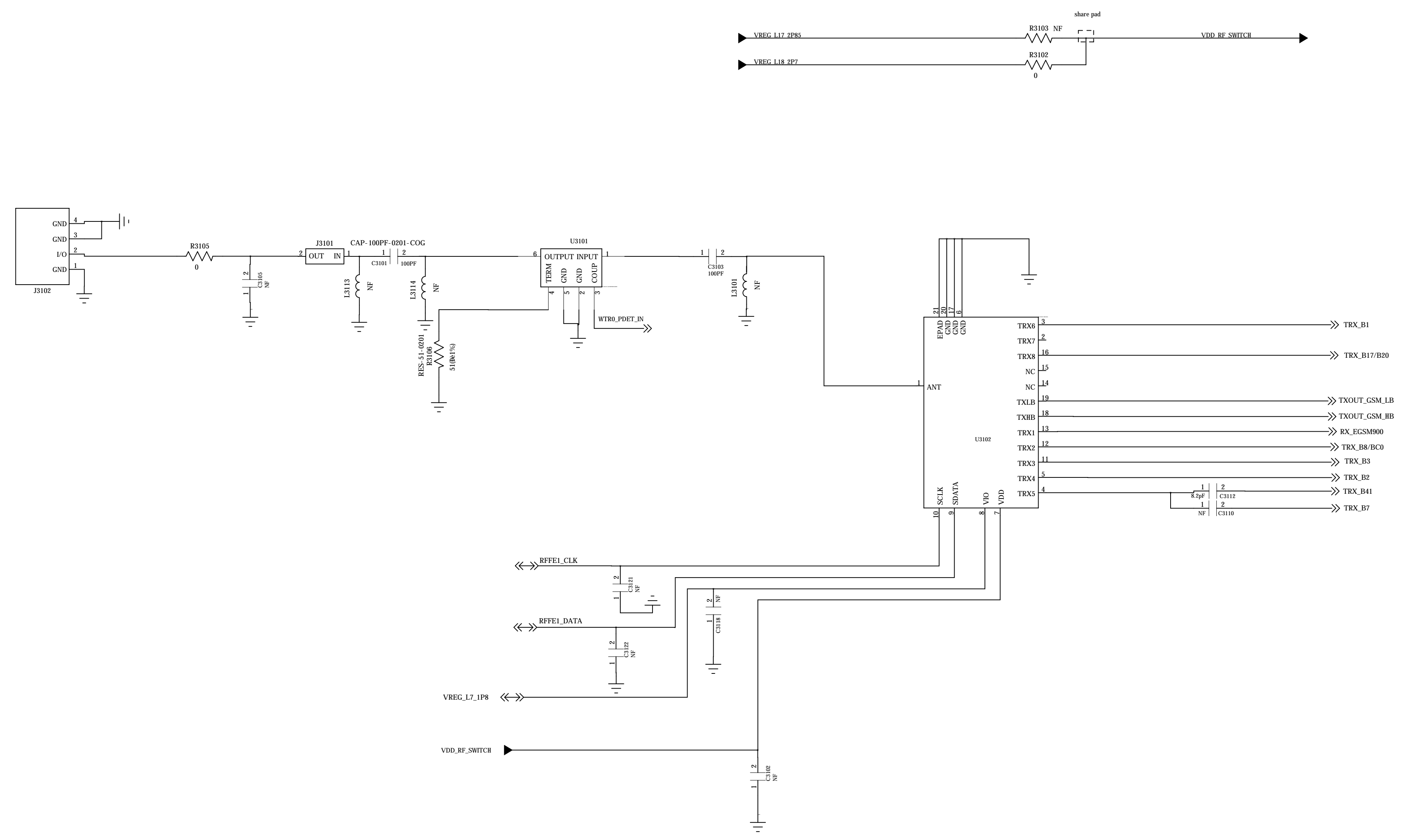
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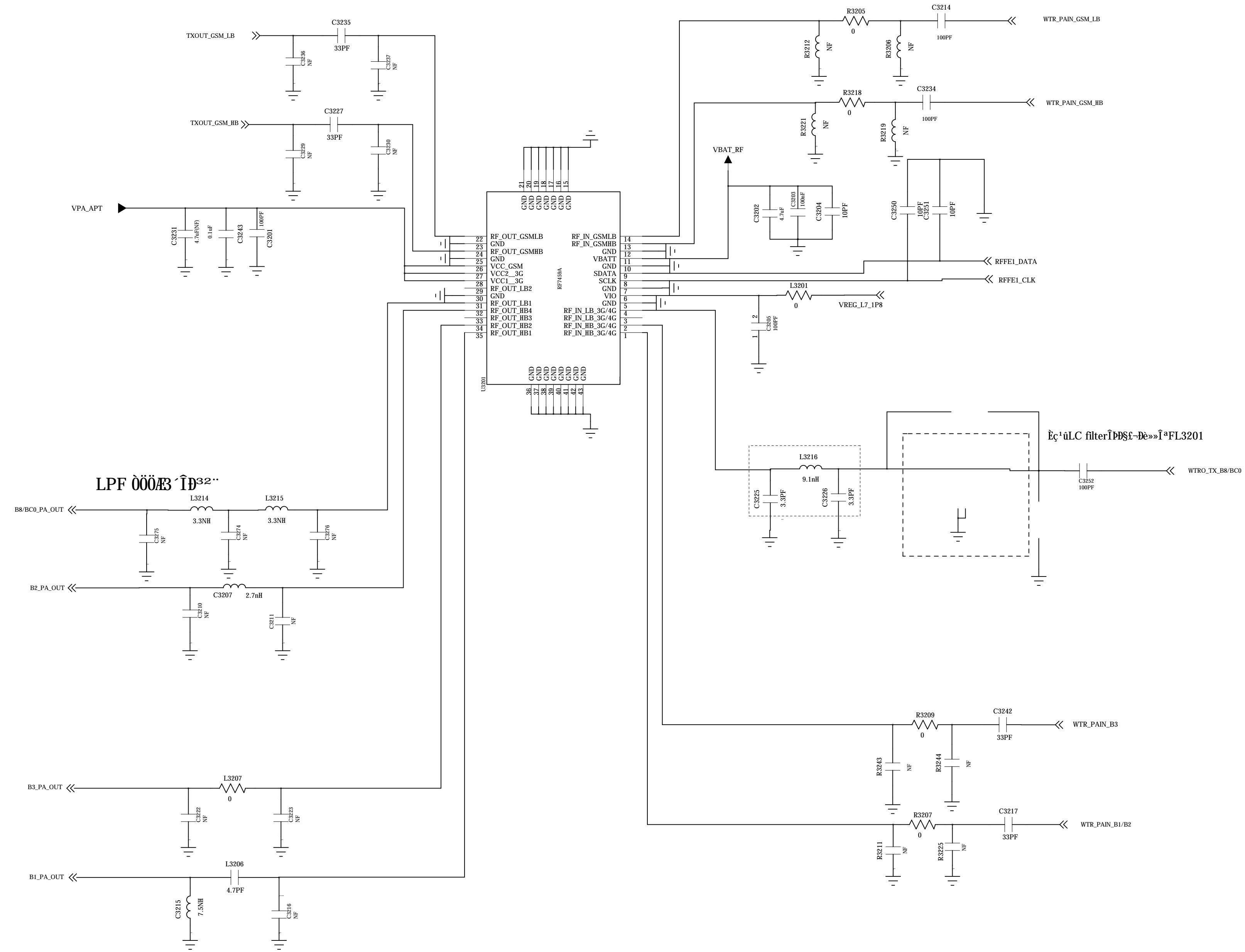






	ANT_SWITCH_CTL0	ANT_SWITCH_CTL1
TRX_B7		
TRX_B40		
QFE2320_ANT		
TRX_B38_41		

MMMB PA : B1,2,3,5,17 2G HB, 2G LB



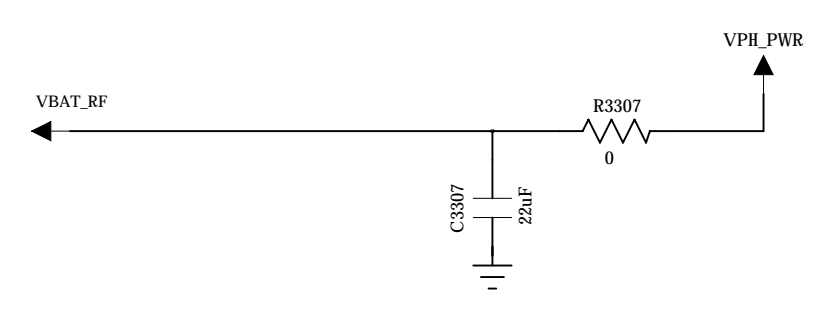
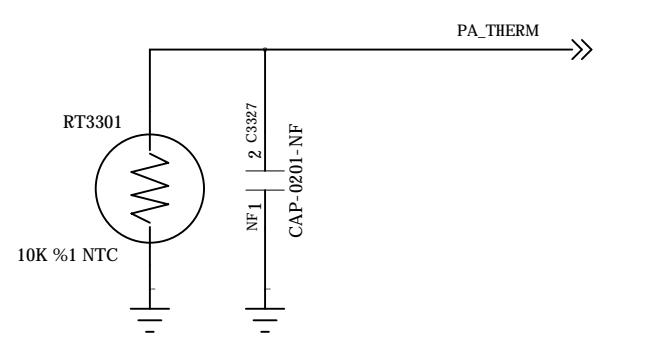
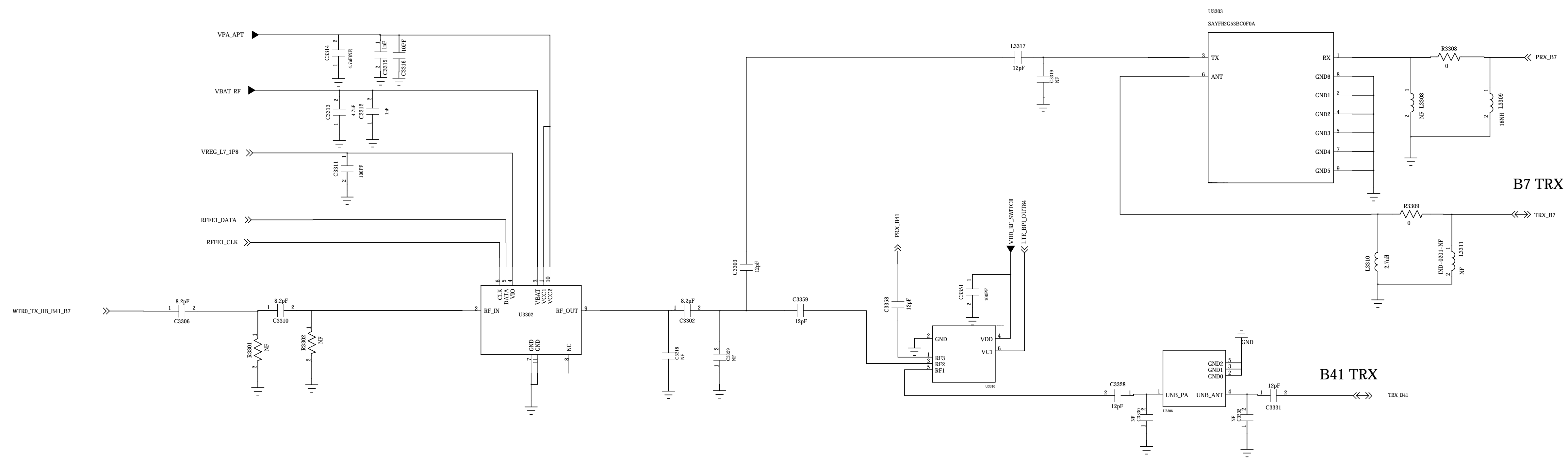
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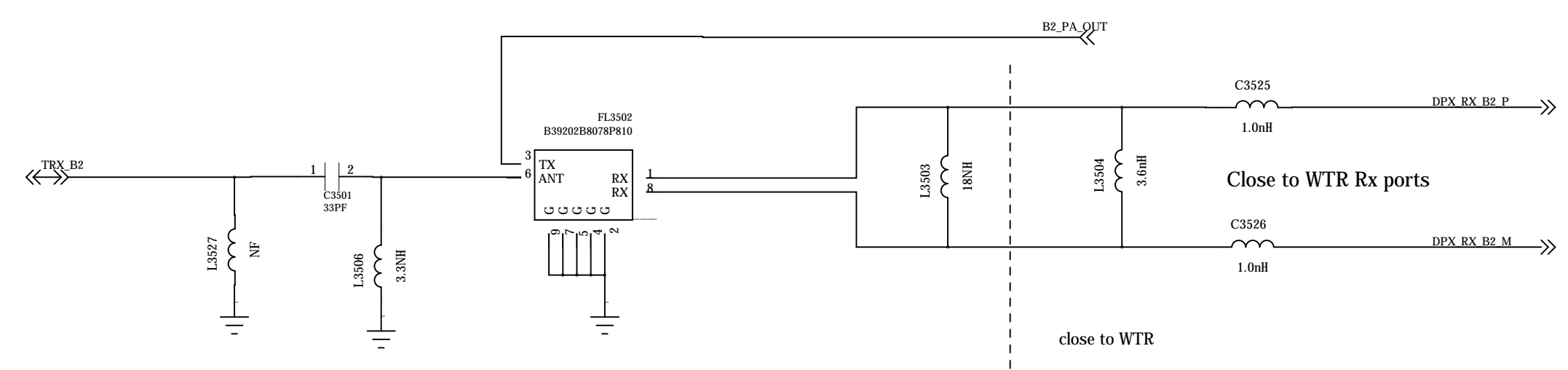


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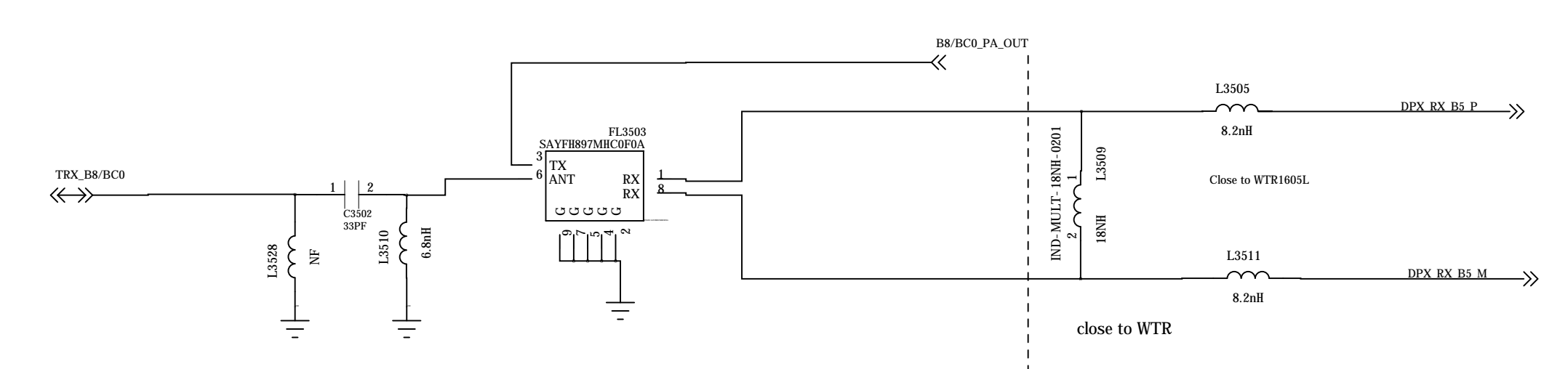
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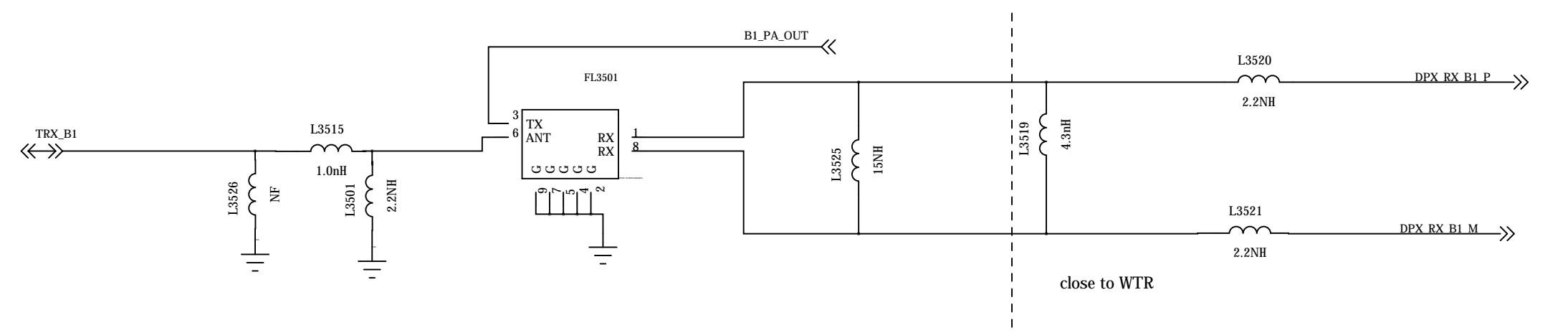
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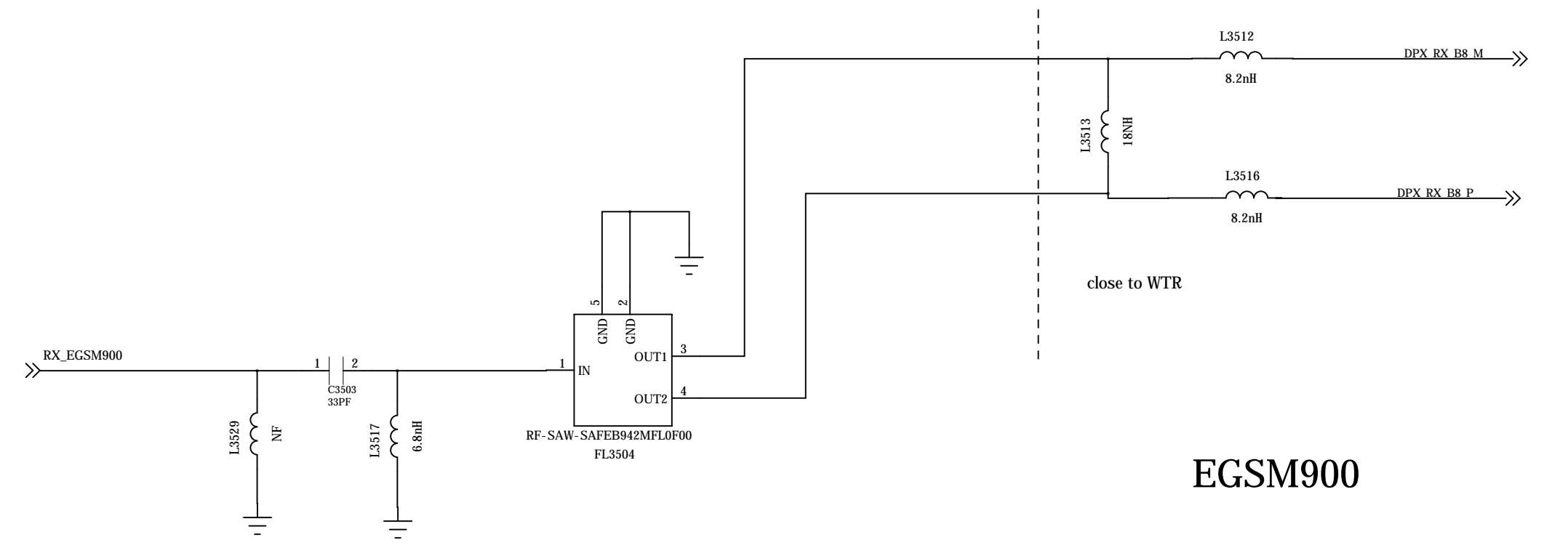
UMTS B2



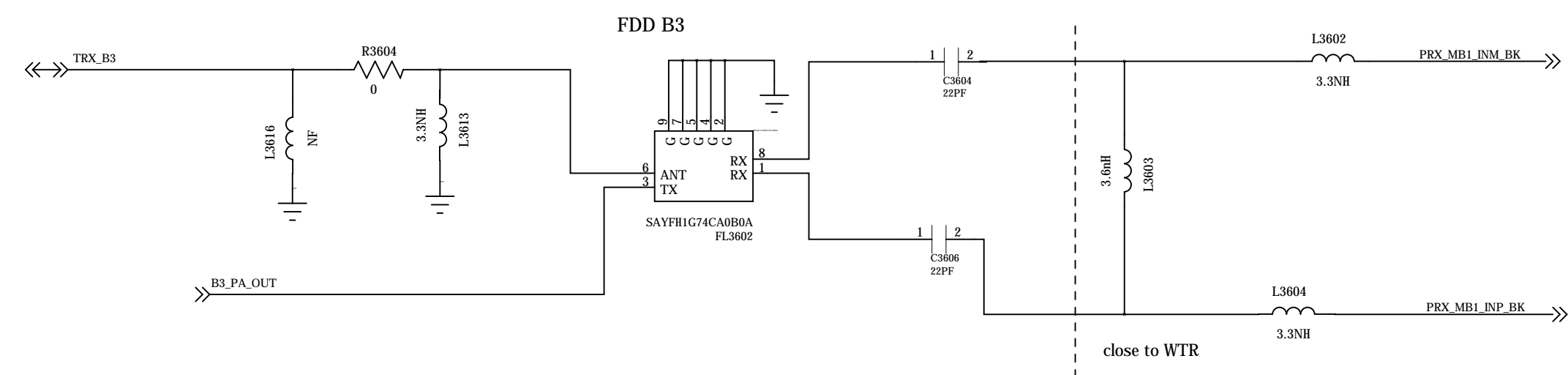
UMTS B5



UMTS B1



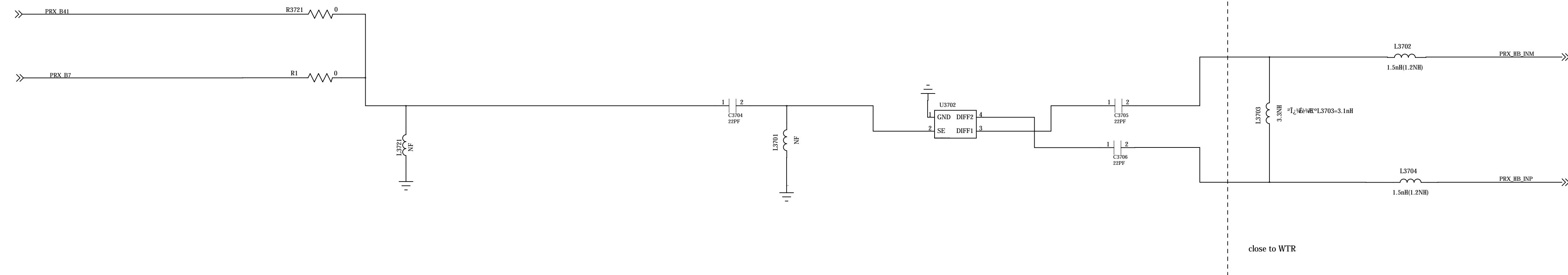
EGSM900

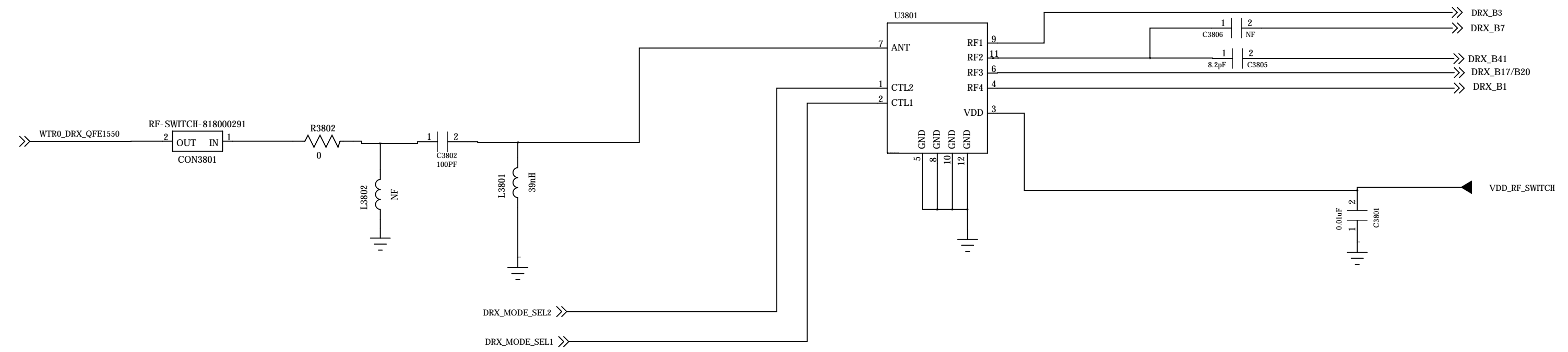


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RX BAND SELECT LOGIC

	PORT	DRX_MODE_SEL0	DRX_MODE_SEL1	DRX_MODE_SEL2
DRX_B7	RF1			
DRX_B40	RF2			
DRX_B41	RF3			
DRX_B39_B1	RF4			
DRX_B3	RF6			

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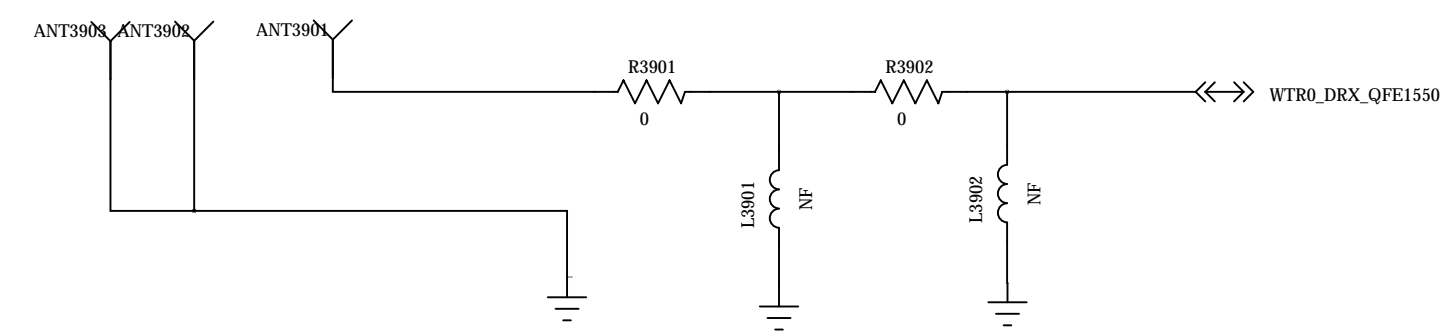
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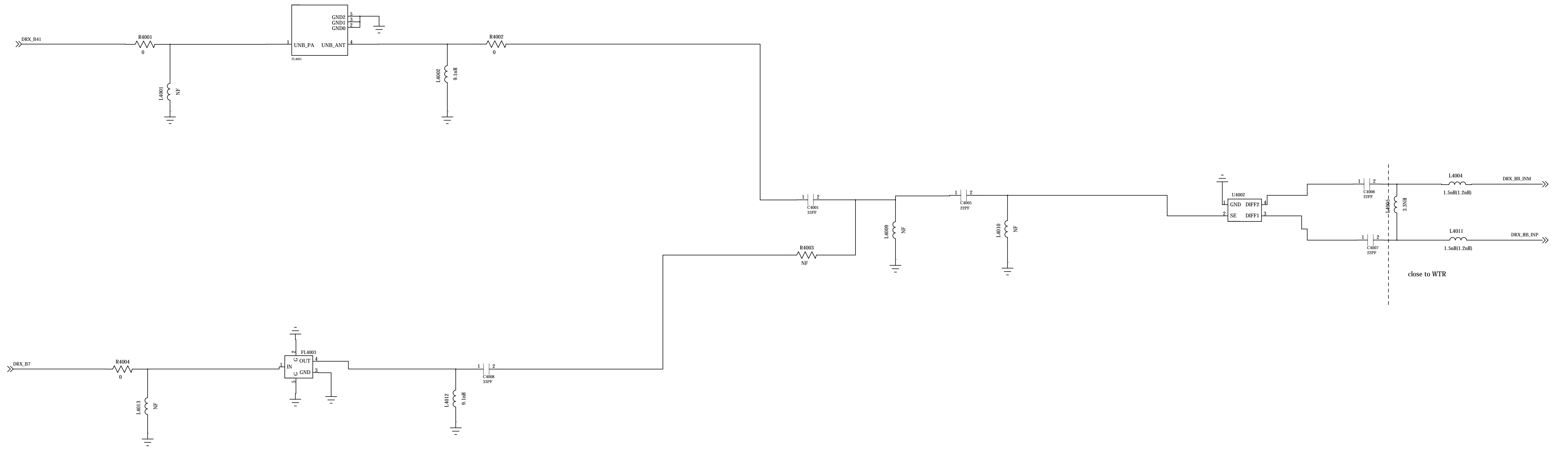
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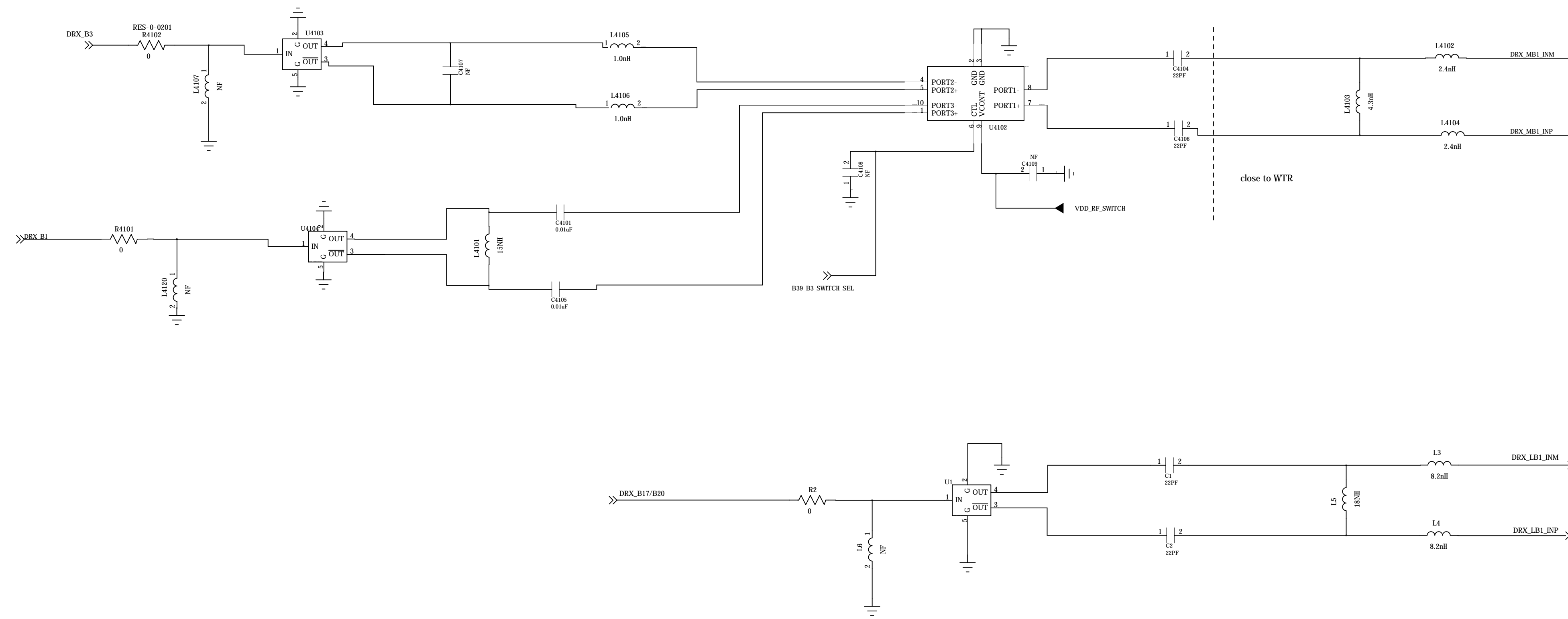


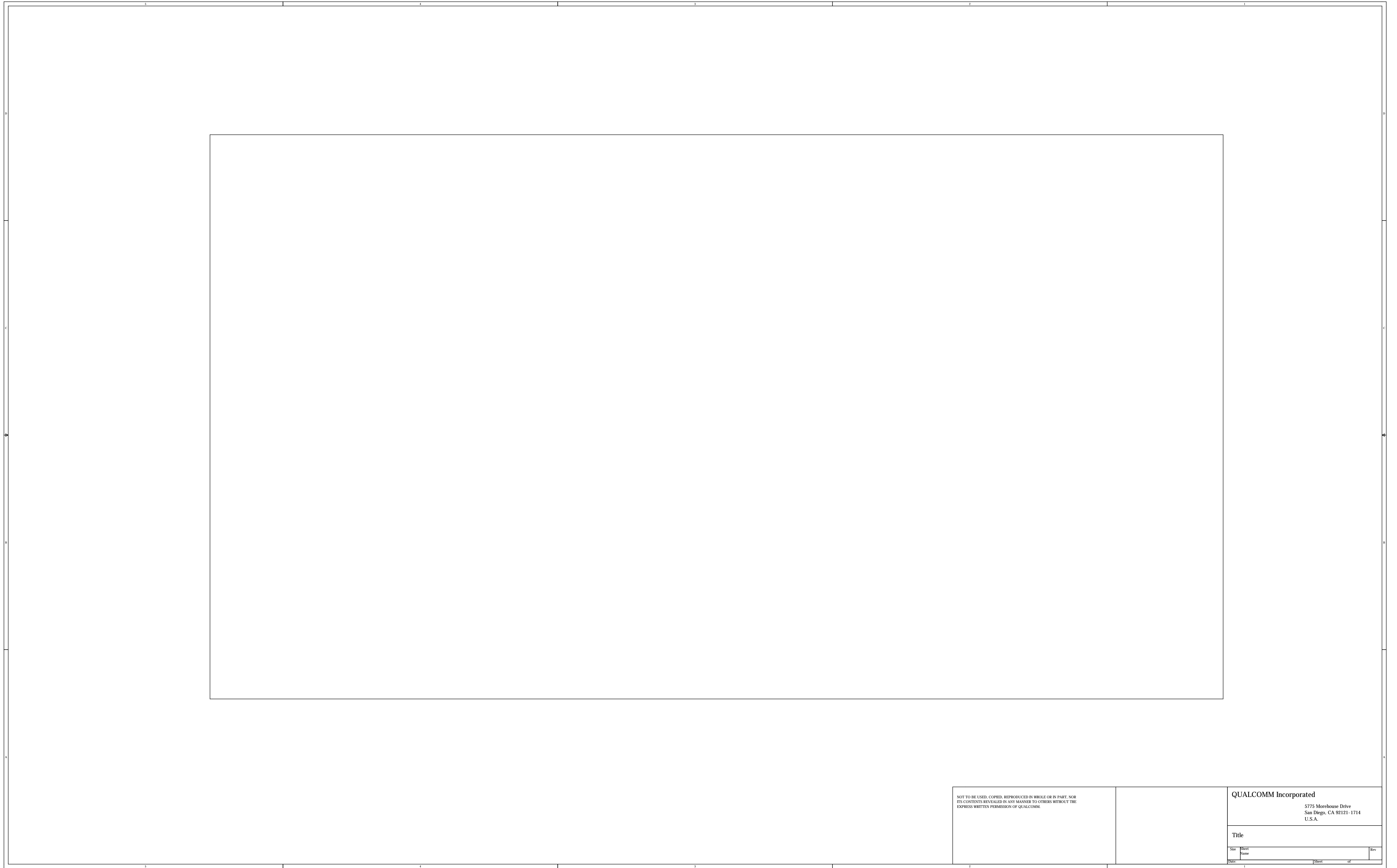
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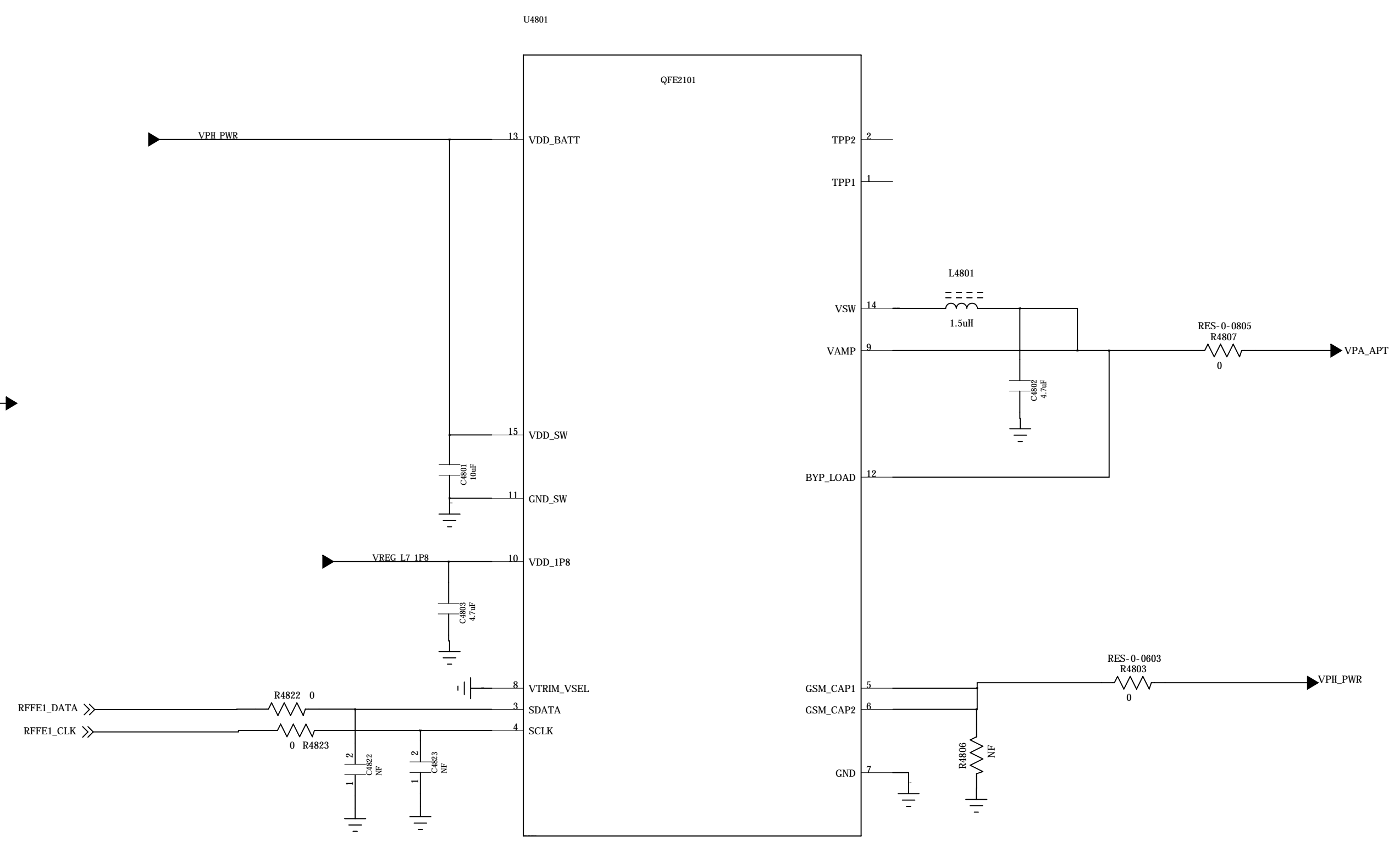
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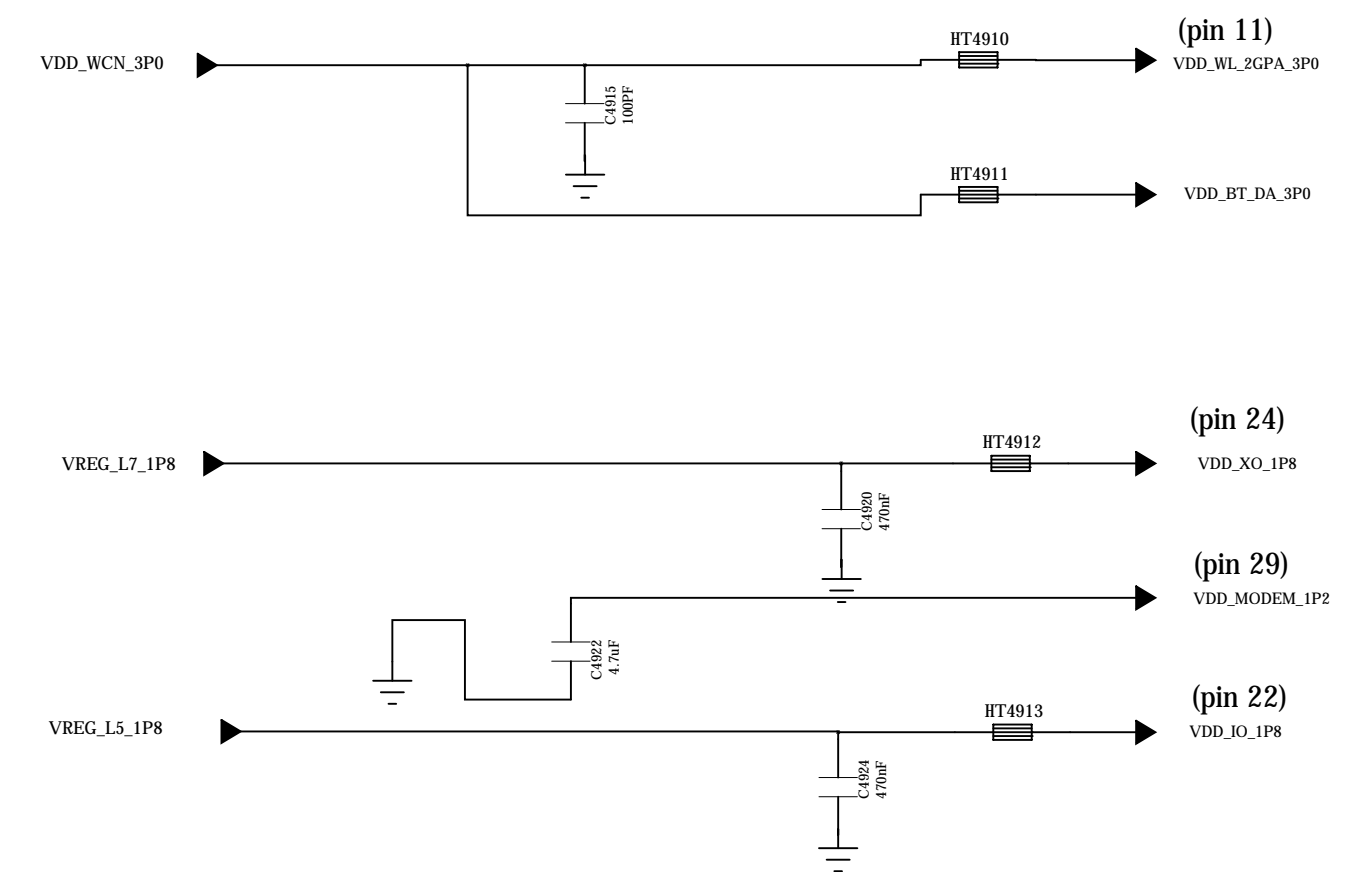
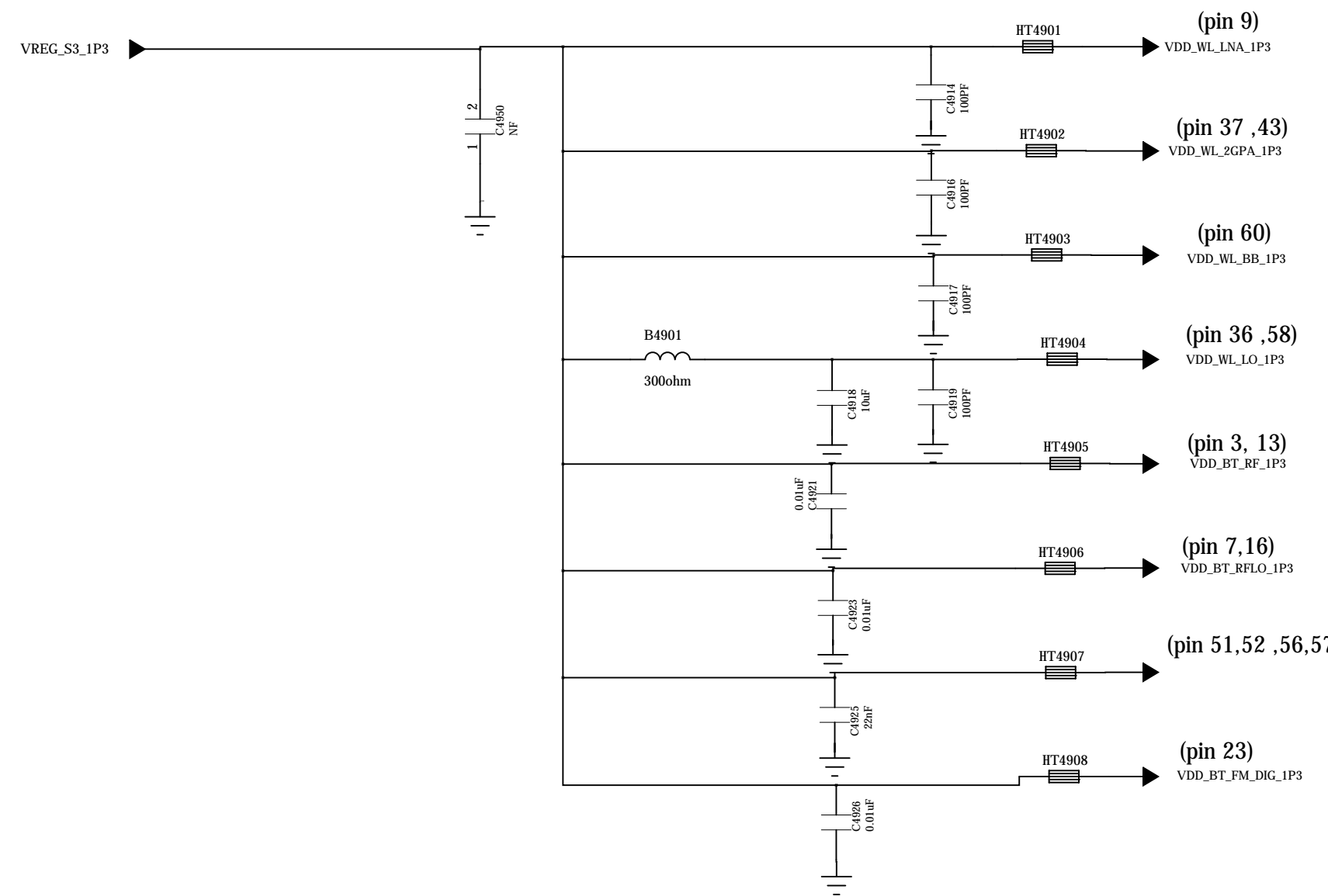
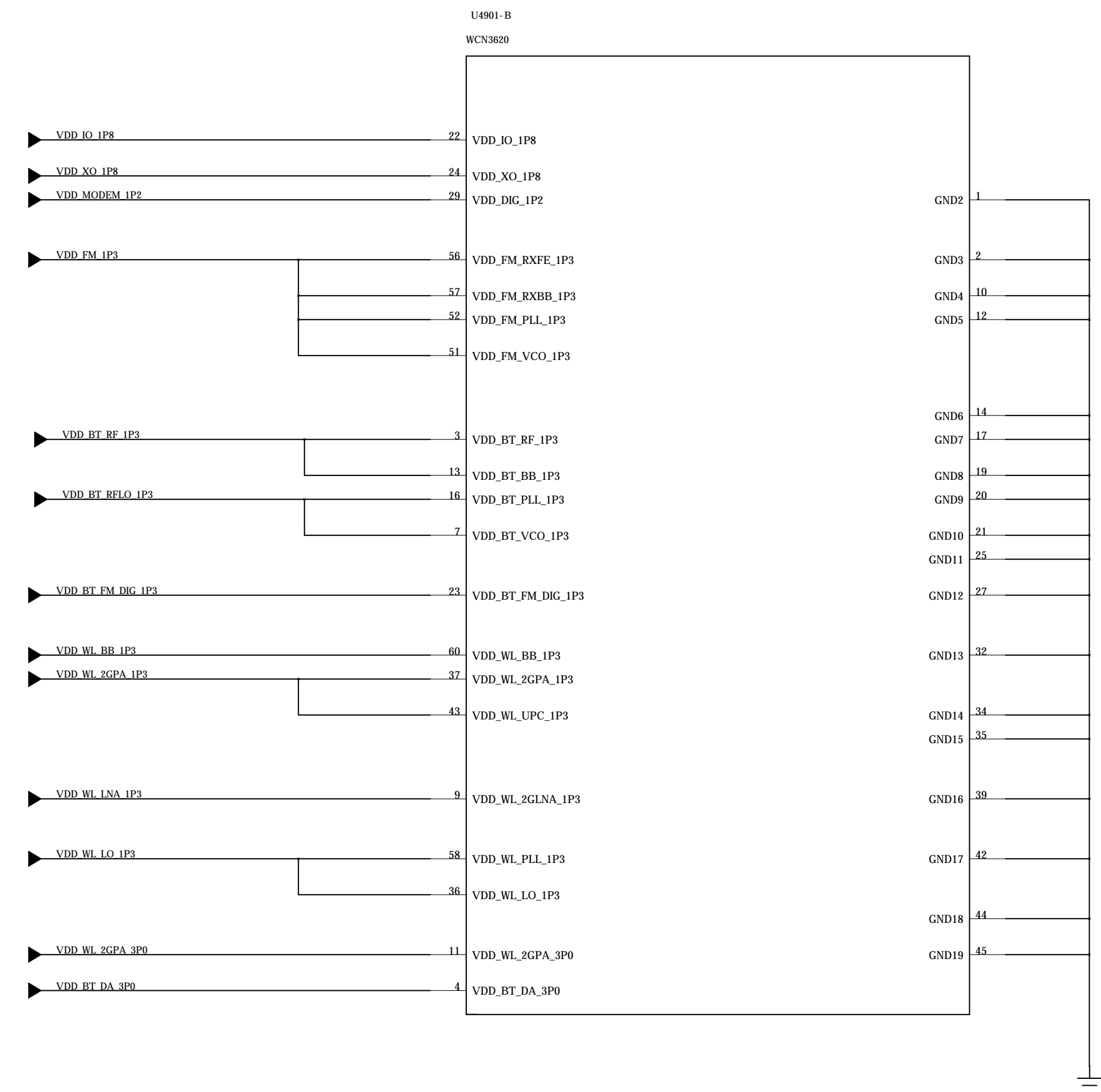
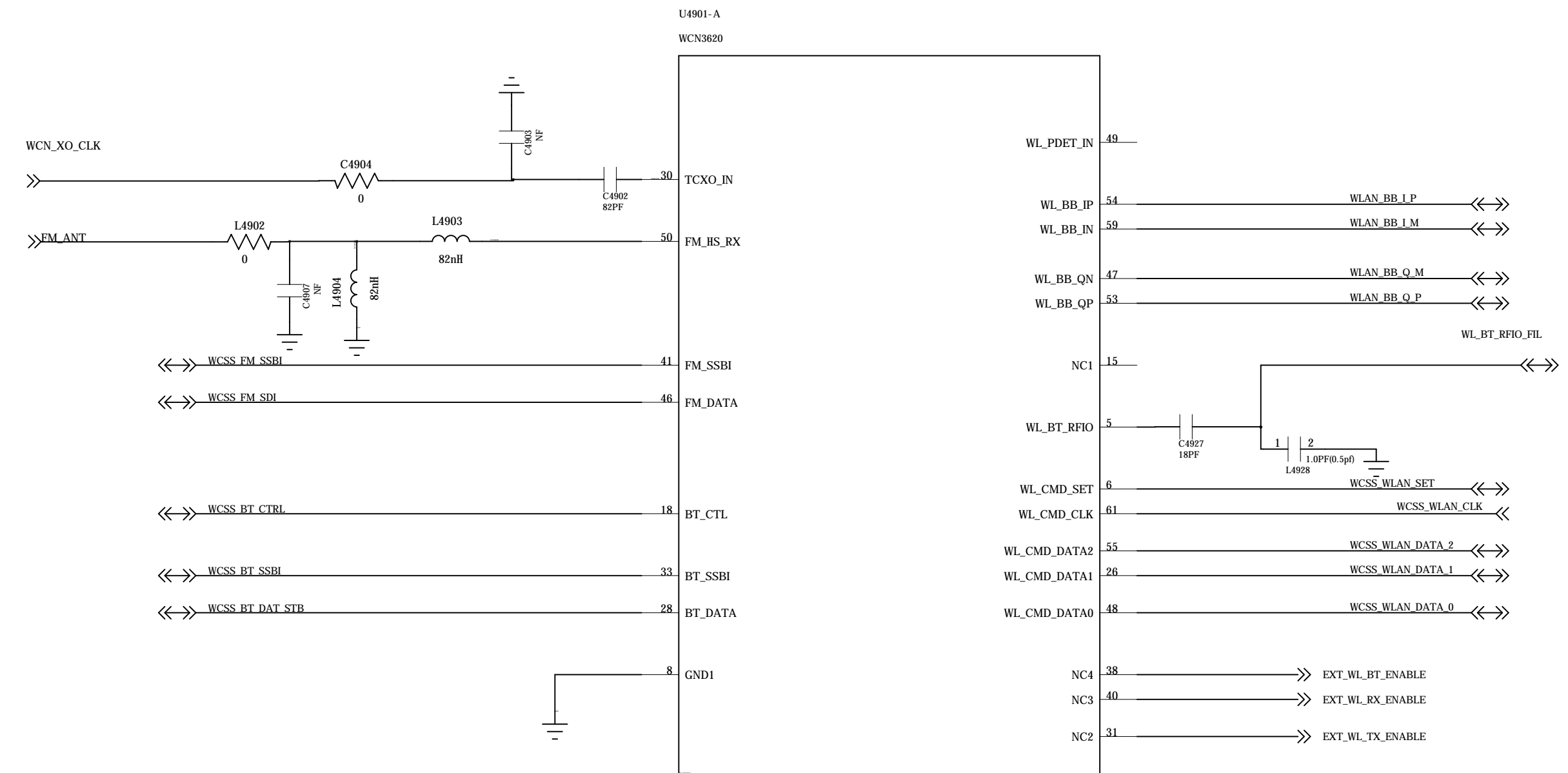
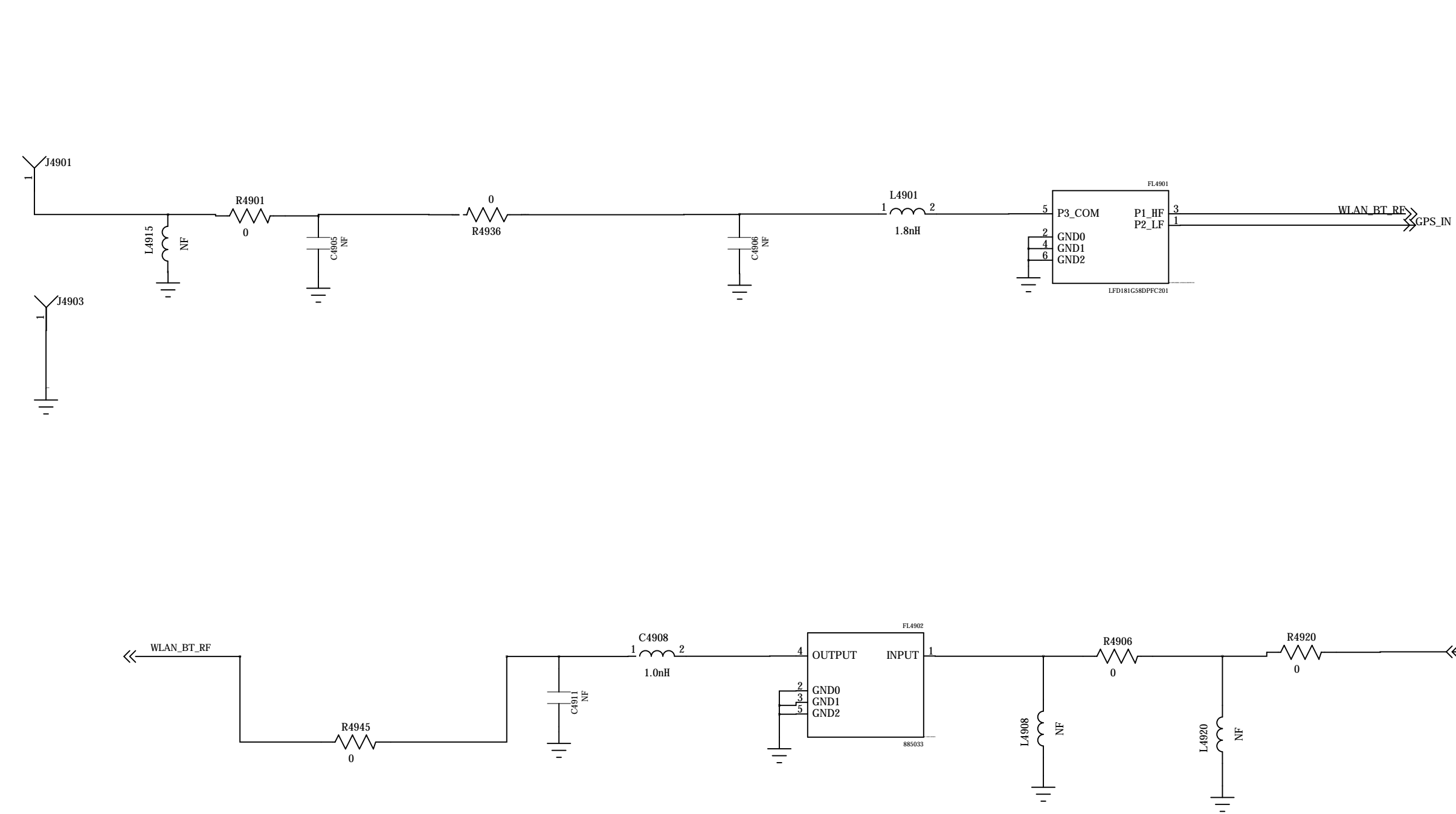
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Note: Use a single low impedance power plane/fill VPA for all ET PA VCC1/VCC2.
 Refer to 80-NA681-91 rev.B or later revisions for more layout details.



WIFI/BT





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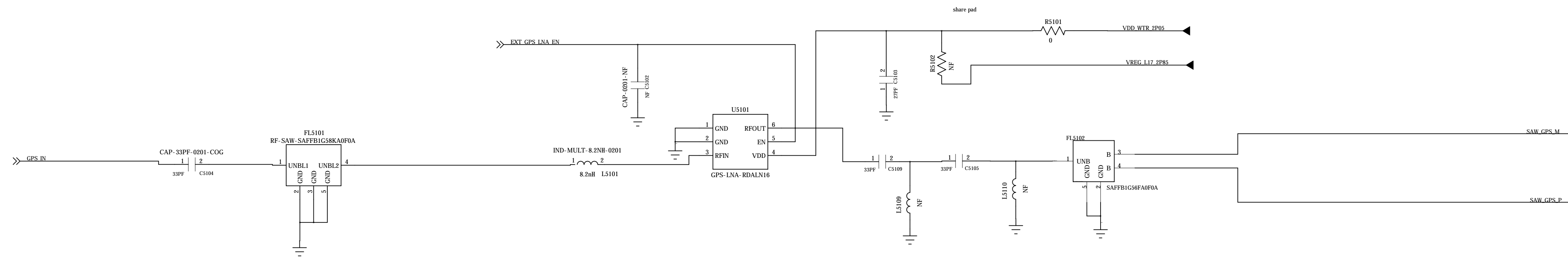
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SCHEMATIC, CCA, MAIN BOARD, QRB8916.2-4-2, CMCC CSFB, EVT

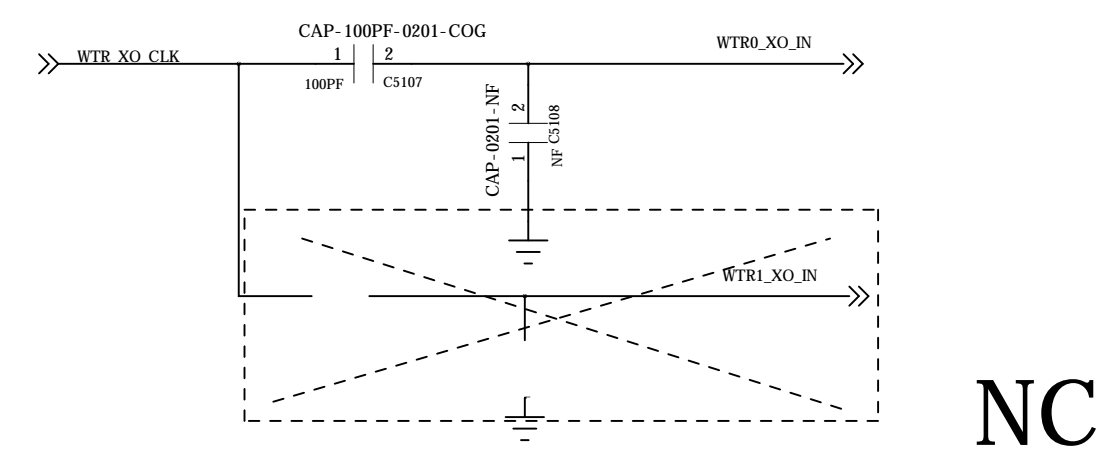
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GPS



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