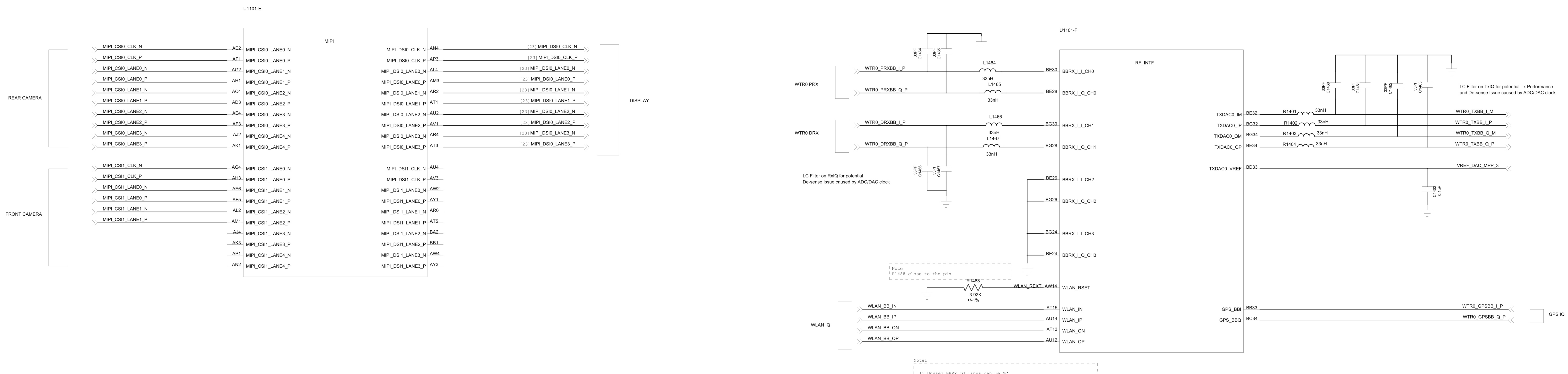

Note: If best EMI practices are followed for MIPI CSI/DSI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints.
Extreme care must be taken that no stubs are created by doing so.

MSM8937 MIPI and RF Control

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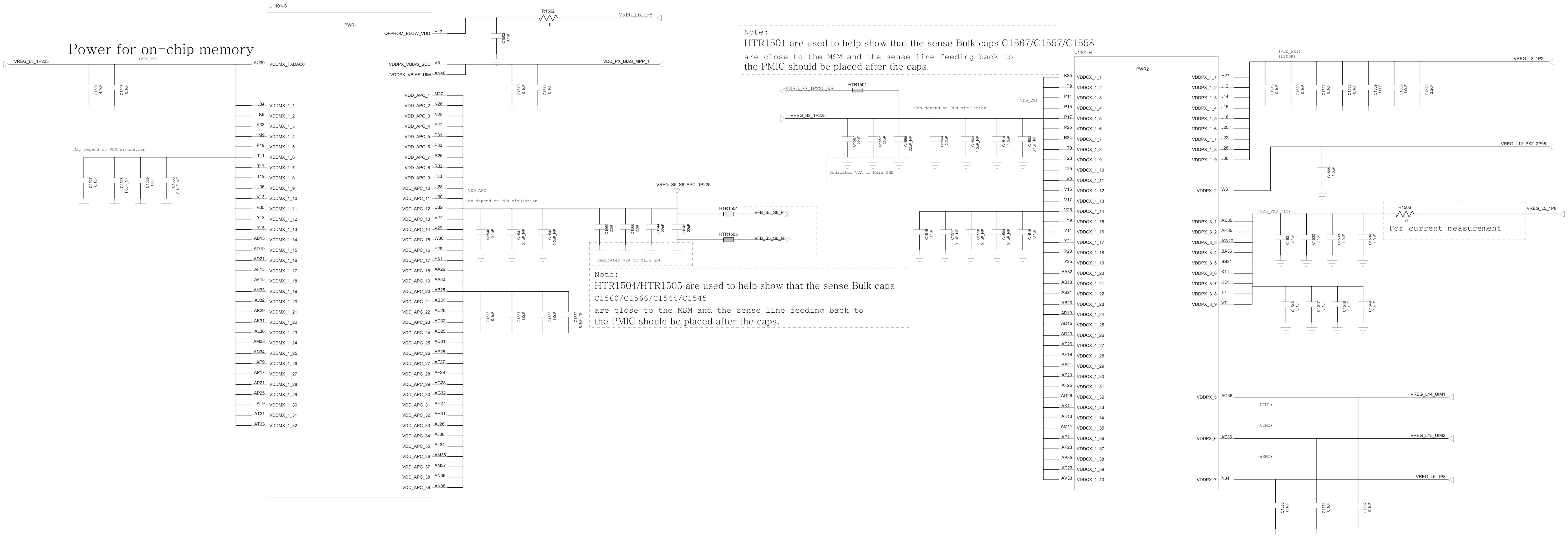
PM TGI



MSM8937 POWER1

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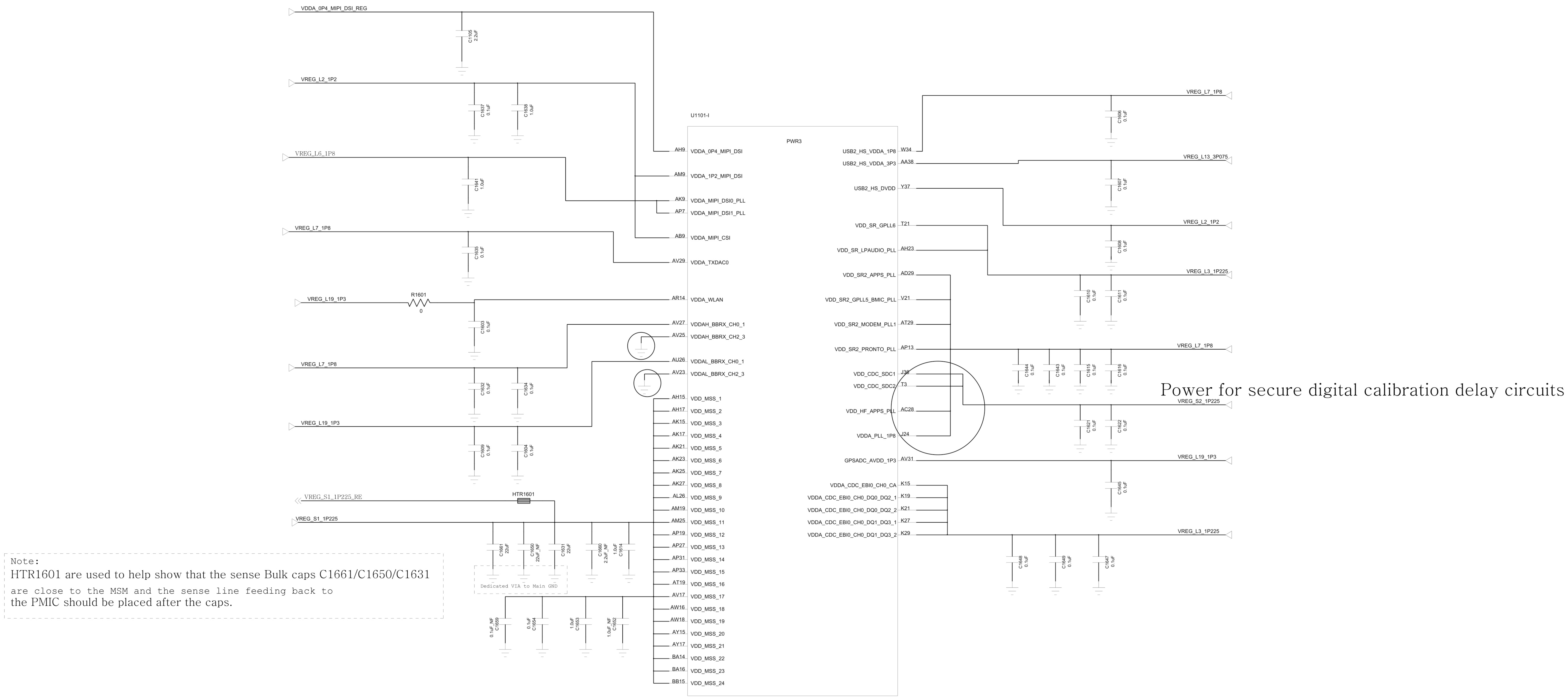

MSM8937 POWER2

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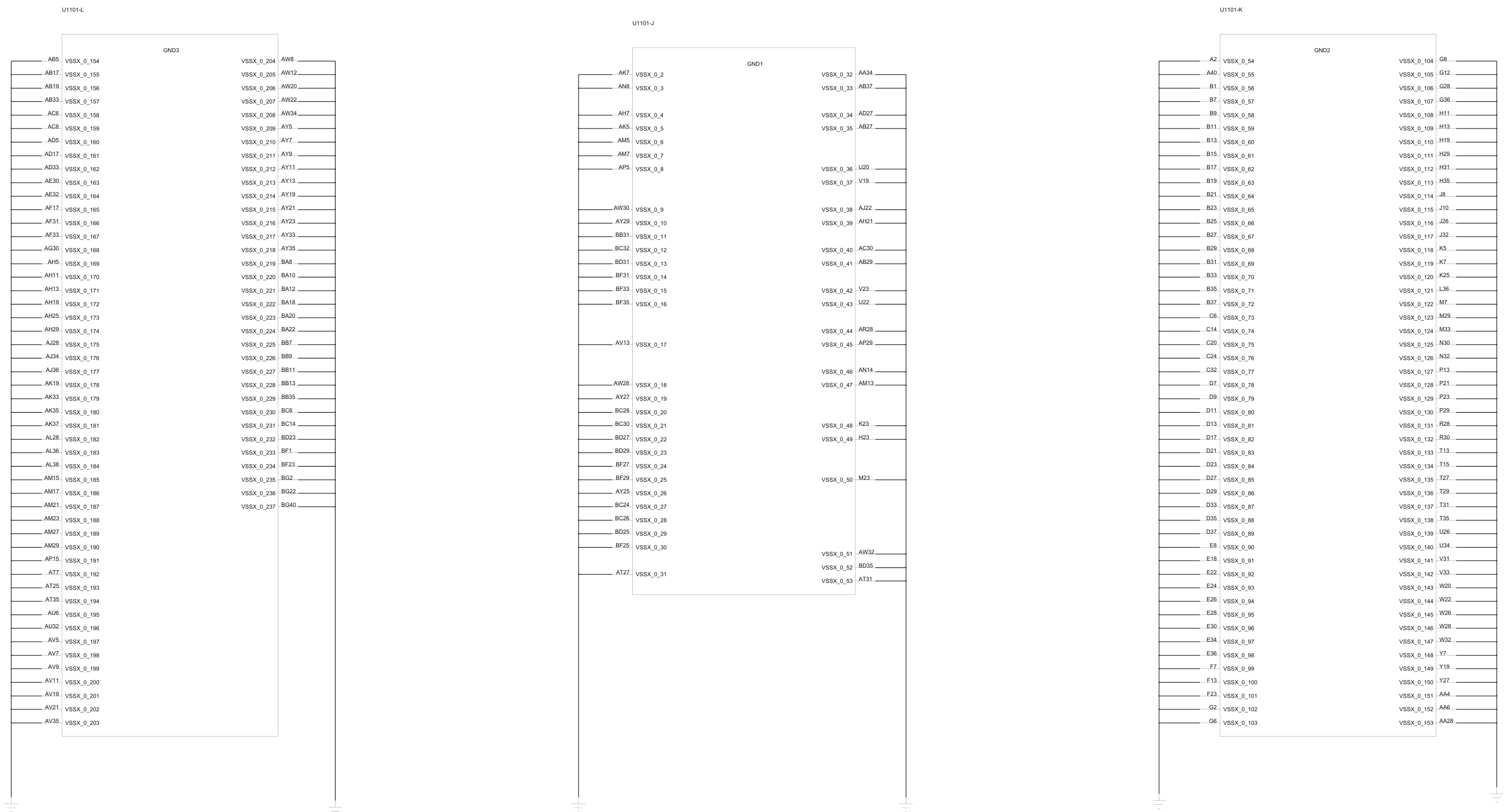

MSM8937 GND

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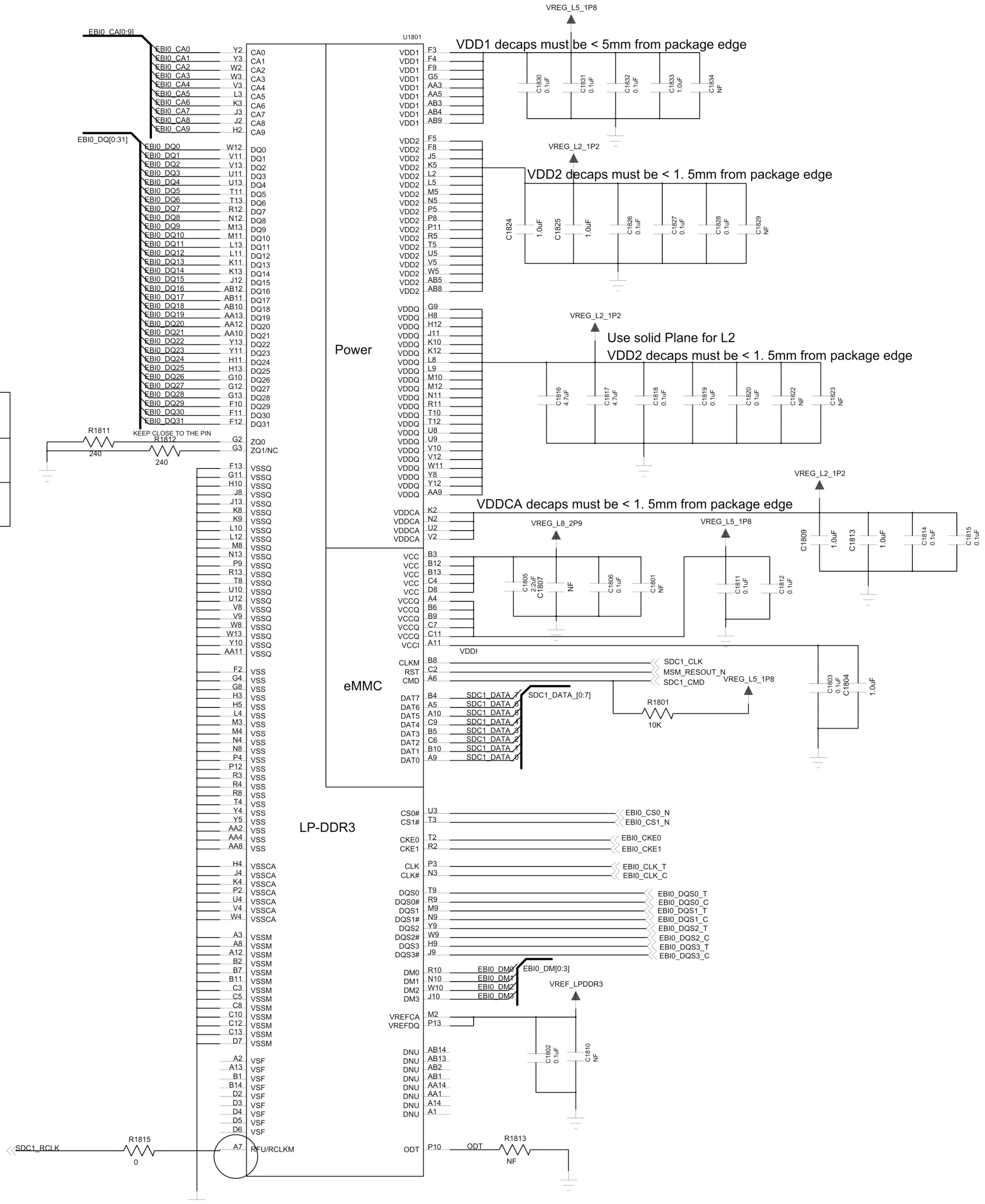

	MICRO/HYNIX	SAMSUNG
U1501.G3	ZQ1	NC
U1501.A7	RFU	RCLK

Note: Pull-up resistors on SDC1_DATA are PCB and eMMC vender dependent

MEMORY:LPDDR3+ EMMC

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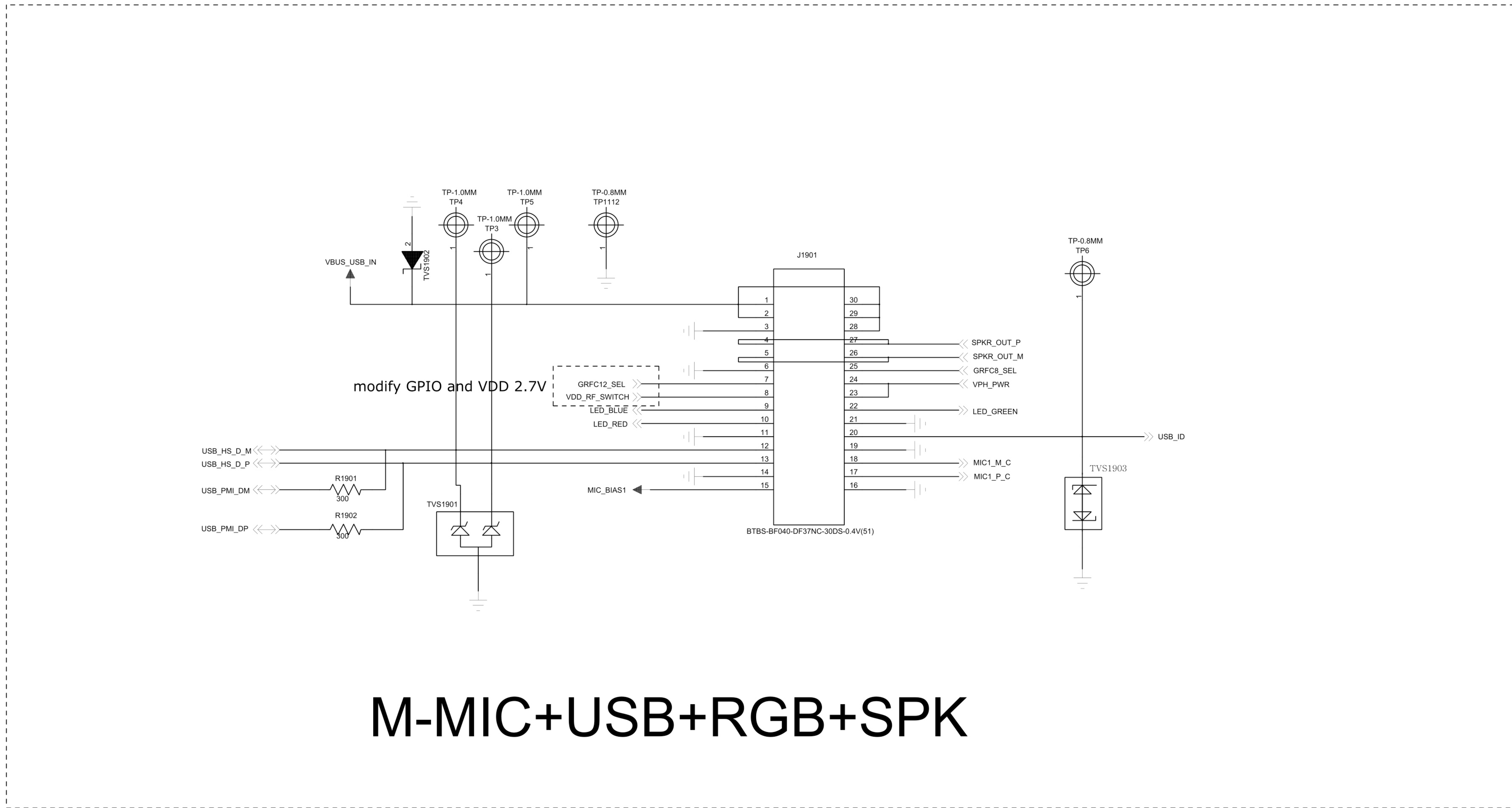

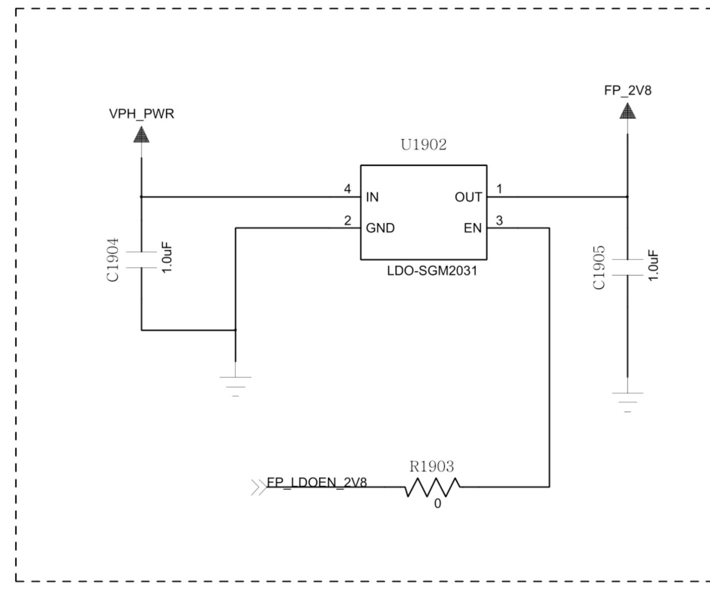

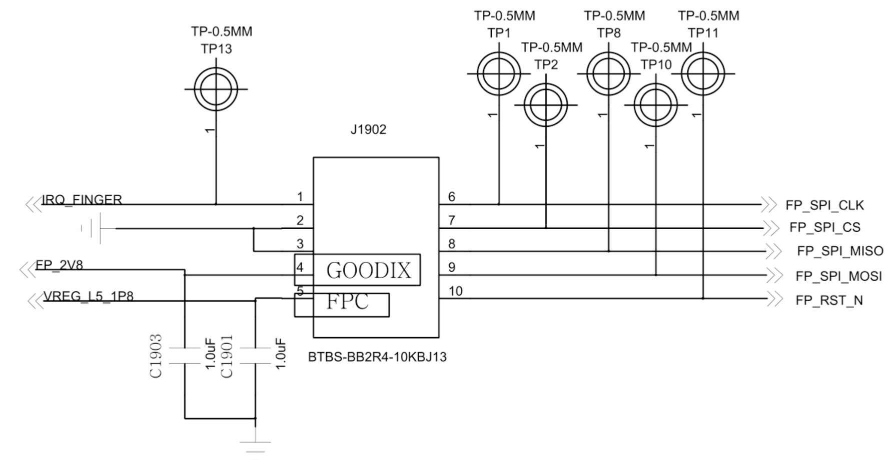

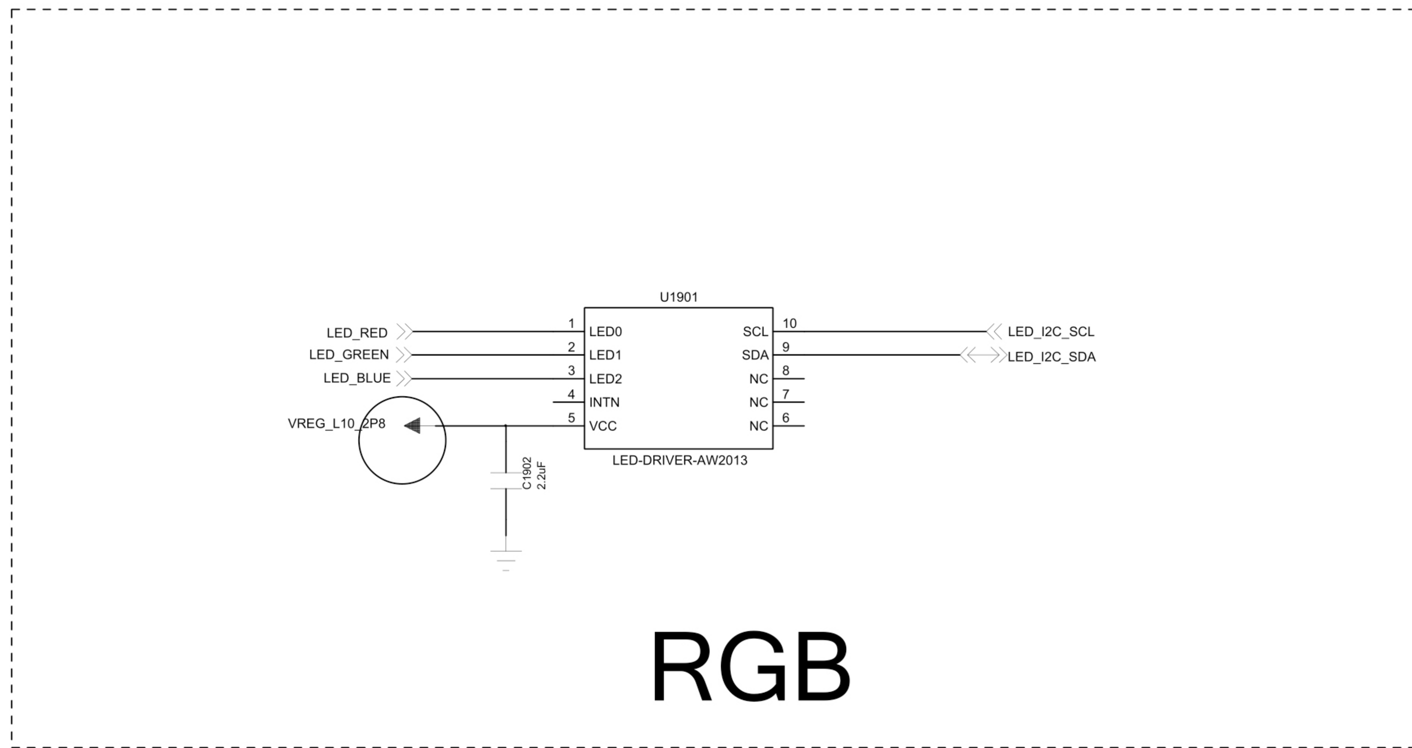

I2C Address	
Write	8B0x8A
Read	8B0x8B

Power on sequence: can be applied in any order

USB/SUB BOARD CONNECTOR

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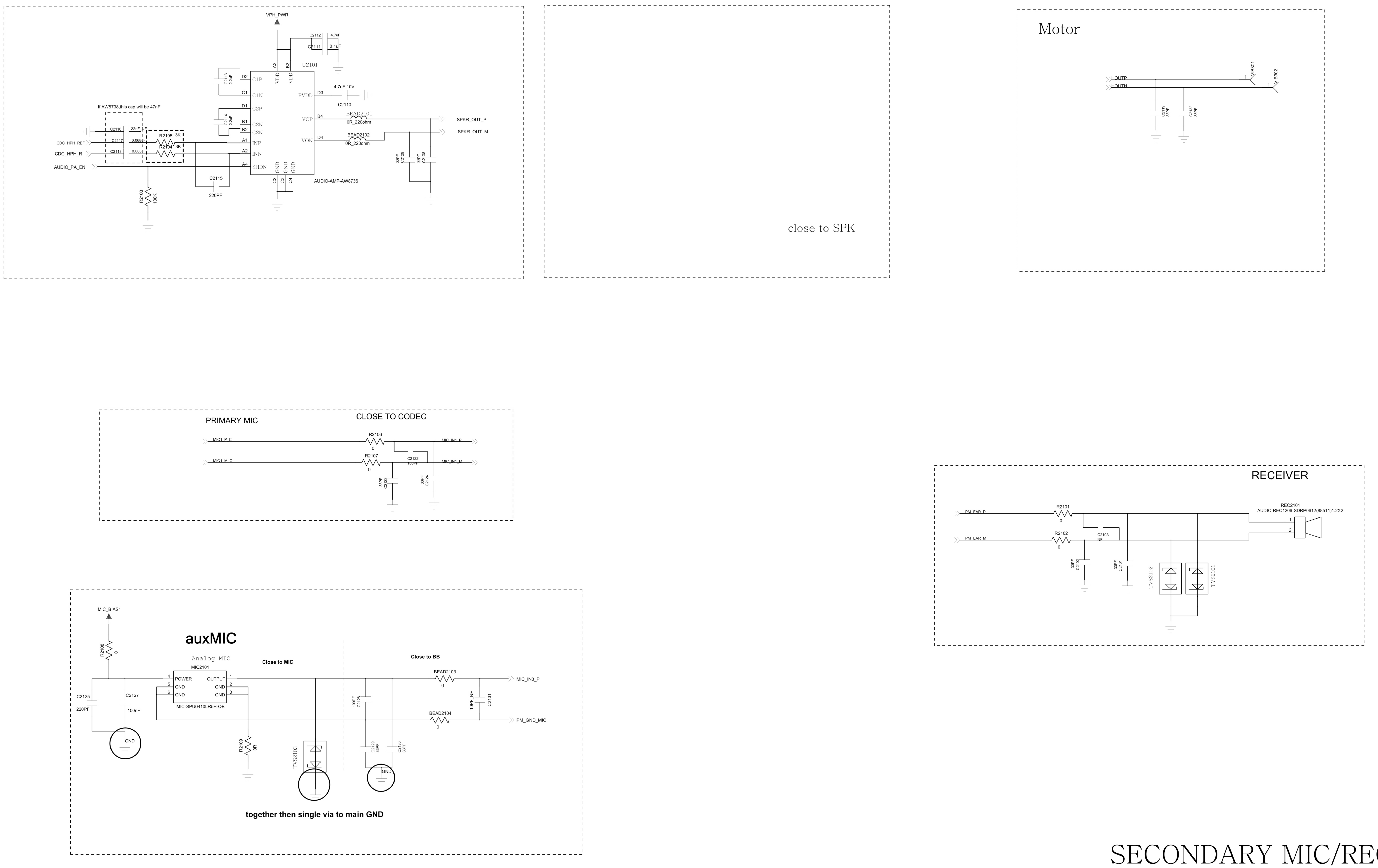

SECONDARY MIC/RECEIVER

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Title恒熙维修助手

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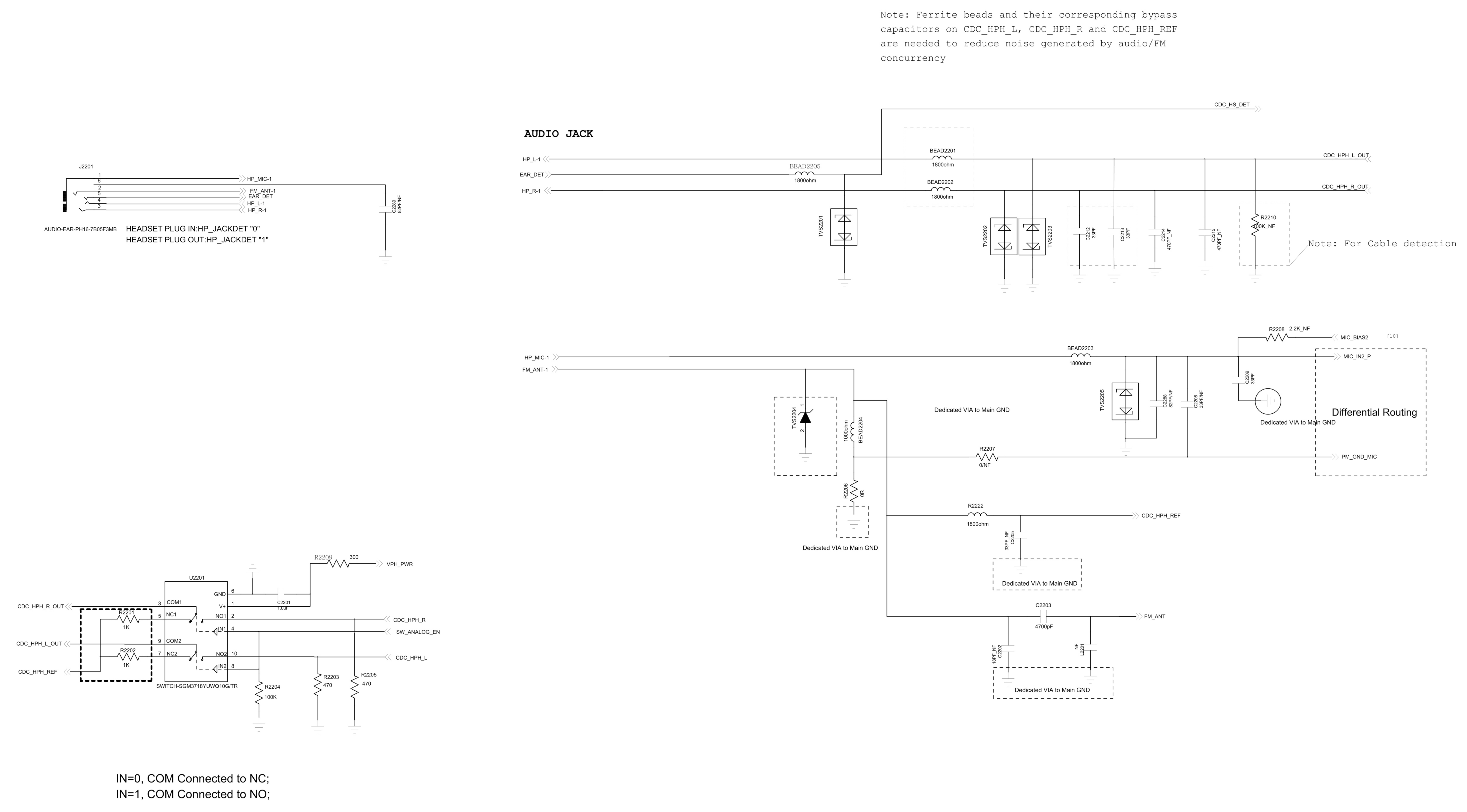
Date: **Sheet** of



EARPHONE

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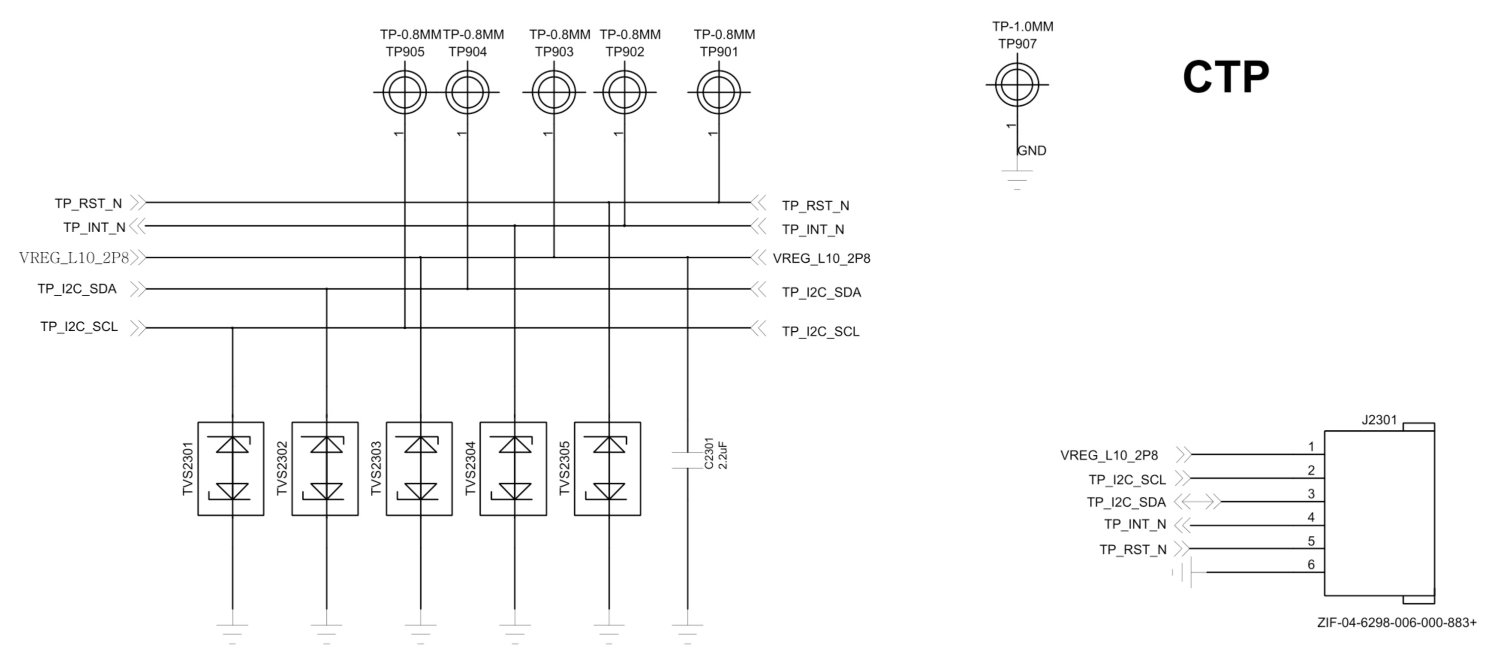
Date: | Sheet | of

CTP

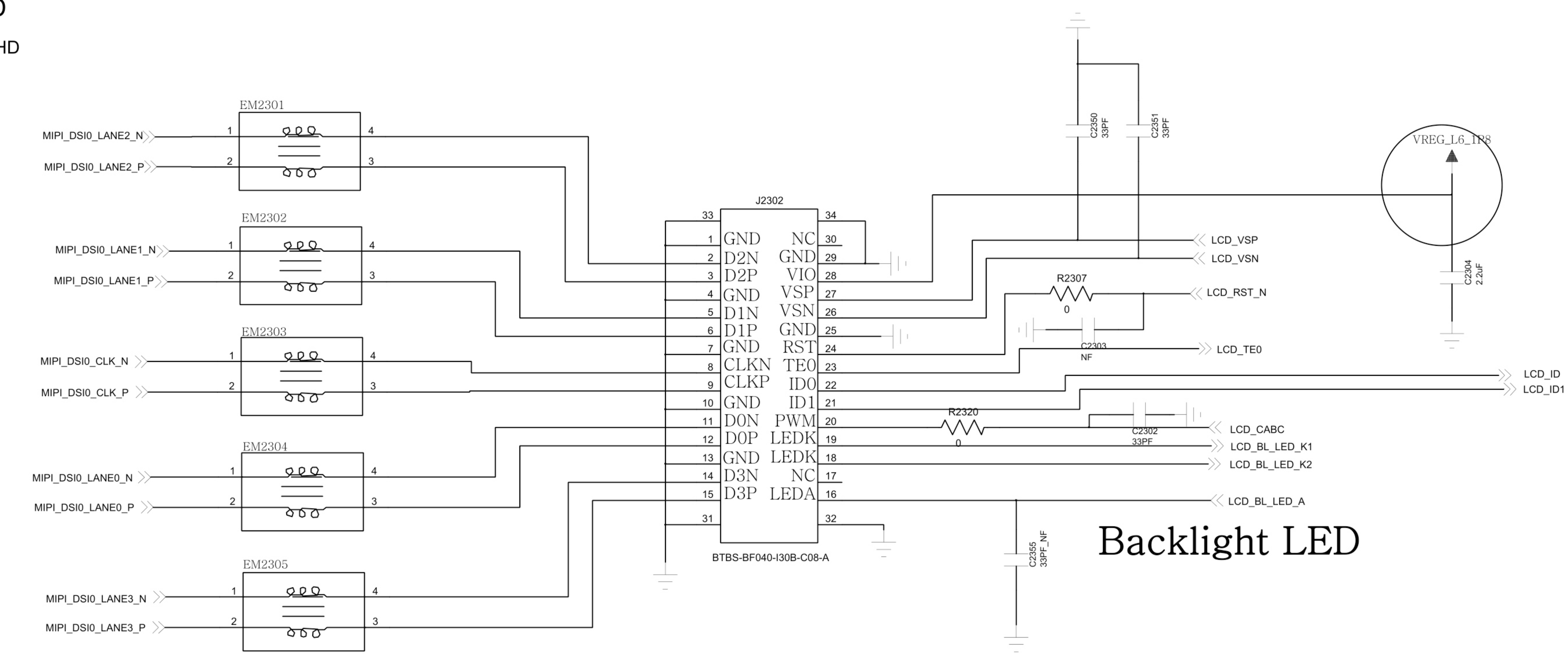


Power on sequence: Single Supply

FocalTech: FT5336	
I2C Address	
Write	0x70 It can be changed by FACTORY.
Read	0x71

LCD

5.0' FHD



Note: If best EMI practices are followed for MIPI CSI/DSI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints.
Extreme care must be taken that no stubs are created by doing so.

LCD interface and backlight

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