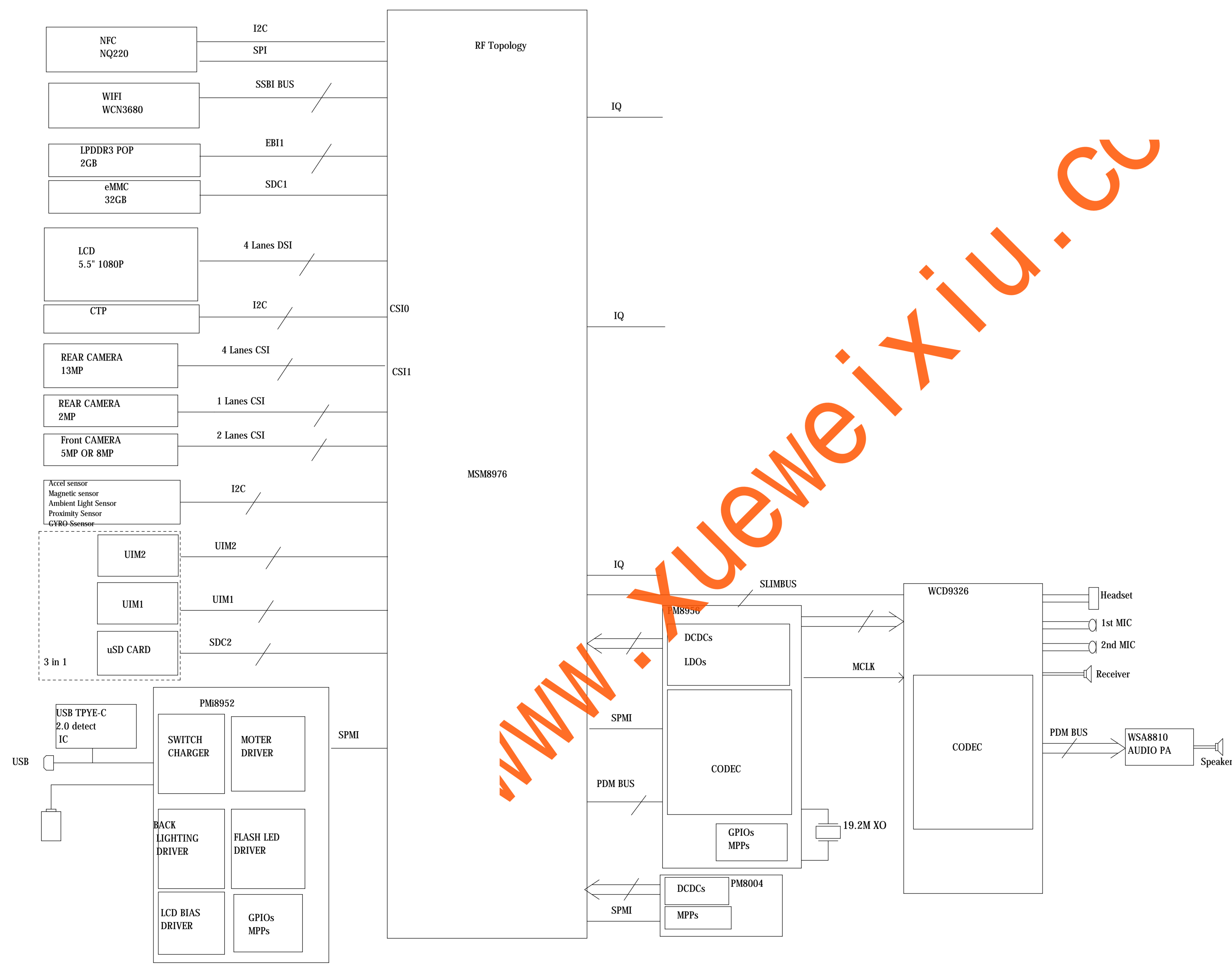


REVISION RECORD			
LT#	ECO NO.	APPROVED	DATE



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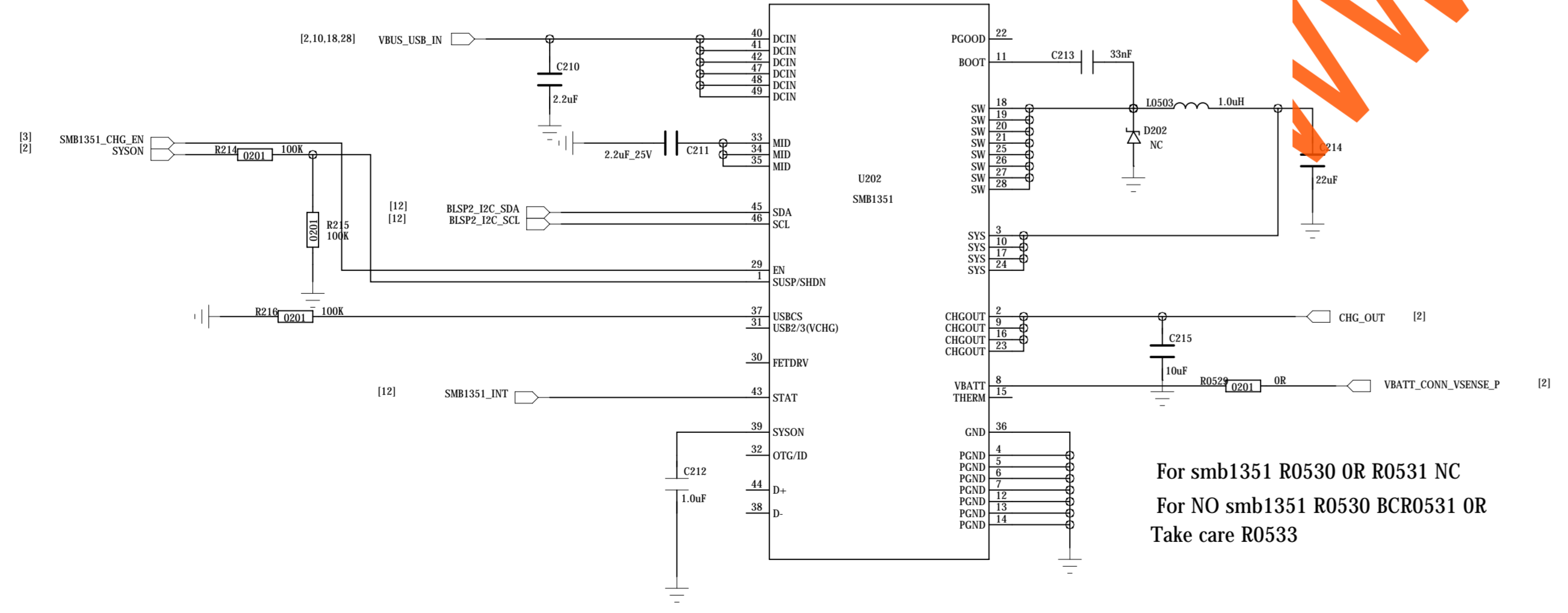
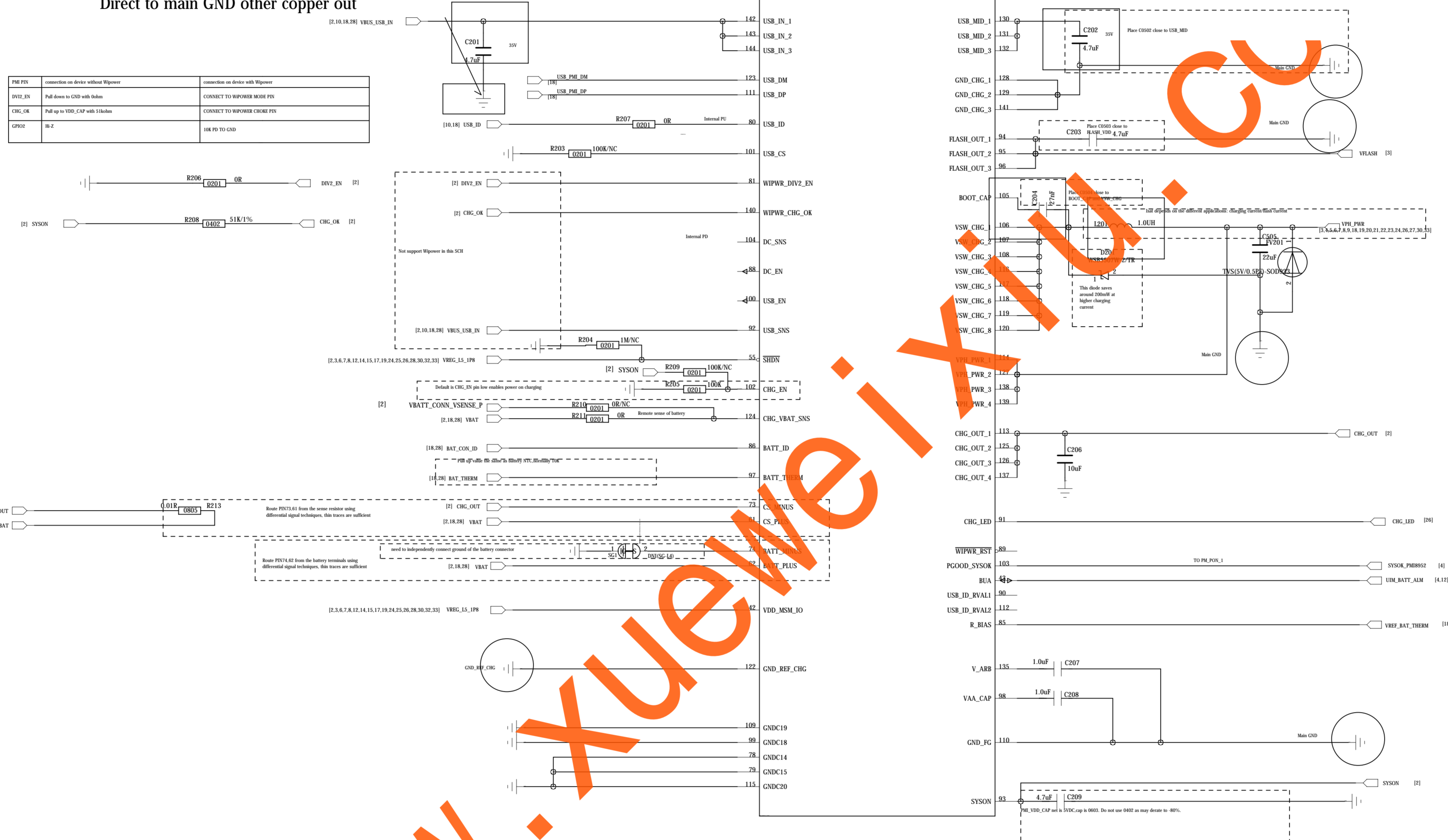
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RELEASED: <Released By>		DATE: <Release Date>		SCALE: <Scale>		SHEET: 1v 34	

REVISION RECORD			
ITR	ECO NO.	APPROVED	DATE

USE C = 4.7uF for Single Charging Config
 USE C = 2.2uF for Single Charging Config

Direct to main GND other copper out

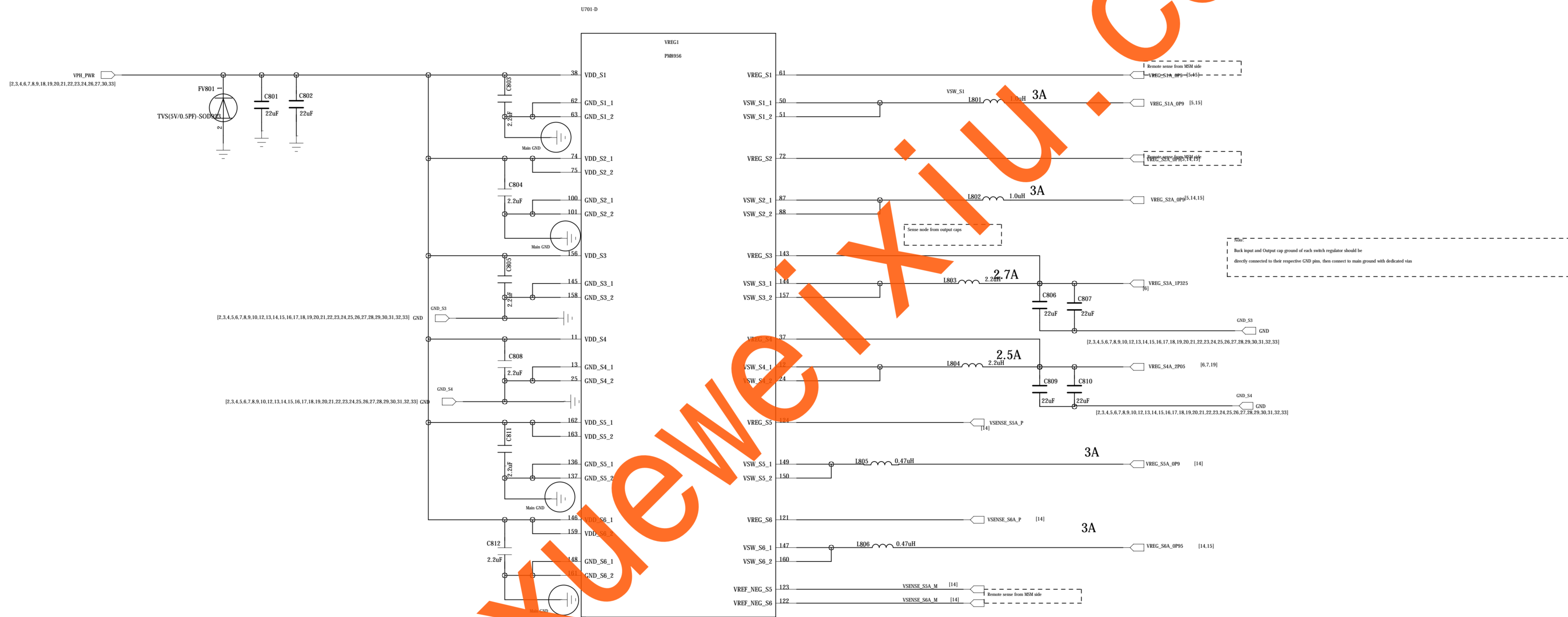
REF ID	Description	Value	Notes
R0533	Resistor	0.1	0.1 Ohm
C202	Capacitor	4.7	4.7uF
C203	Capacitor	2.2	2.2uF
C204	Capacitor	1.0	1.0uF
C205	Capacitor	1.0	1.0uF
C206	Capacitor	1.0	1.0uF
C207	Capacitor	1.0	1.0uF
C208	Capacitor	1.0	1.0uF
C209	Capacitor	1.0	1.0uF
C210	Capacitor	1.0	1.0uF
C211	Capacitor	1.0	1.0uF
C212	Capacitor	1.0	1.0uF
C213	Capacitor	1.0	1.0uF
C214	Capacitor	1.0	1.0uF
C215	Capacitor	1.0	1.0uF
C216	Capacitor	1.0	1.0uF
C217	Capacitor	1.0	1.0uF
C218	Capacitor	1.0	1.0uF
C219	Capacitor	1.0	1.0uF
C220	Capacitor	1.0	1.0uF
C221	Capacitor	1.0	1.0uF
C222	Capacitor	1.0	1.0uF
C223	Capacitor	1.0	1.0uF
C224	Capacitor	1.0	1.0uF
C225	Capacitor	1.0	1.0uF
C226	Capacitor	1.0	1.0uF
C227	Capacitor	1.0	1.0uF
C228	Capacitor	1.0	1.0uF
C229	Capacitor	1.0	1.0uF
C230	Capacitor	1.0	1.0uF
C231	Capacitor	1.0	1.0uF
C232	Capacitor	1.0	1.0uF
C233	Capacitor	1.0	1.0uF
C234	Capacitor	1.0	1.0uF
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C245	Capacitor	1.0	1.0uF
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C249	Capacitor	1.0	1.0uF
C250	Capacitor	1.0	1.0uF
C251	Capacitor	1.0	1.0uF
C252	Capacitor	1.0	1.0uF
C253	Capacitor	1.0	1.0uF
C254	Capacitor	1.0	1.0uF
C255	Capacitor	1.0	1.0uF
C256	Capacitor	1.0	1.0uF
C257	Capacitor	1.0	1.0uF
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C295	Capacitor	1.0	1.0uF
C296	Capacitor	1.0	1.0uF
C297	Capacitor	1.0	1.0uF
C298	Capacitor	1.0	1.0uF
C299	Capacitor	1.0	1.0uF
C300	Capacitor	1.0	1.0uF



For smb1351 R0530 OR R0531 NC
 For NO smb1351 R0530 BCR0531 OR
 Take care R0533

DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: <Title>	
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REVISION RECORD			
LTB	ECO NO.	APPROVED	DATE

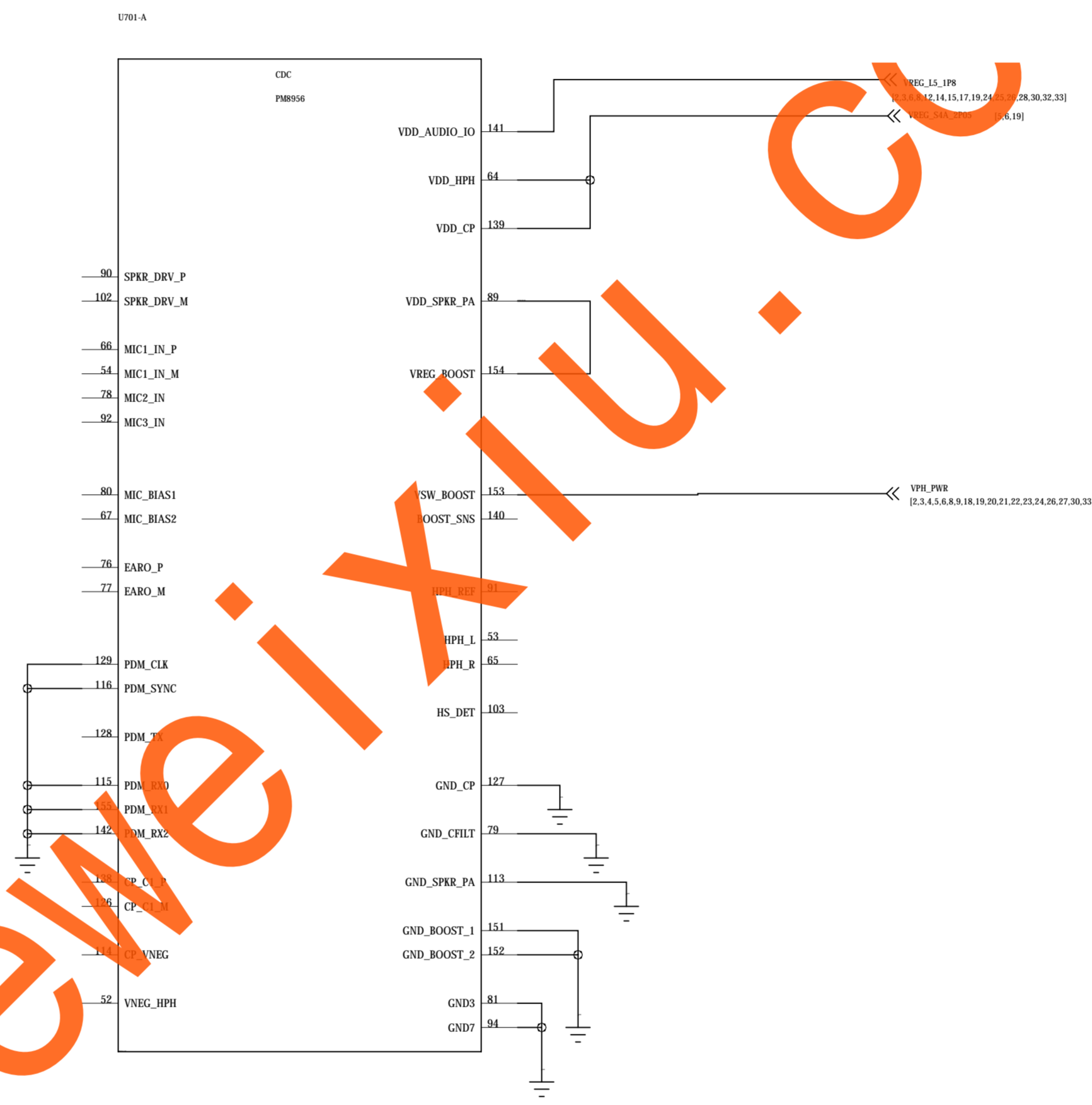


COMPANY: <Company Name>

TITLE: <Title>

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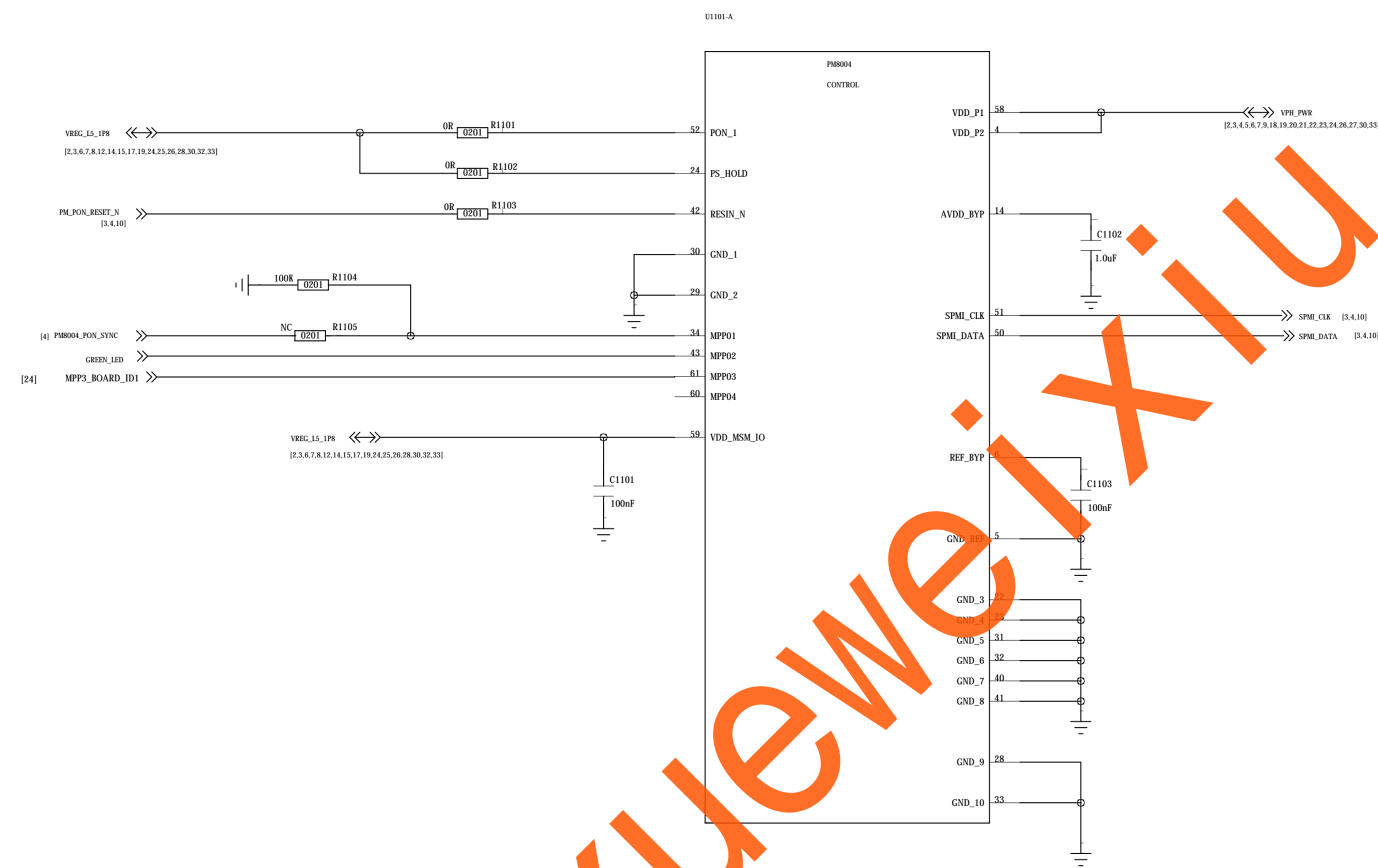


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Title			

COMPANY: <Company Name>			
TITLE: <Title>			
DRAWN:	DATE:	CODE:	SIZE:
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CHECKED:	DATE:	DRAWING NO.:	REV.:
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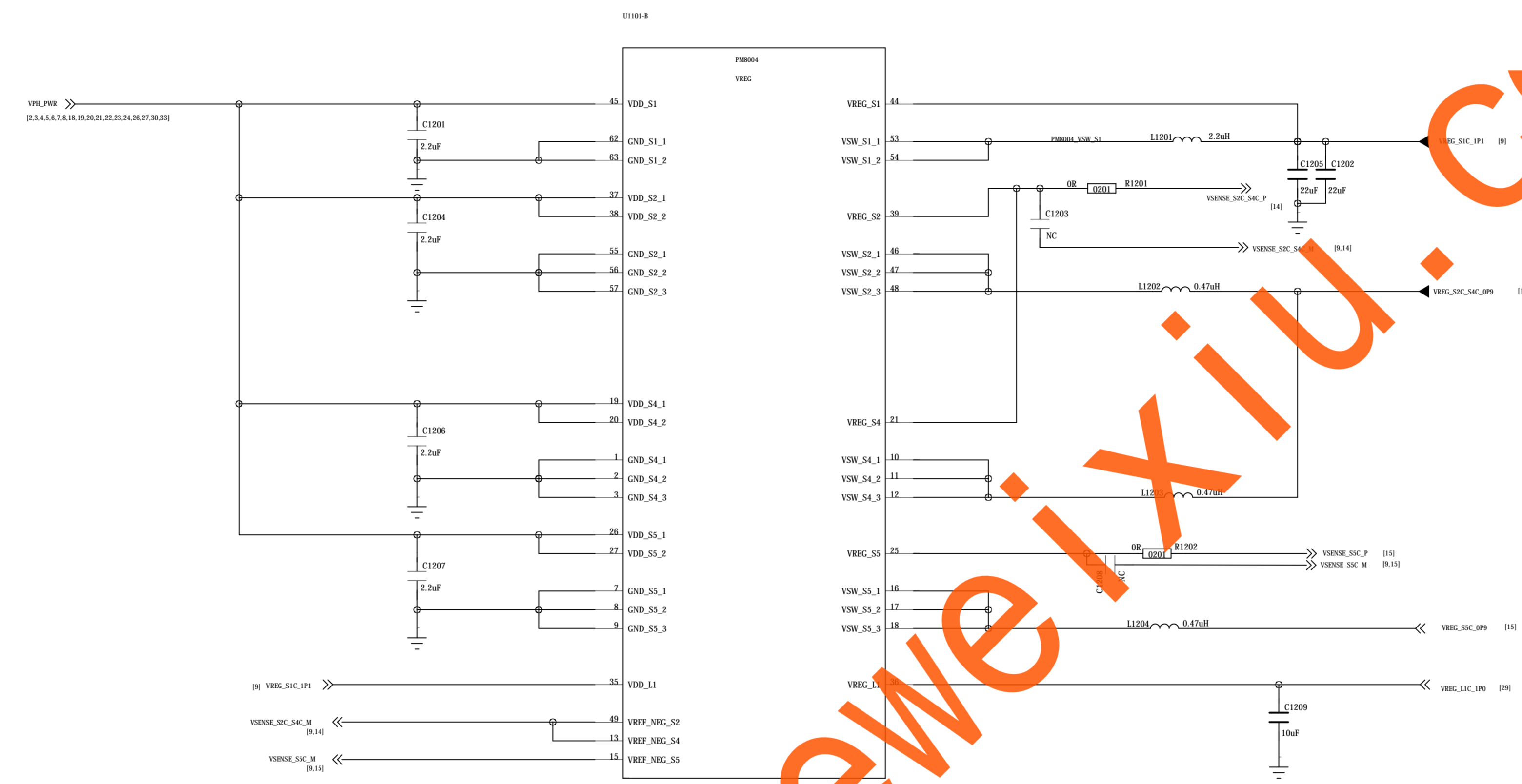
REVISION RECORD			
LTW	REV NO	APPROVED	DATE



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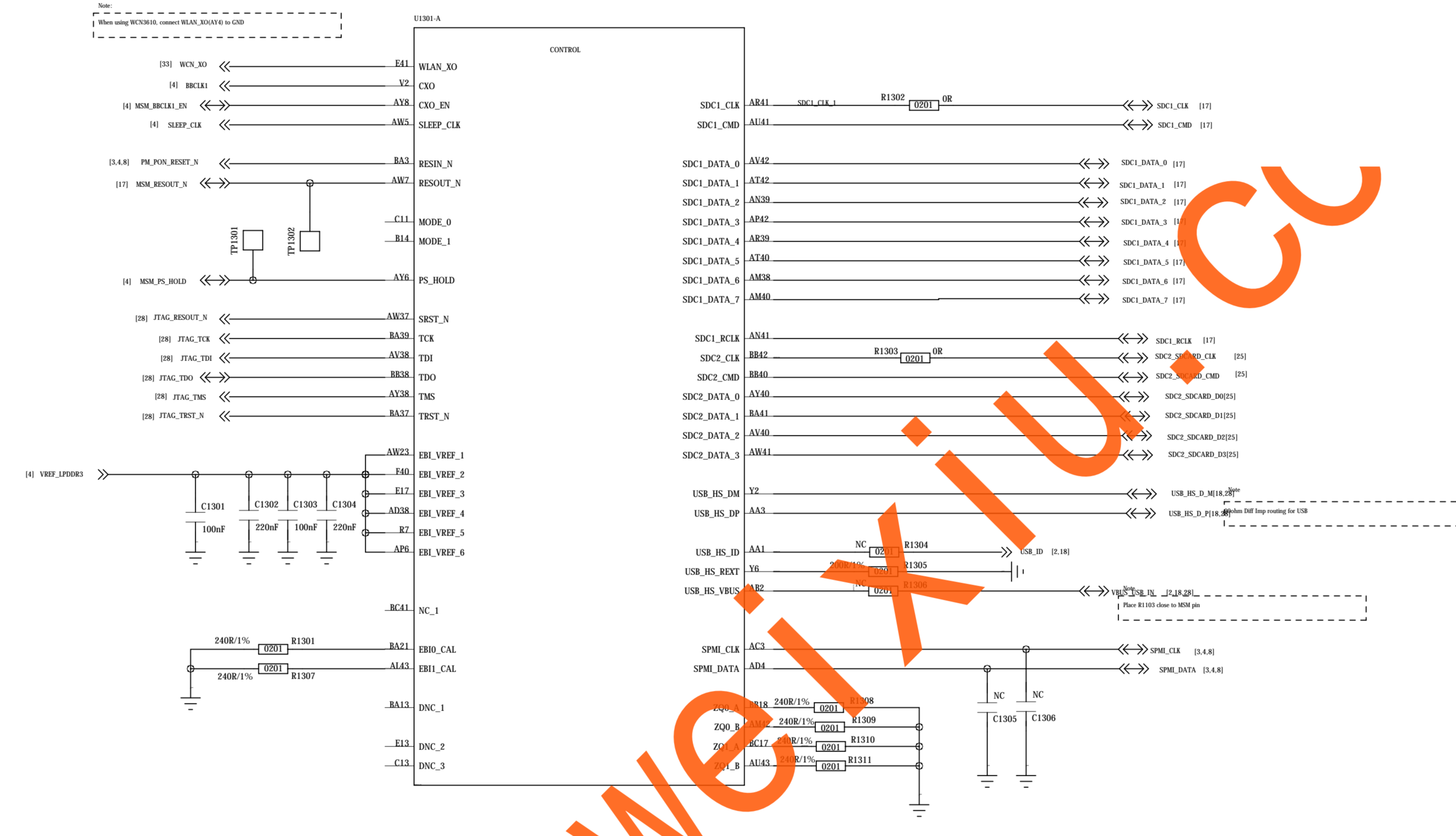
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REVISION RECORD			
ITER	ECO NO.	APPROVAL	DATE



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REVISION RECORD			
NO.	REV. NO.	APPROVED	DATE



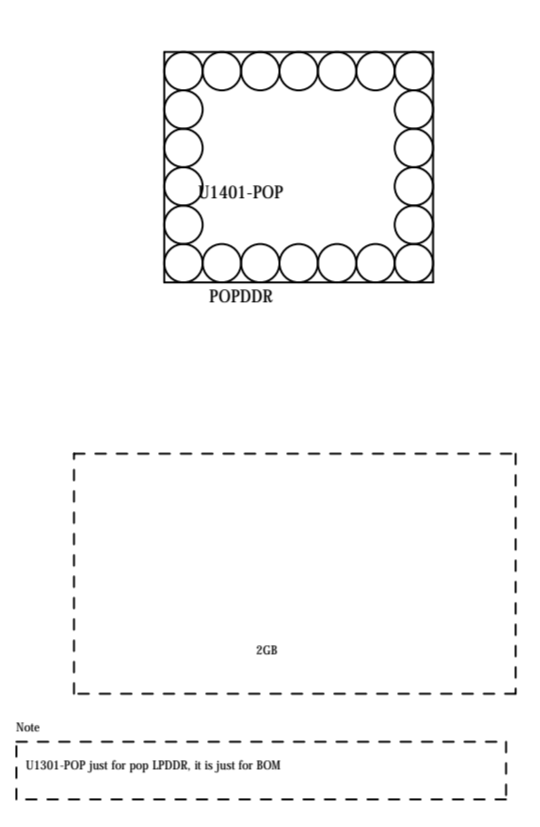
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5	05	2014.01.01	
6	06	2014.01.01	
7	07	2014.01.01	
8	08	2014.01.01	
9	09	2014.01.01	
10	10	2014.01.01	
11	11	2014.01.01	
12	12	2014.01.01	
13	13	2014.01.01	
14	14	2014.01.01	
15	15	2014.01.01	
16	16	2014.01.01	
17	17	2014.01.01	
18	18	2014.01.01	
19	19	2014.01.01	
20	20	2014.01.01	

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DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>	
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REVISION RECORD			
REV	ECO NO	APPROVED	DATE

POPDDR

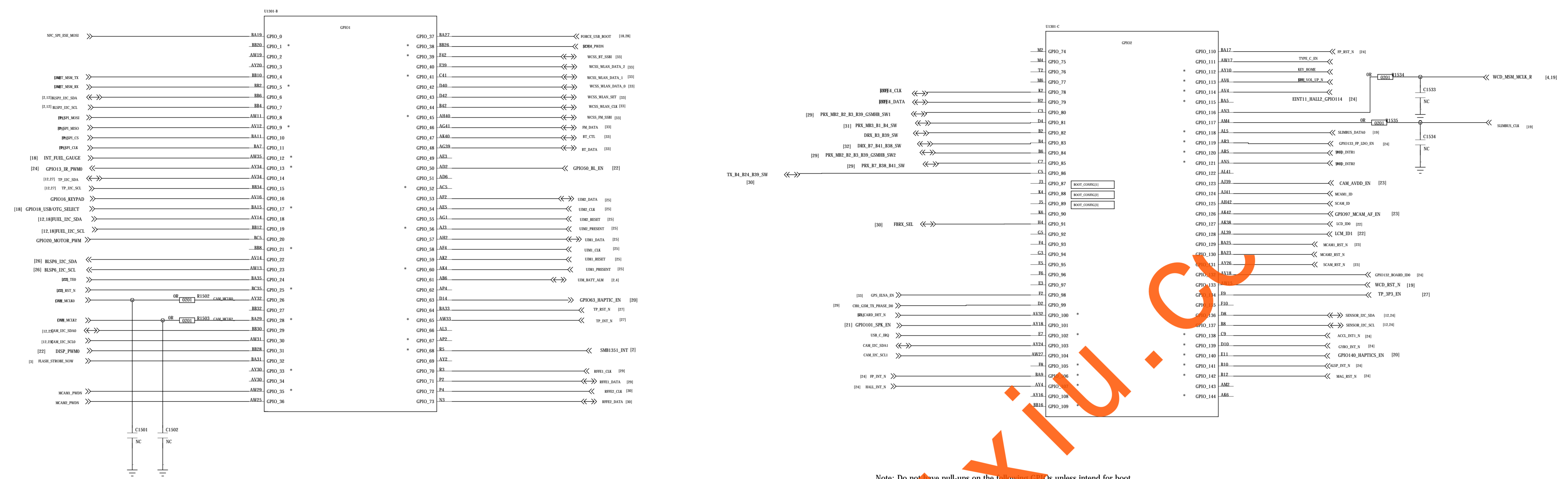


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DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>			
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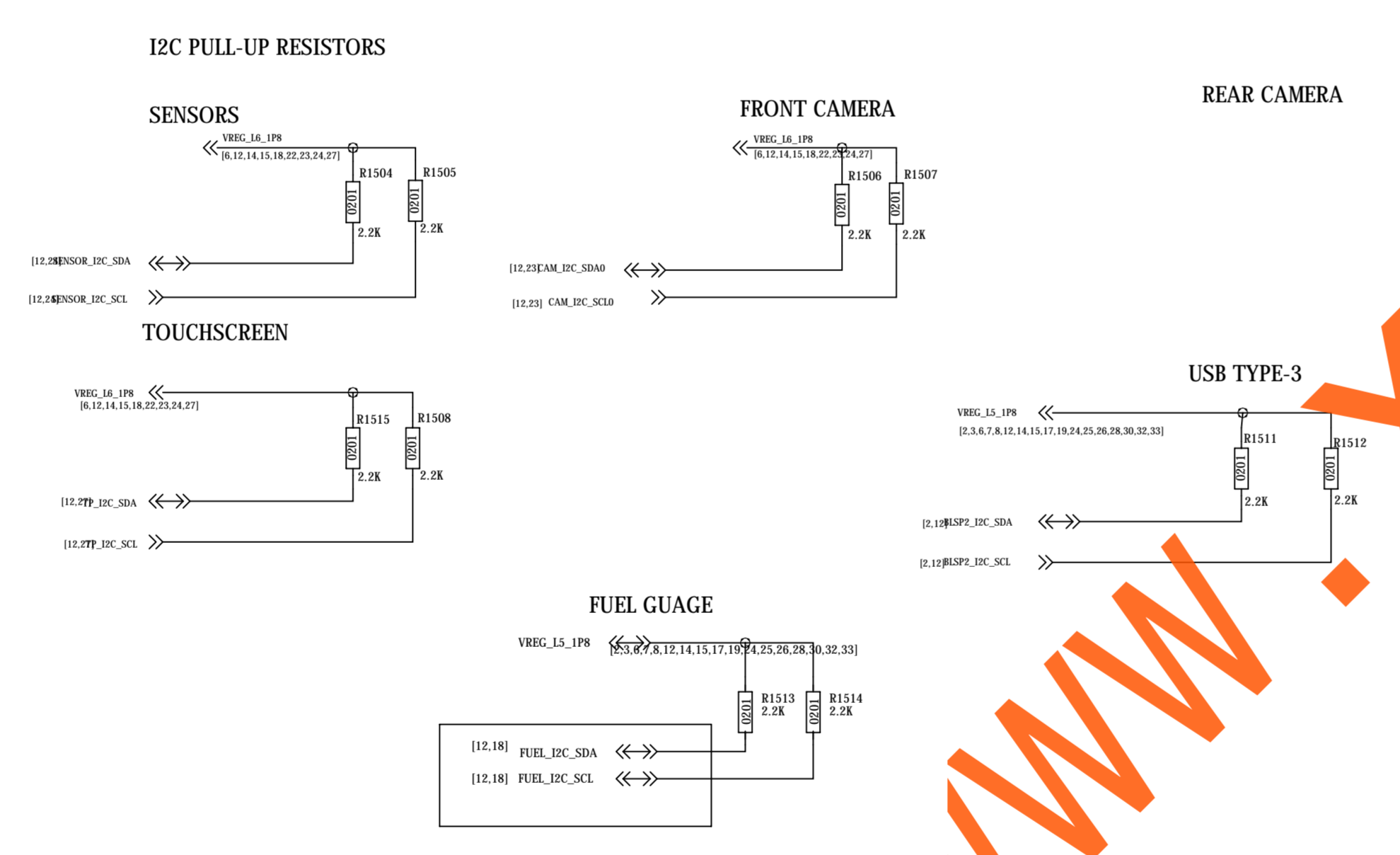
REVISION RECORD			
ITER	ECO NO.	APPROVED	DATE

Note: Asterisks (*) indicate modem power management (MPM) wake-up pins



Note: Do not have pull-ups on the following GPIOs unless intend for boot or secure-boot related configuration
GPIO_87, 88, 89

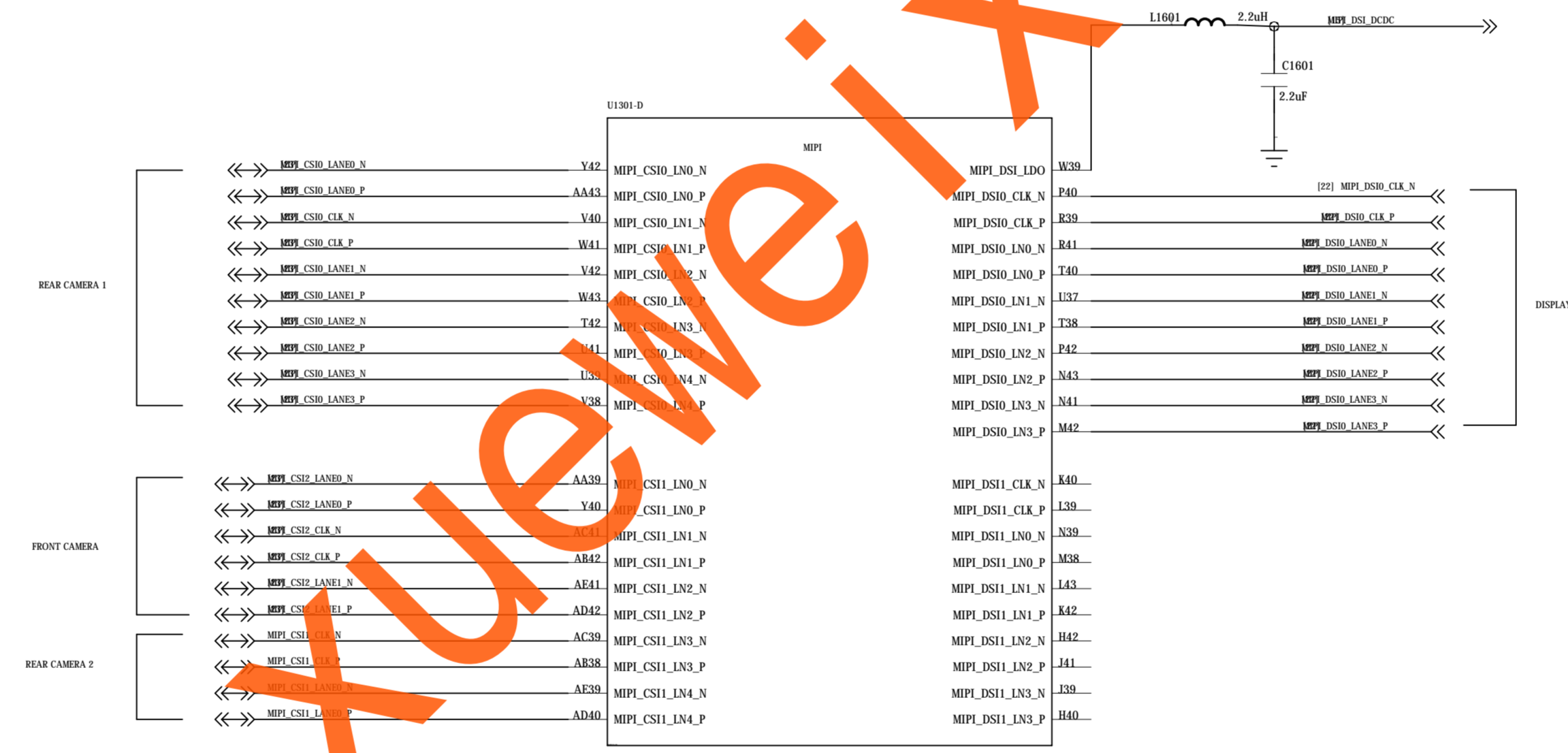
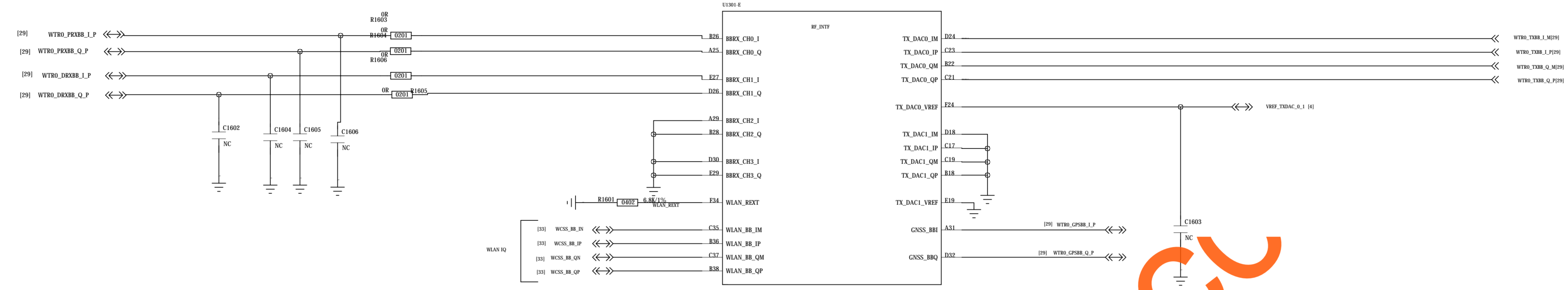
GPIO	WAKE_CONFIG	WAKE_CONFIG	WAKE_CONFIG
GPIO_87	WAKE_CONFIG	WAKE_CONFIG	WAKE_CONFIG
GPIO_88	WAKE_CONFIG	WAKE_CONFIG	WAKE_CONFIG
GPIO_89	WAKE_CONFIG	WAKE_CONFIG	WAKE_CONFIG



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DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: <Title>	
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RELEASED: <Released By>		DATE: <Release Date>		DRAWING NO. <Drawing Number> Revision <Revision>	
SCALE: <Scale>				SHEET 12 34	

REVISION RECORD			
ITEM	REV. NO.	APPROVED	DATE



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COMPANY: <Company Name>			
TITLE: <Title>			
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REVISION RECORD			
ITER	REV NO.	APPROVED	DATE



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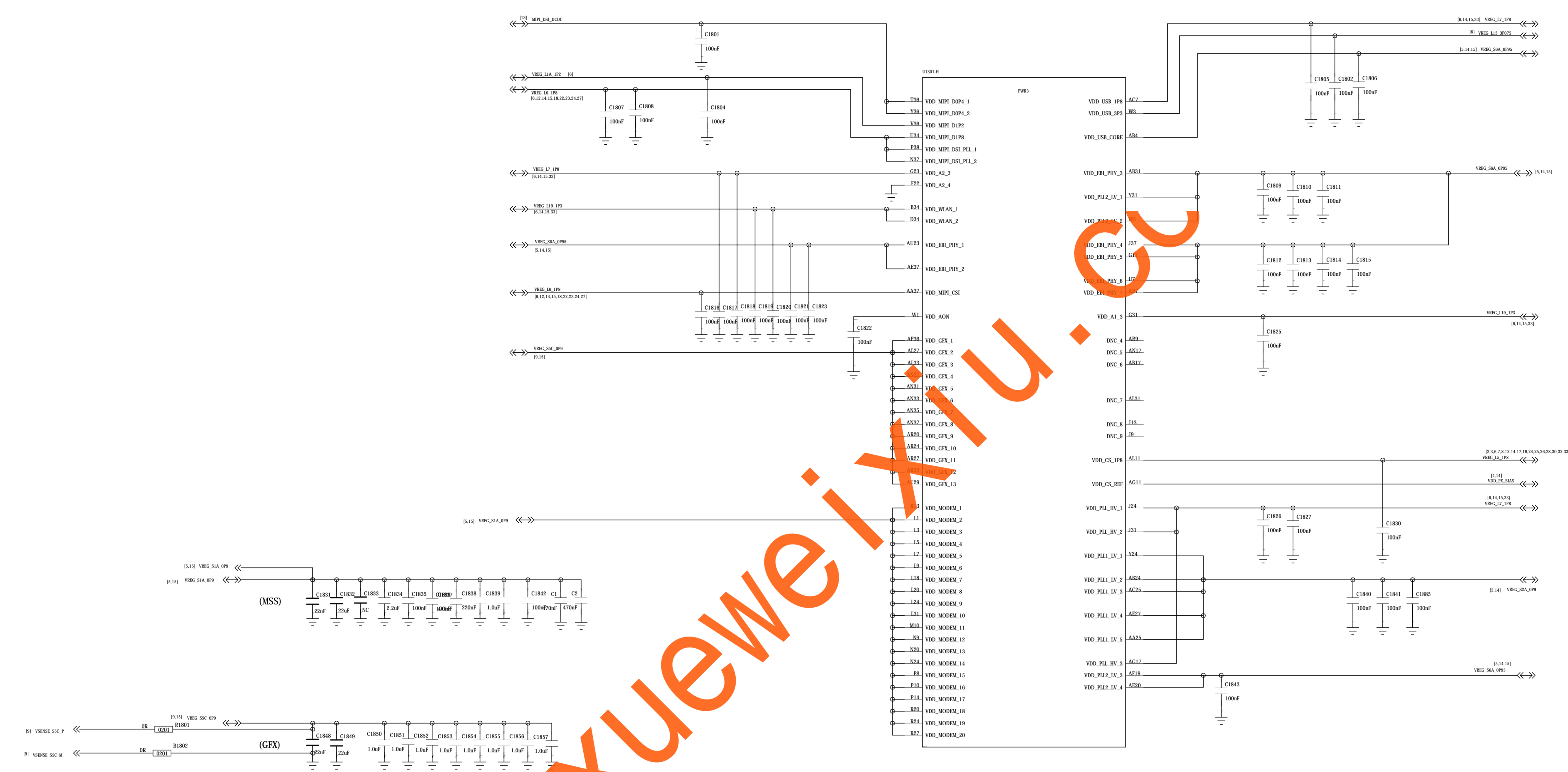
D
C
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D
C
B
A

This is preliminary version, for reference only

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REVISION RECORD			
LTZ	REV NO	APPROVED	DATE



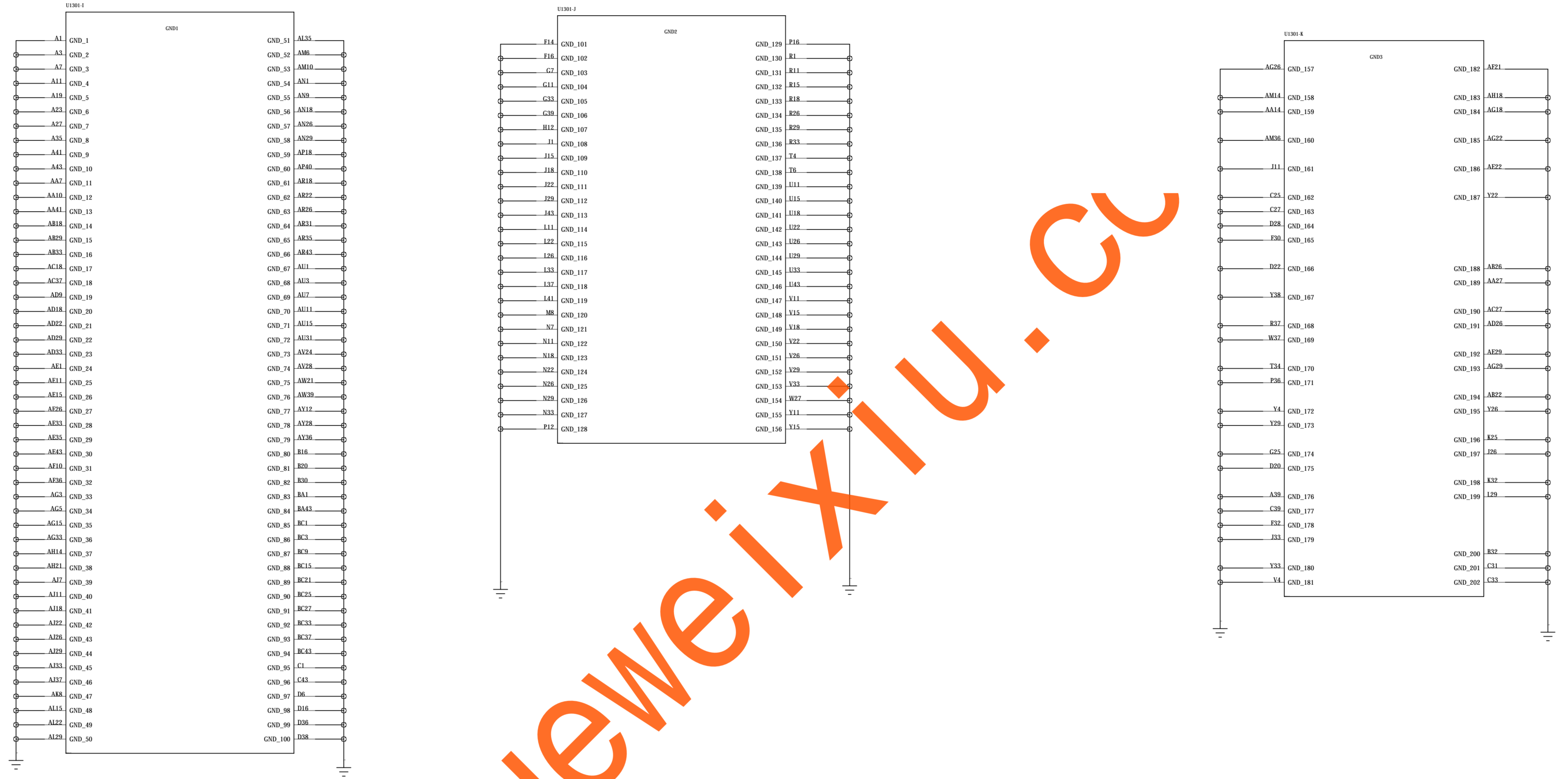
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This is preliminary version, for reference only

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COMPANY				
<Company Name>				
TITLE				
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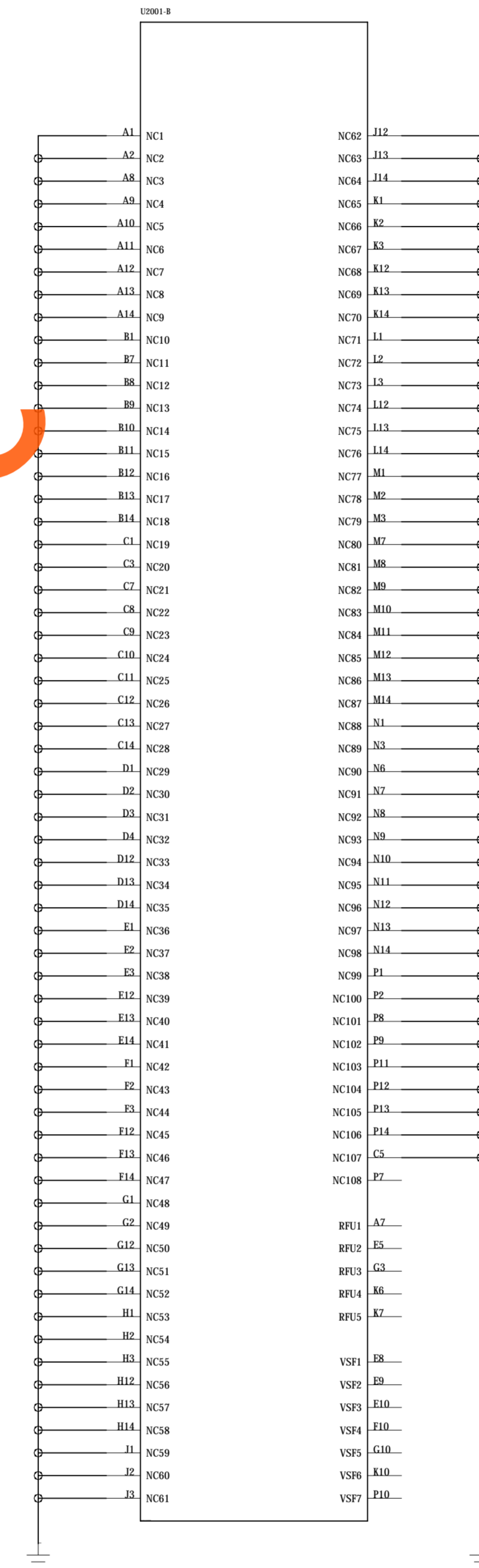
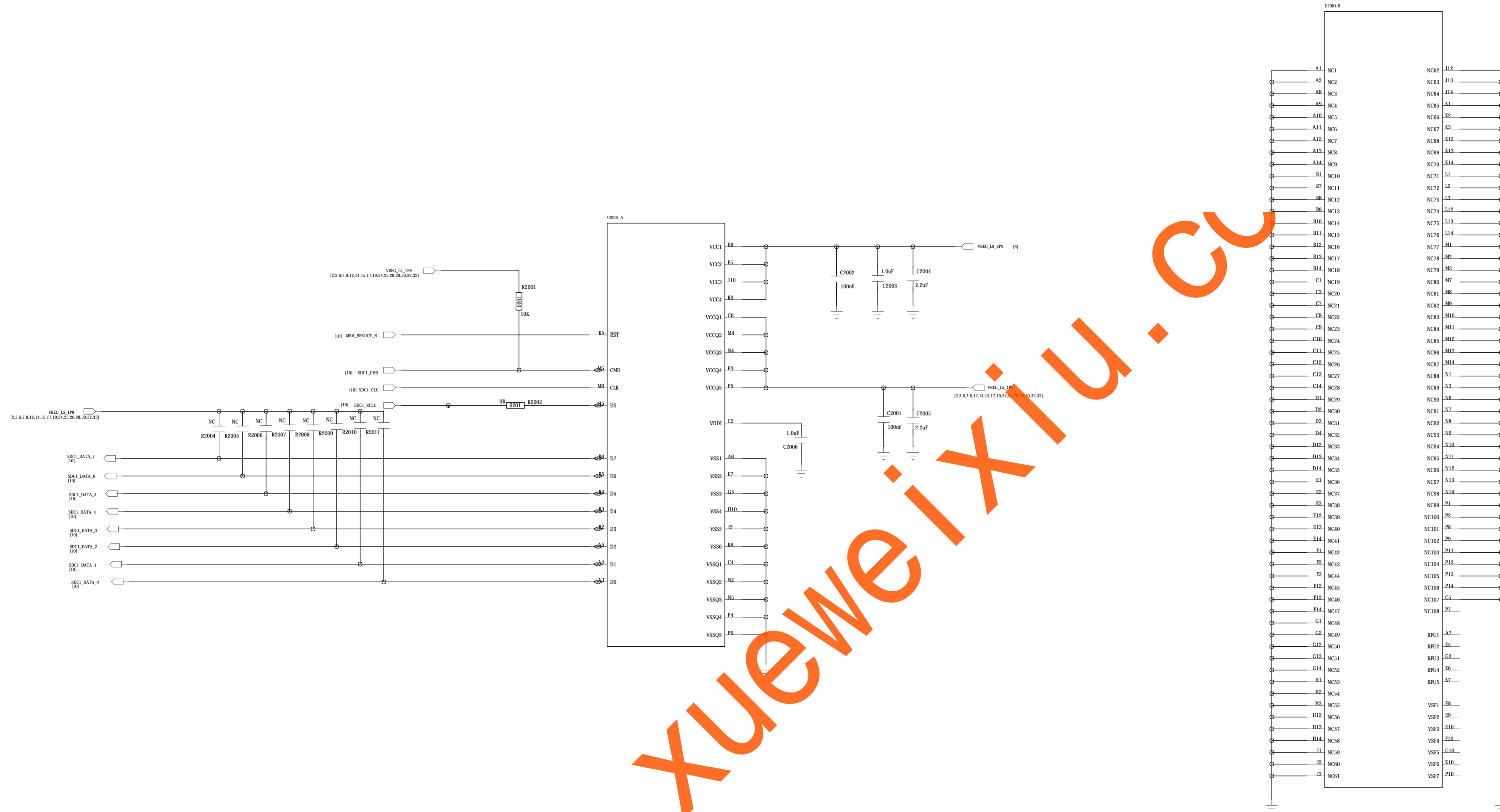
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ITR	ECO NO.	APPROVAL	DATE



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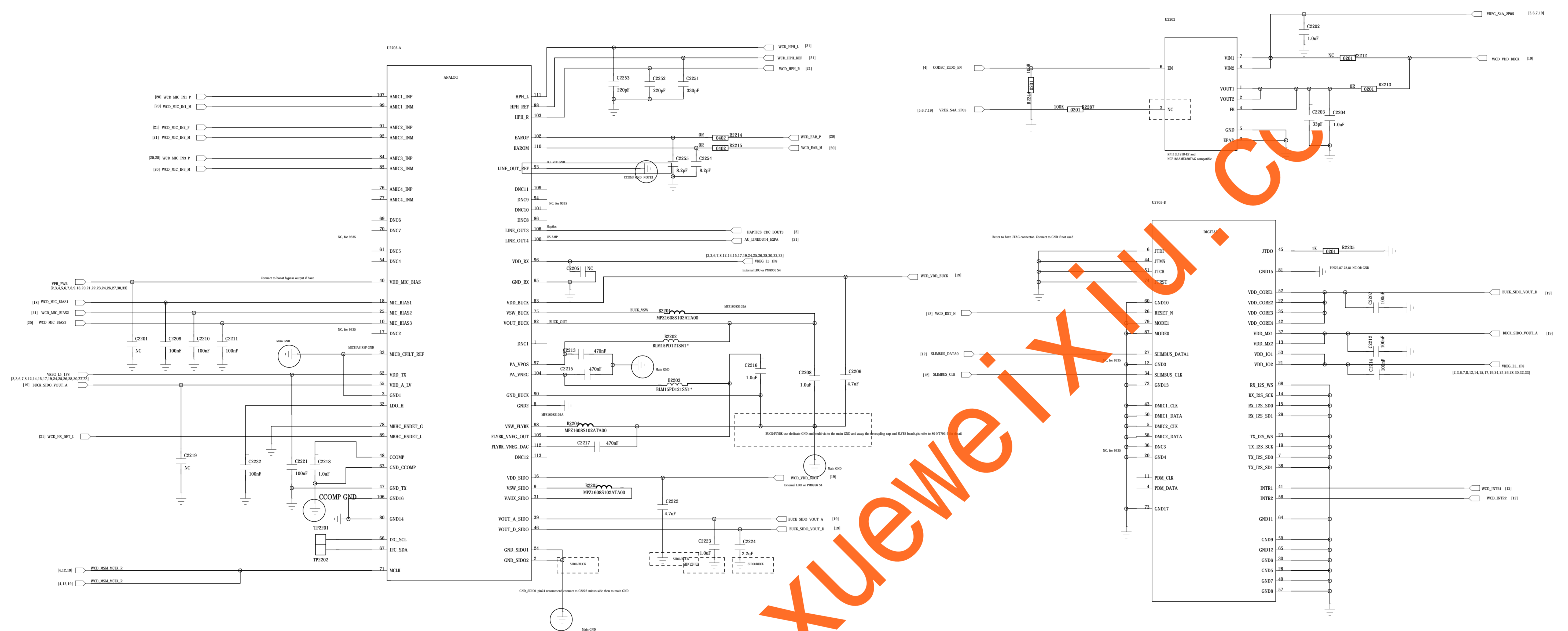
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REVISION RECORD			
LTN	ECO NO.	APPROVED	DATE



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REVISION RECORD			
ITER	REV NO.	APPROVED	DATE

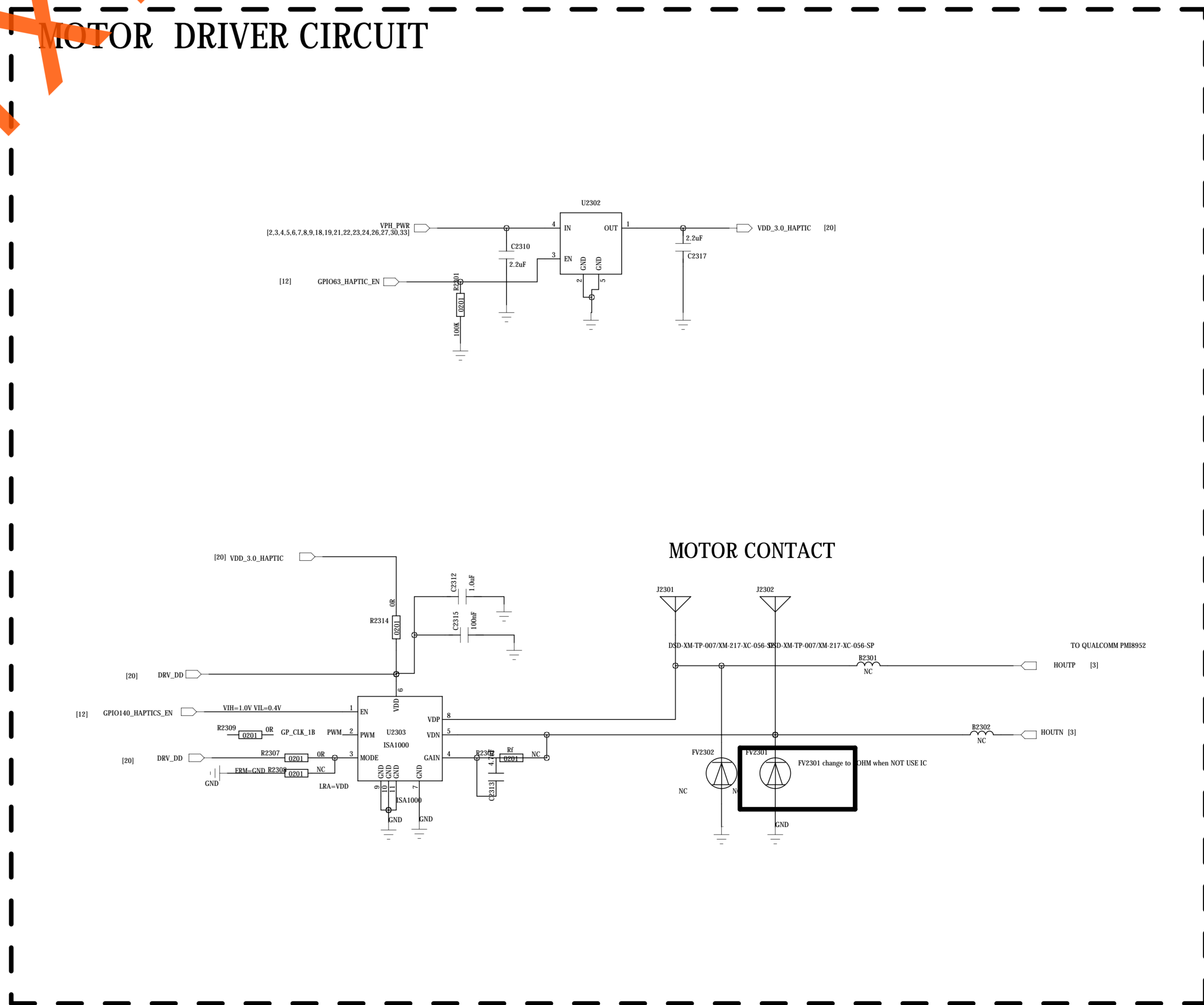
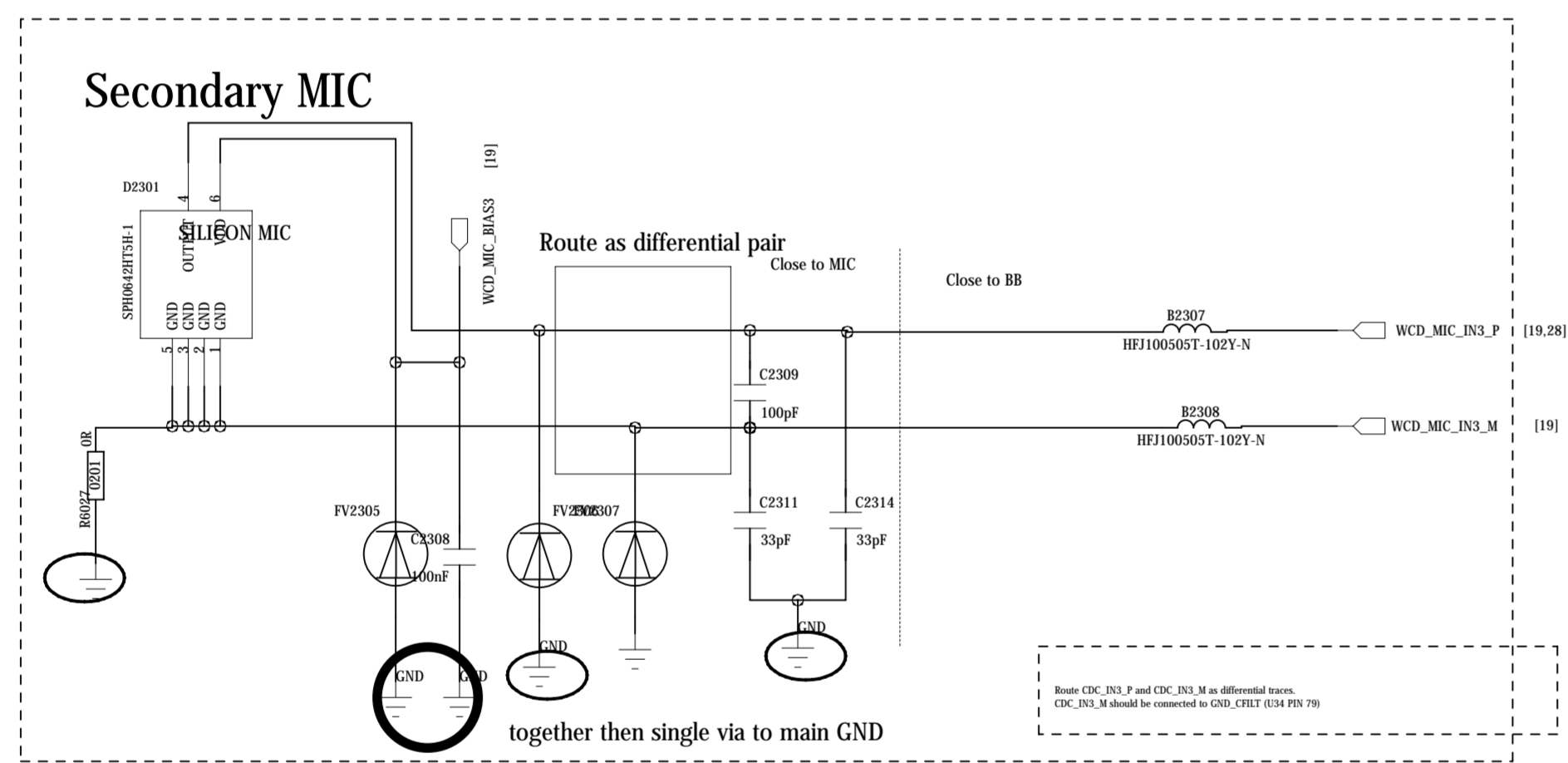
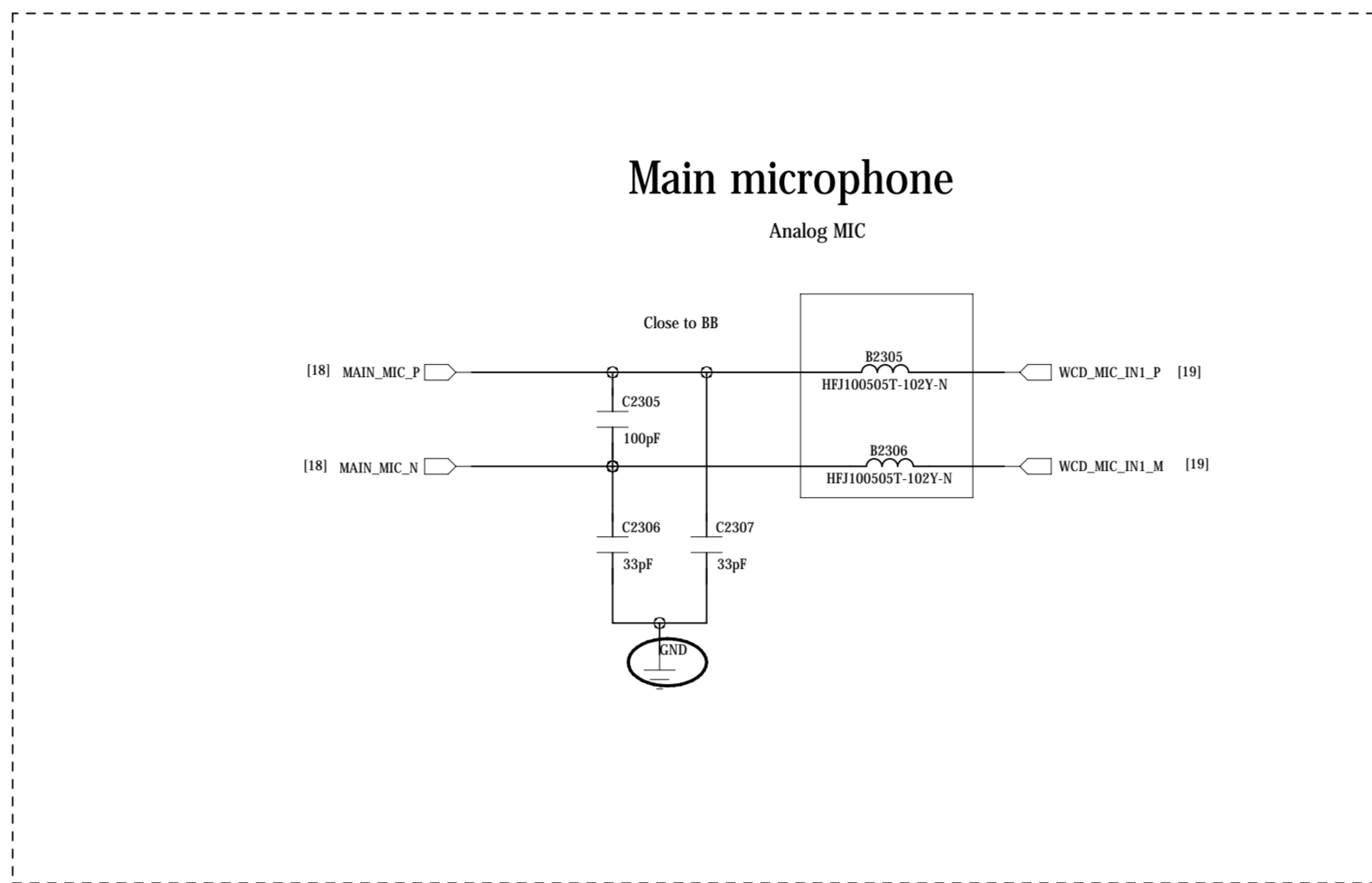
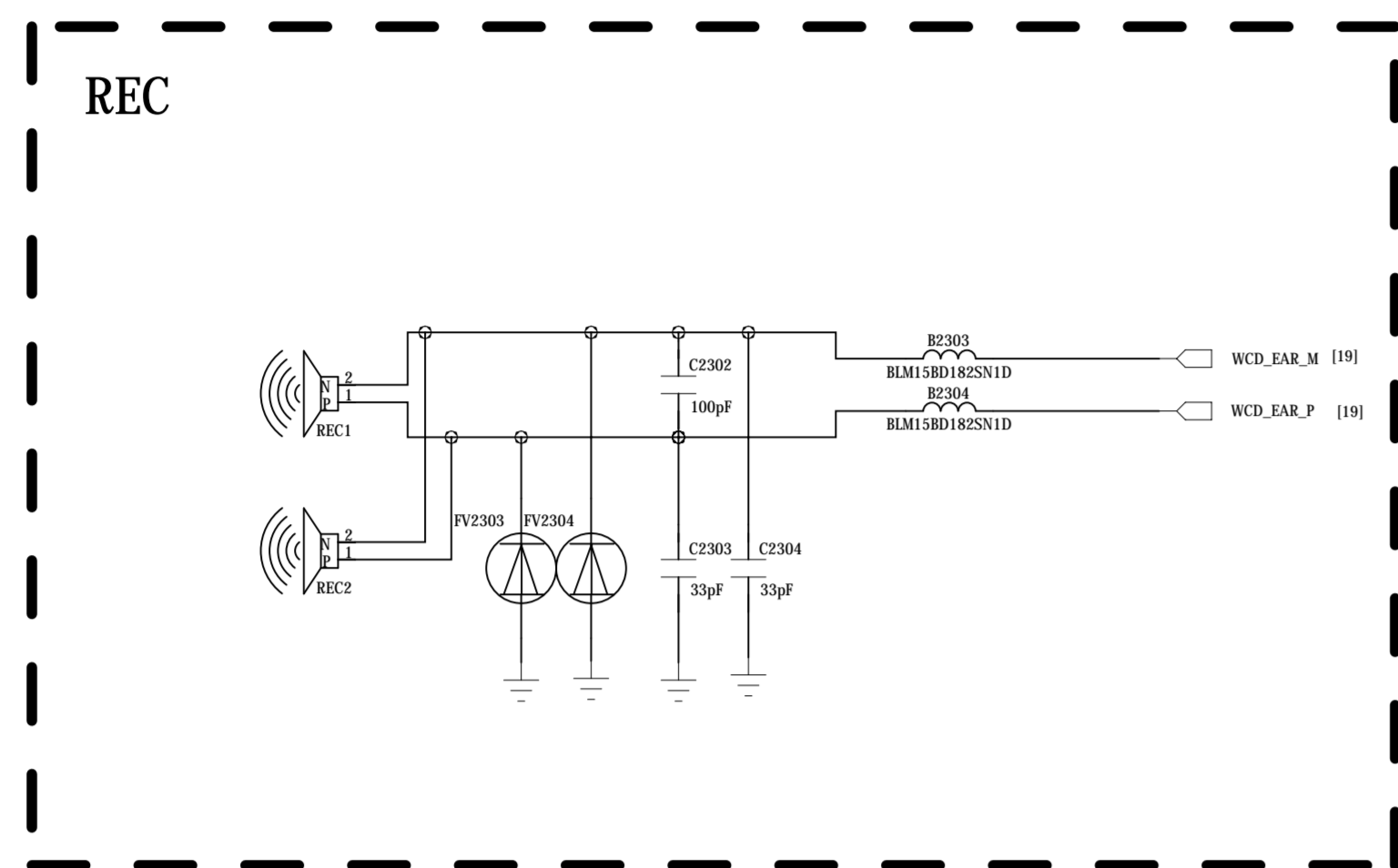


NOTE 4 :The line output ground reference sense.LINE_OUT_REF is only for LINE_OUT_1and LINE_OUT_2 and only required for single-ended connection made off board

Rout LINE_OUT_REF as a separate trace all the way to the off board connector's ground pin.
Connect LINE_OUT_REF to the CCOMP GND when the outputs are used as differential pair or kept on board or not used

COMPANY: <Company Name>				
TITLE: <Title>				
DRAWN: <Drawn By>	DATE: <Drawn Date>	CODE: <Code>	SIZE: A0	DRAWING NO: <Drawing Number>
CHECKED: <Checked By>	DATE: <Checked Date>	SCALE: <Scale>	REV: 10	34
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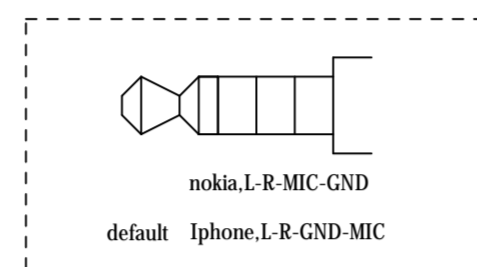
REVISION RECORD			
LTN	ECO NO.	APPROVED	DATE



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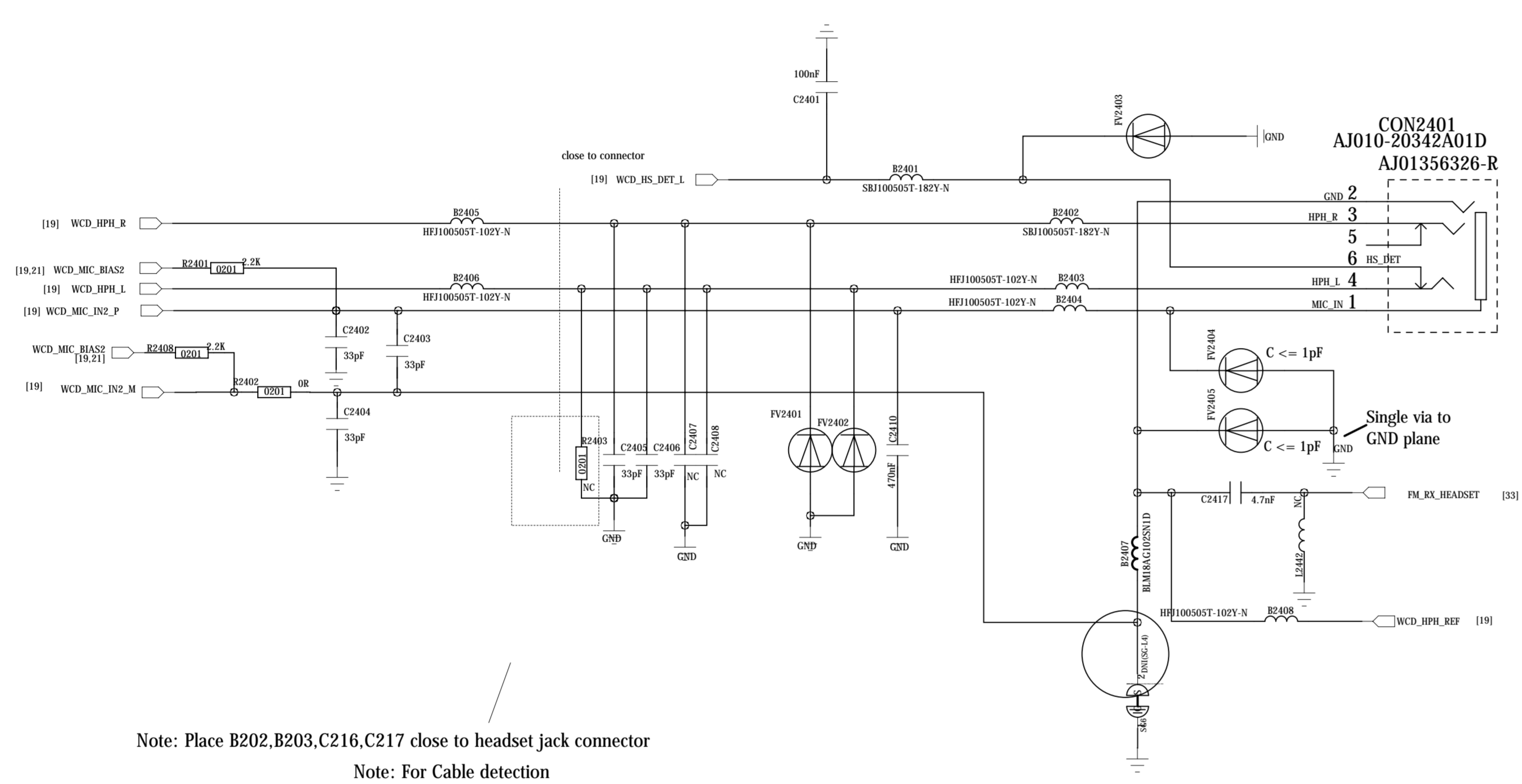
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SHEET: 20 / 34	

REVISION RECORD			
STR	ECO NO.	APPROVED	DATE

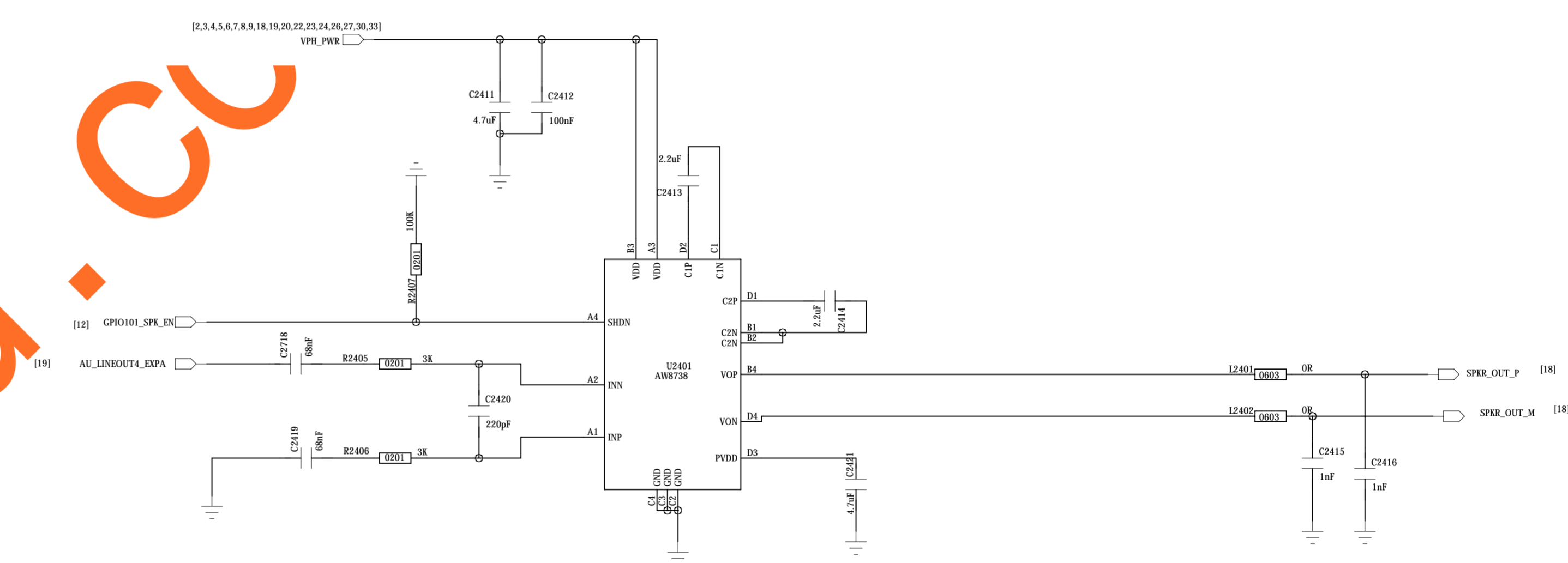


Earphone Audio

Note: Ferrite beads and their corresponding bypass capacitors on CDC_HPH_L_P, CDC_HPH_L_M and CDC_HPH_REF are needed to reduce noise generated by audio/FM concurrency



SPEAKER



C8361 when use AW8145 please use 1uF CAP; when use AW8155 or AW8155A please use 0 ohm res

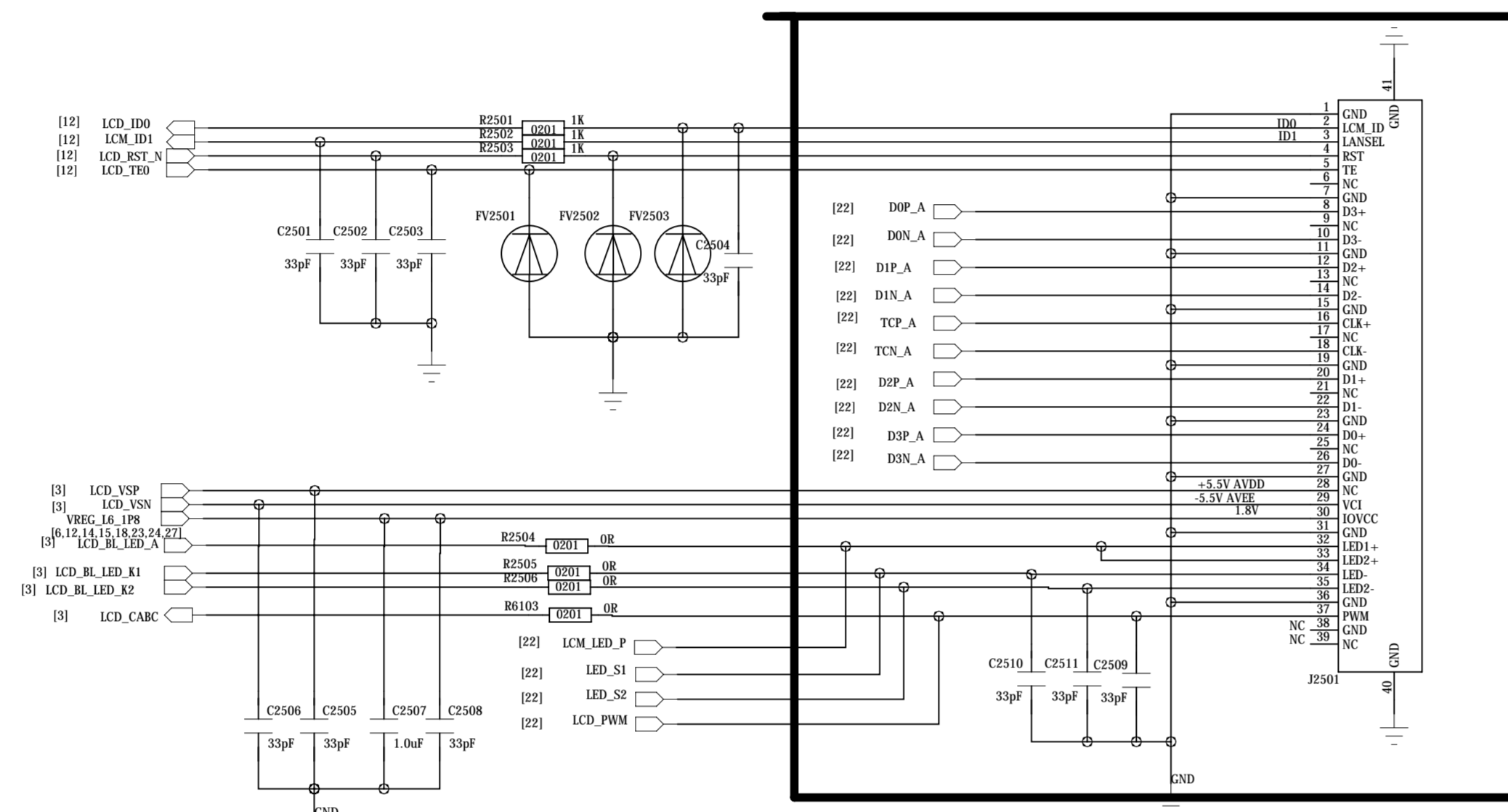
Earphone MICPHONE



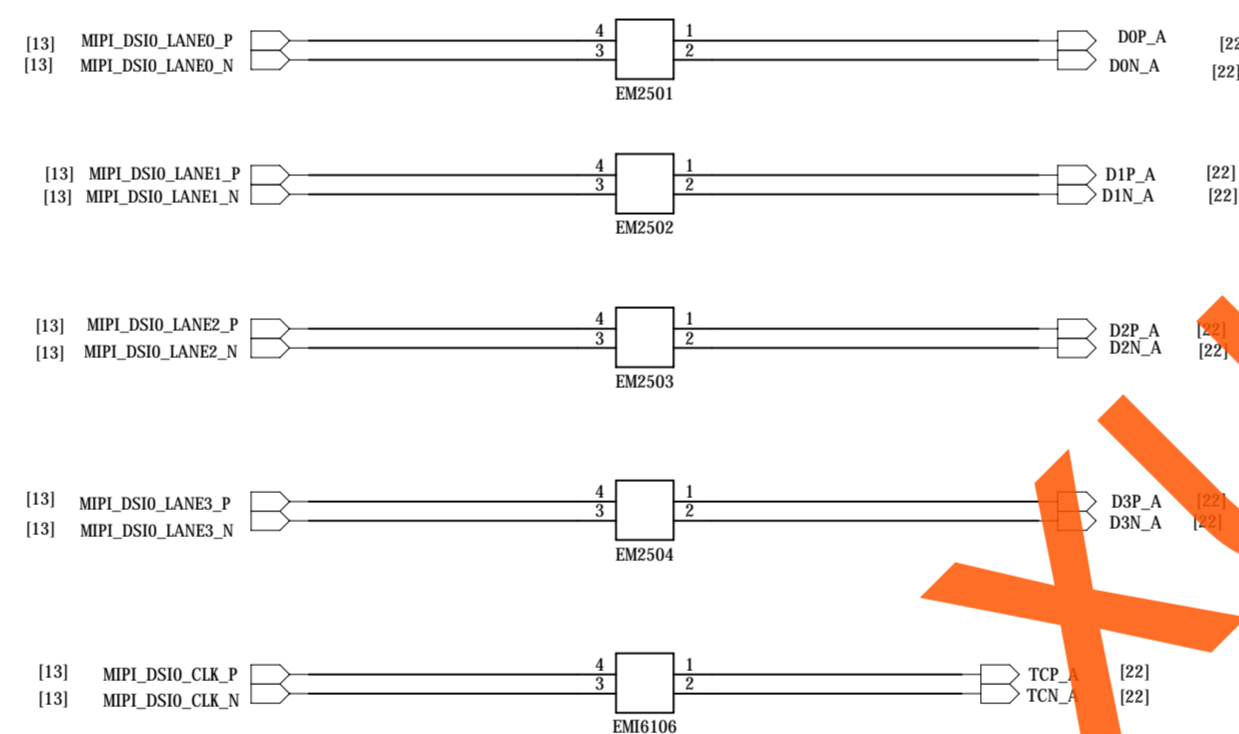
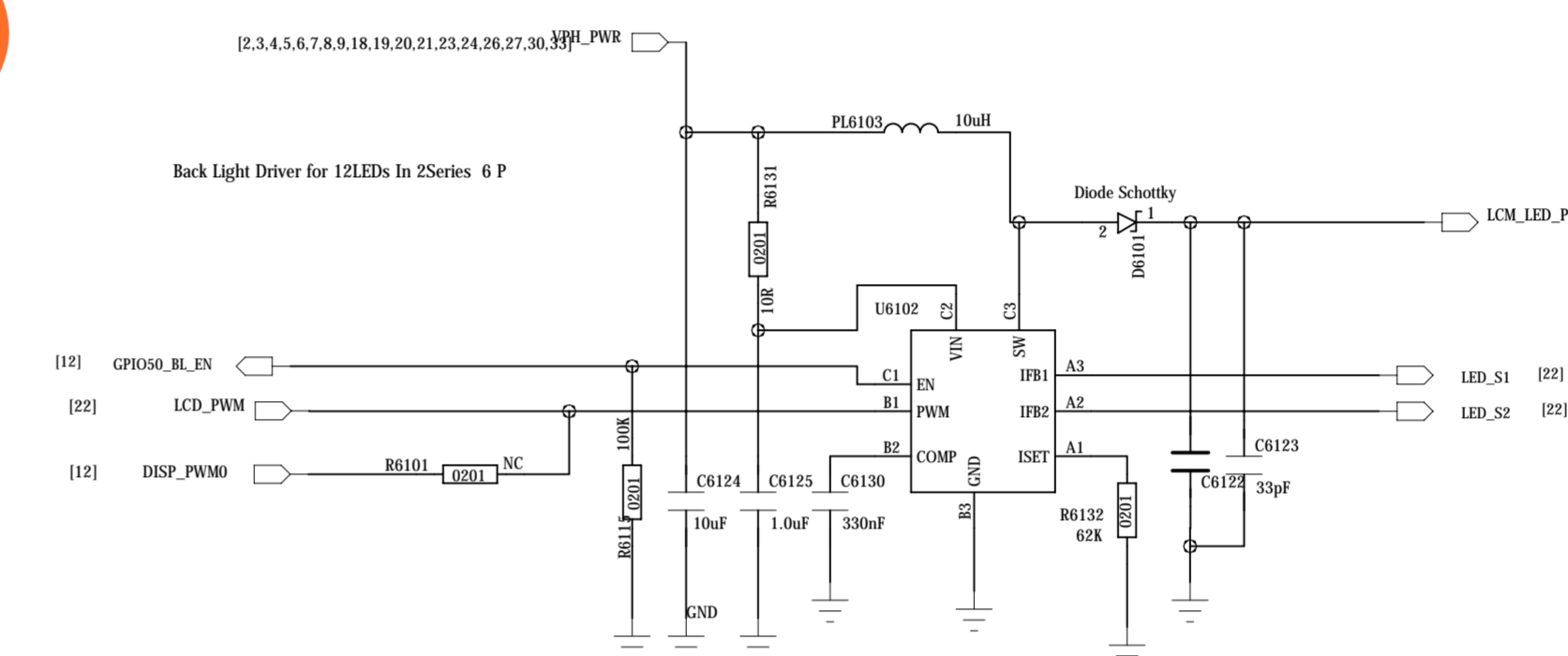
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DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: <Title>	
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE: <Code>	SIZE: A0 <Drawing Number> <Revision>
RELEASED: <Released By>		DATE: <Release Date>		SCALE: <Scale>	SHEET: 24 / 34

REVISION RECORD			
ITER	ECO NO.	APPROVED	DATE



change for better layout



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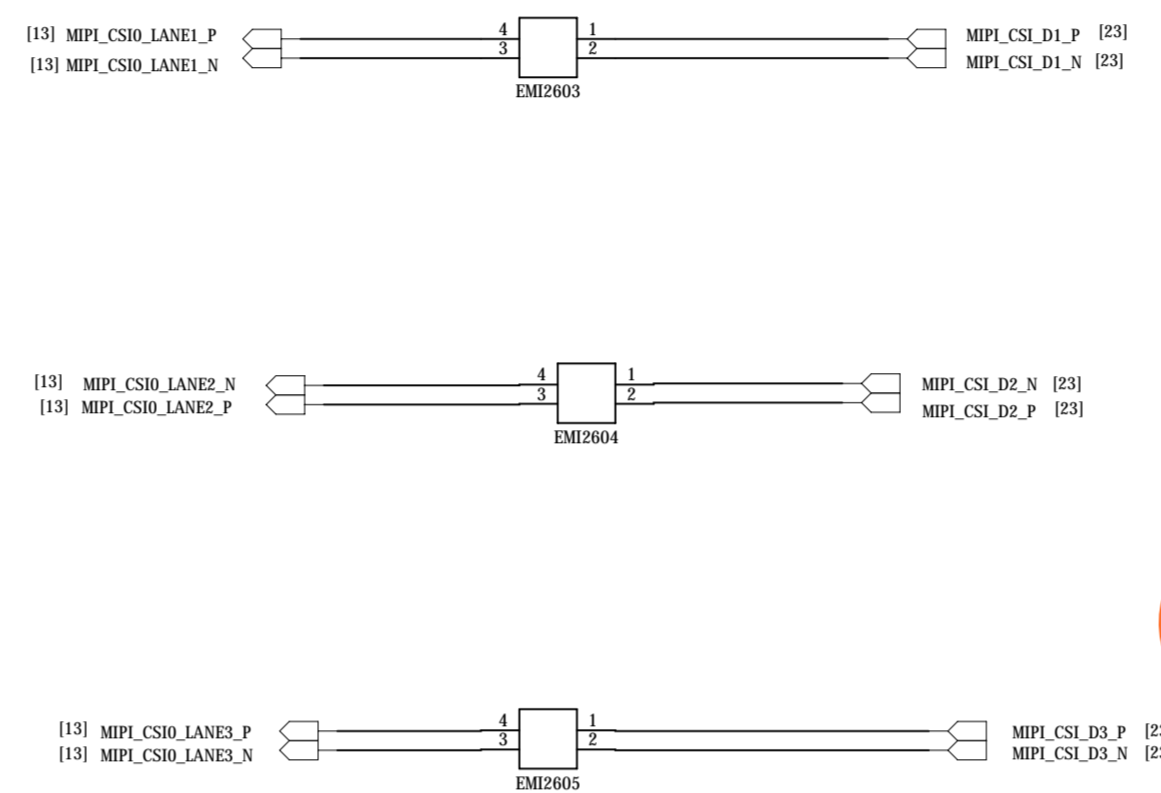
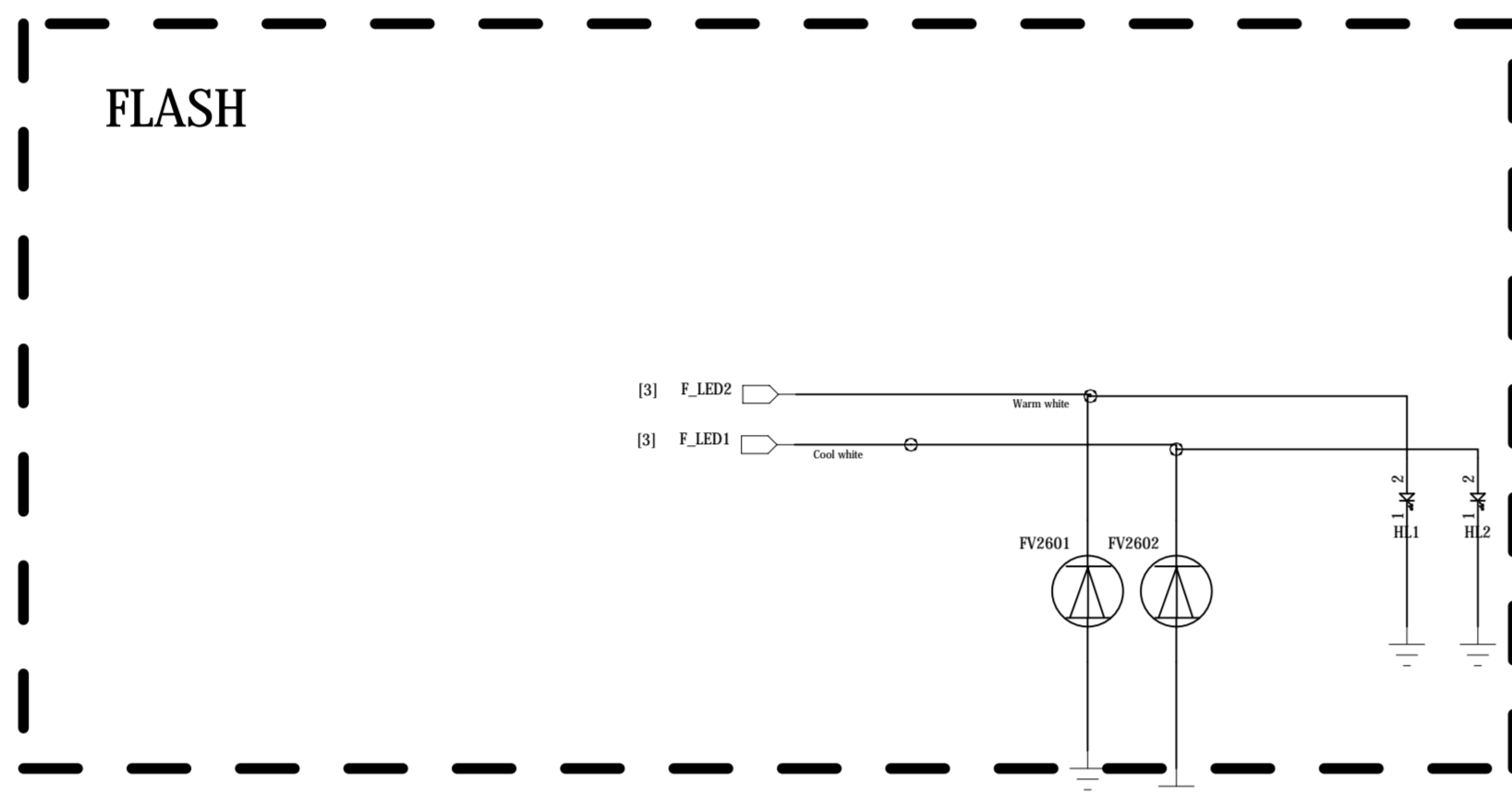
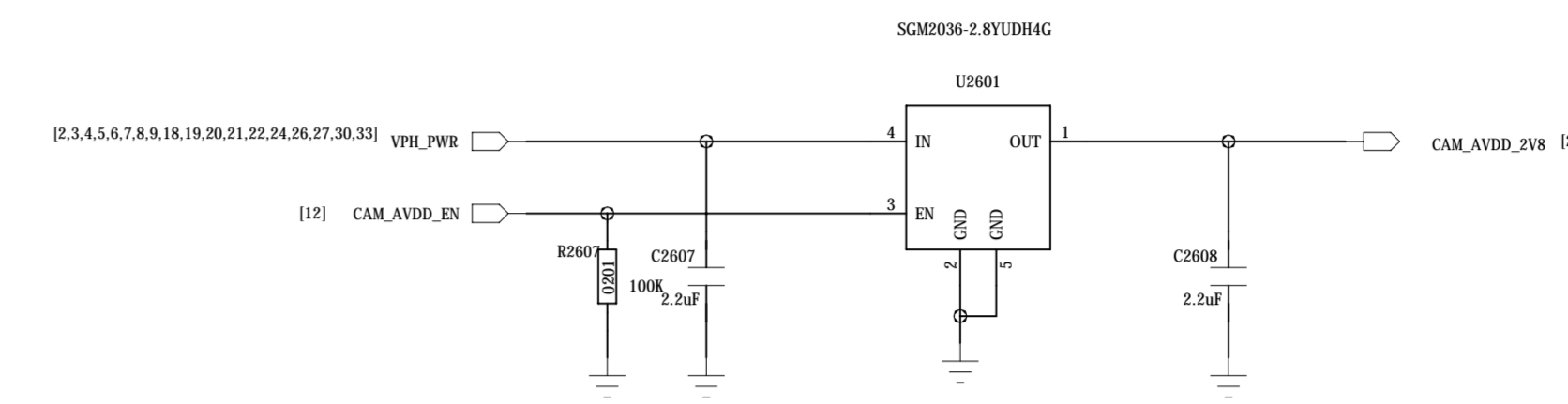
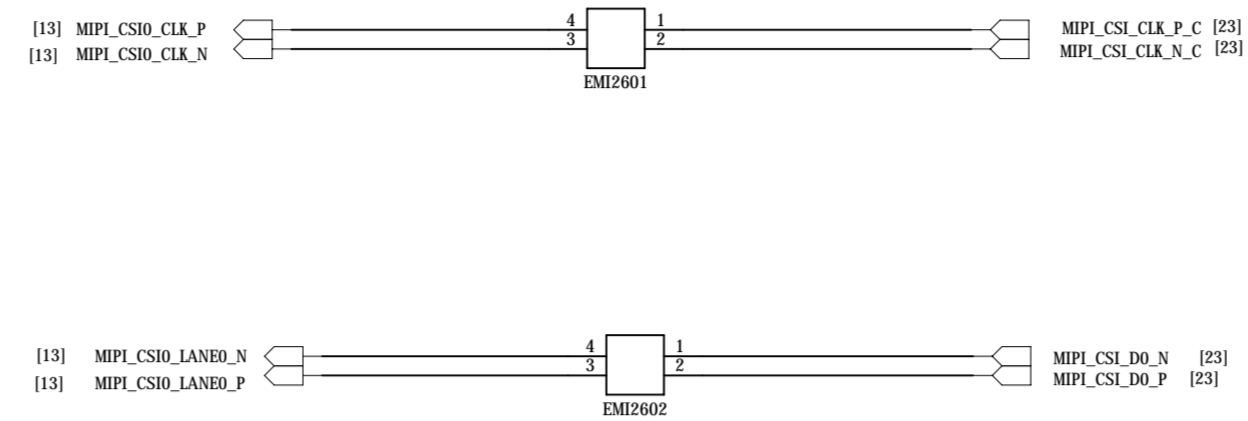
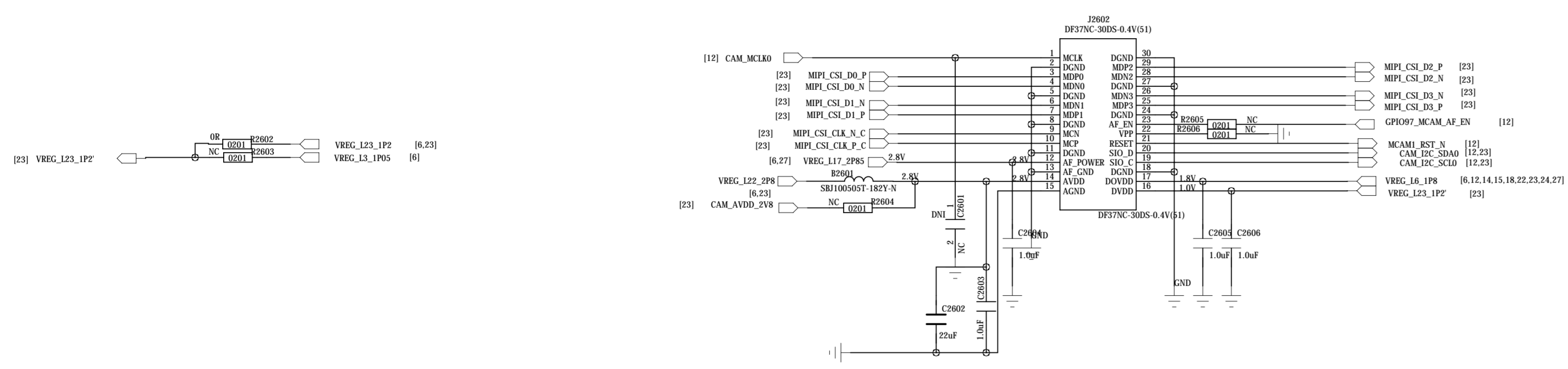
COMPANY: <Company Name>

TITLE: <Title>

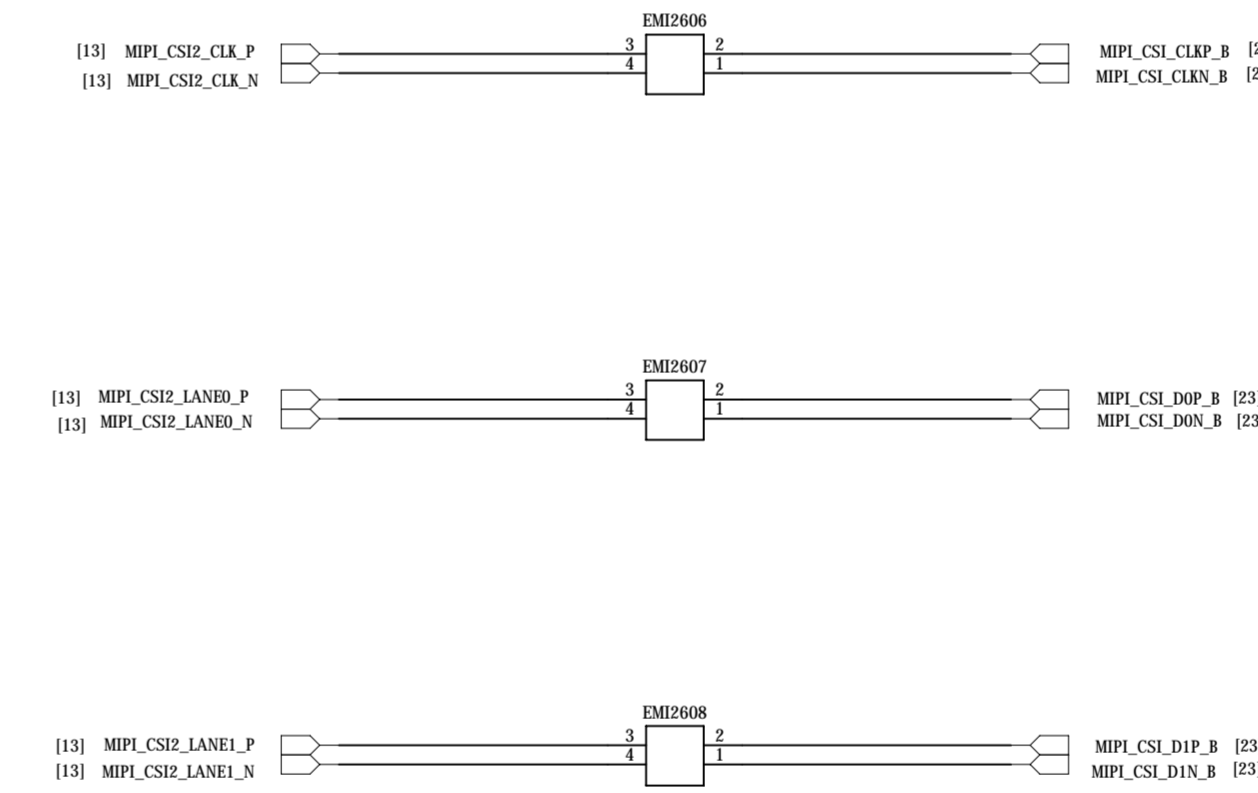
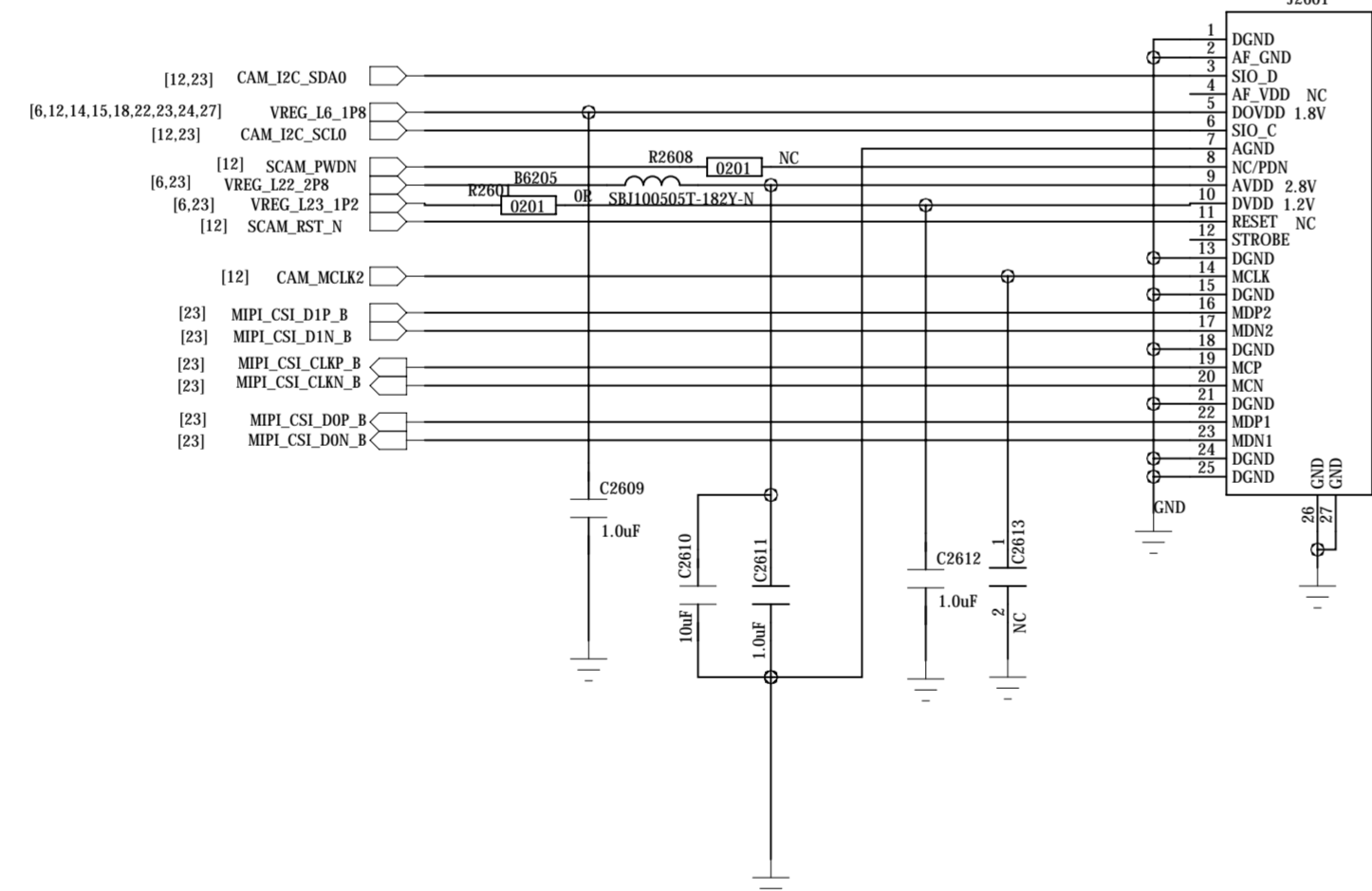
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CHECKED: <Checked By>	DATE: <Checked Date>				
QUALITY CONTROL: <QC By>	DATE: <QC Date>	SCALE: <Scale>			
RELEASED: <Released By>	DATE: <Release Date>	SHEET: 22 / 34			

REVISION RECORD			
REV	ECO NO.	APPROVED	DATE

Main Camera



sub Camera



Schematic design notice of "63_PERI_CAMERA_KEYPAD" page.

Note 62-2: I2C control interface of front camera (with AF) must be assigned to I2C-2 bus when PIP/IVV feature is supported.

Note 62-3: Reserve a capacitor (27pF) on camera's MCLK and shunt it to GND to prevent GPS de-sense.

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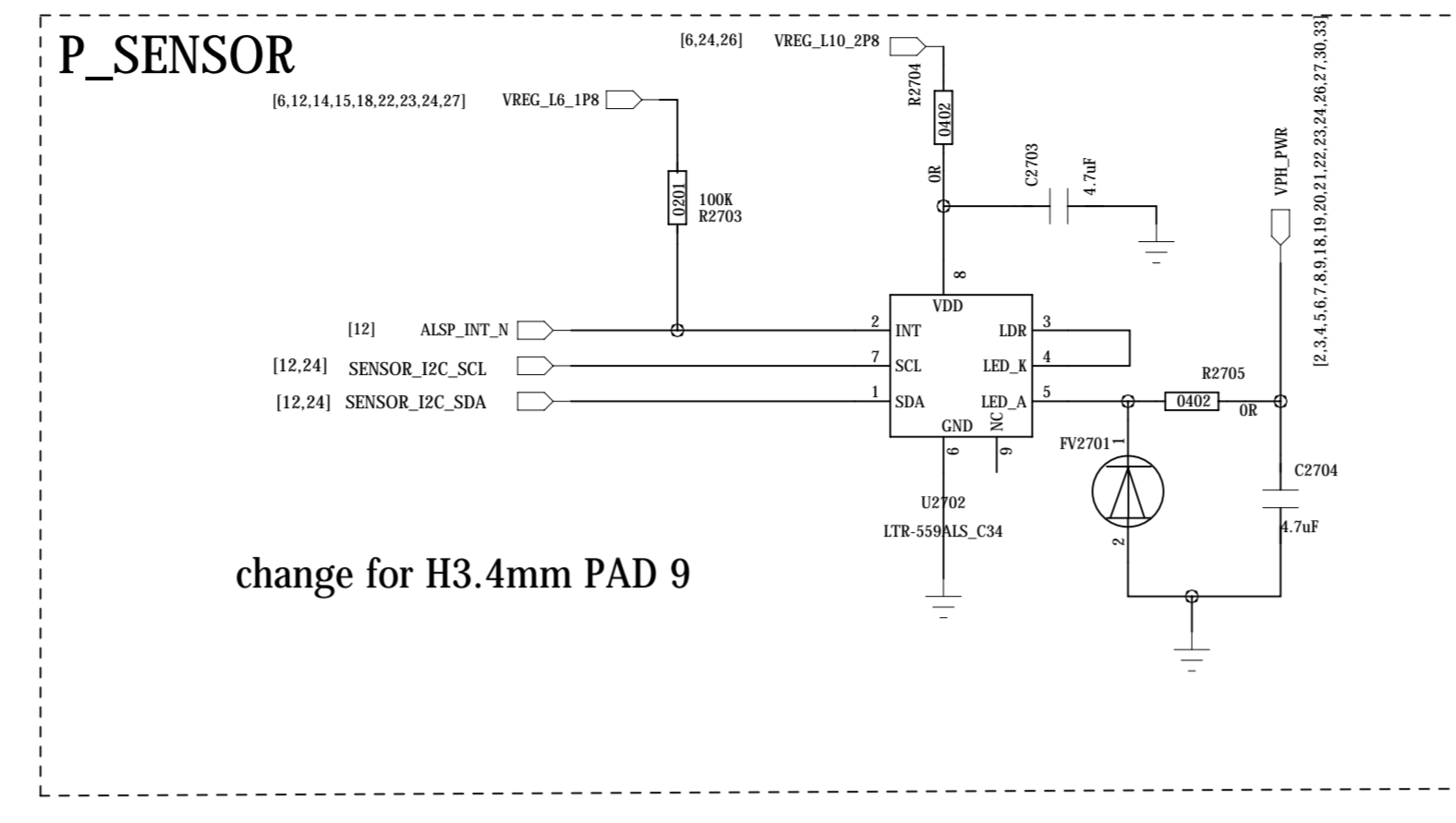
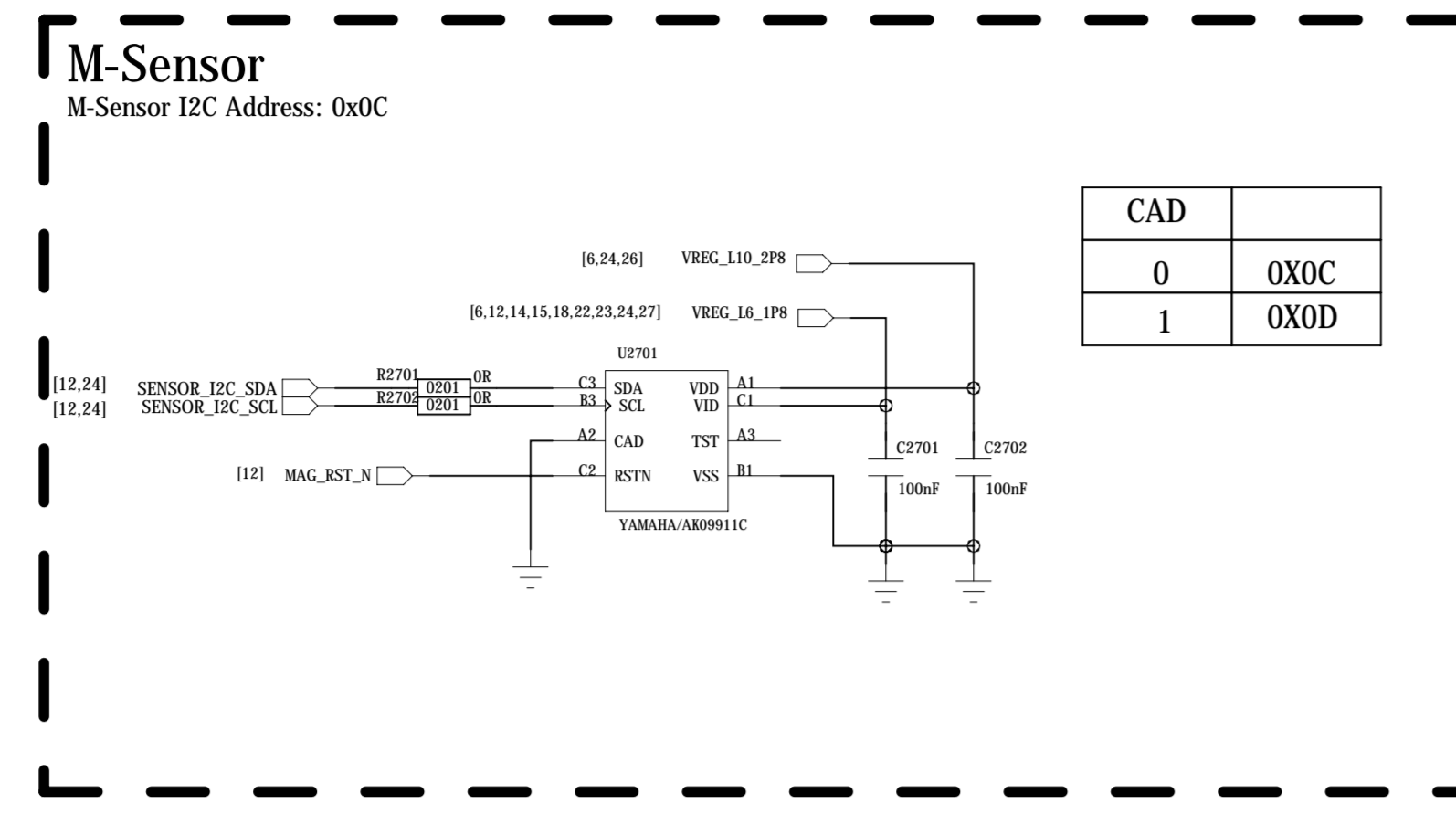
COMPANY: <Company Name>

TITLE: <Title>

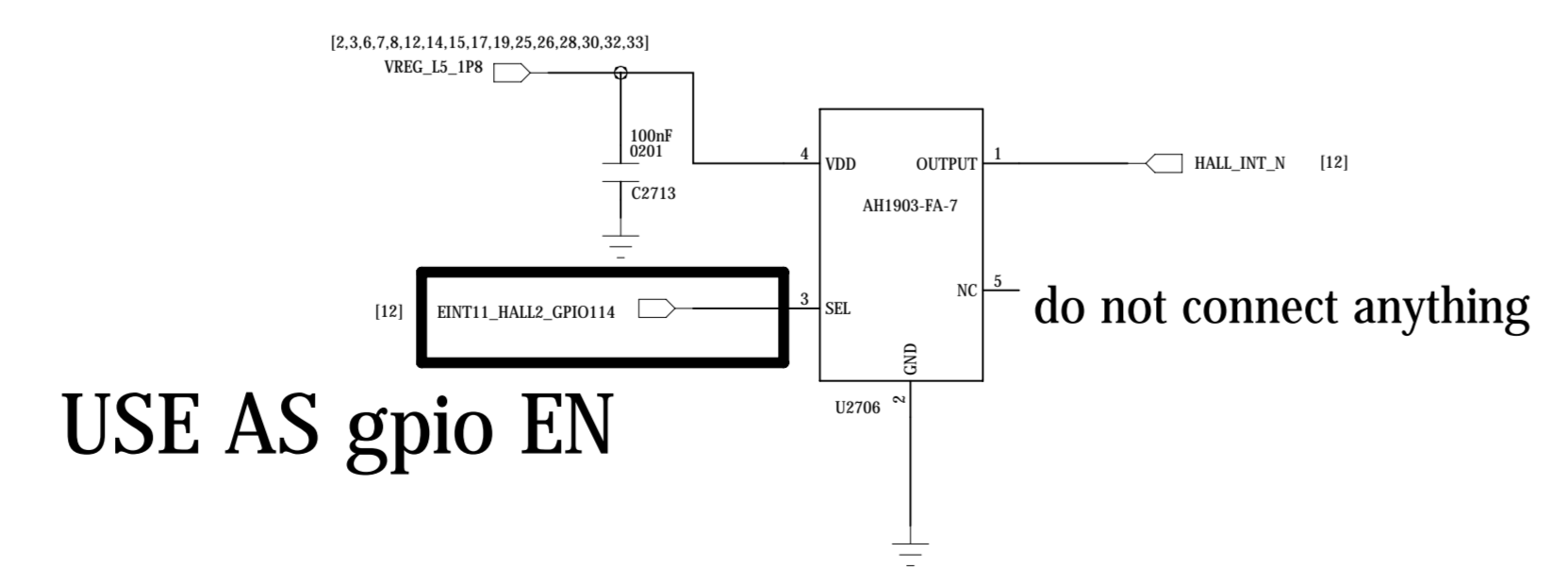
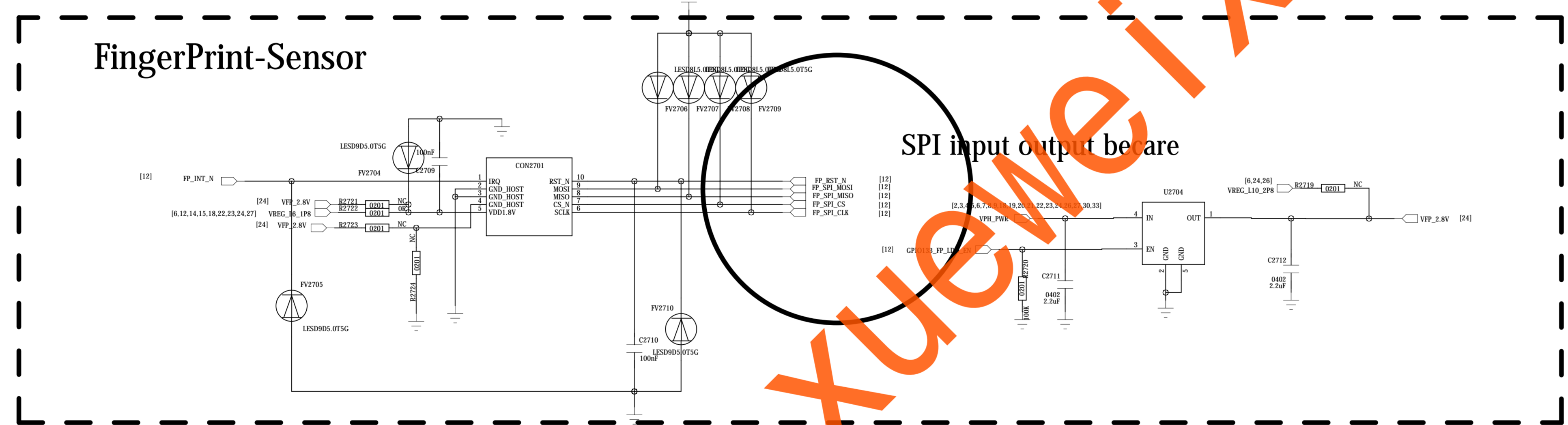
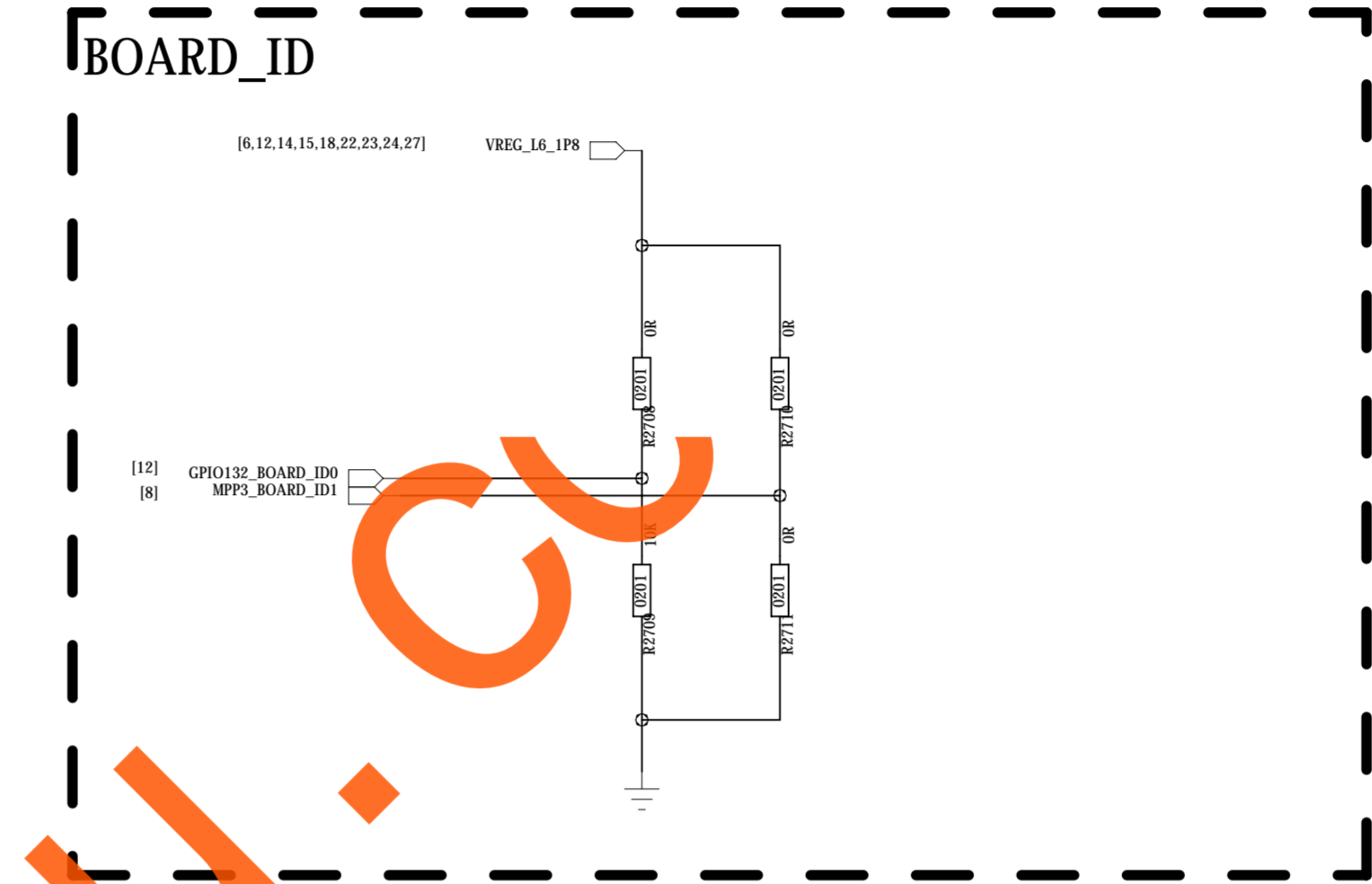
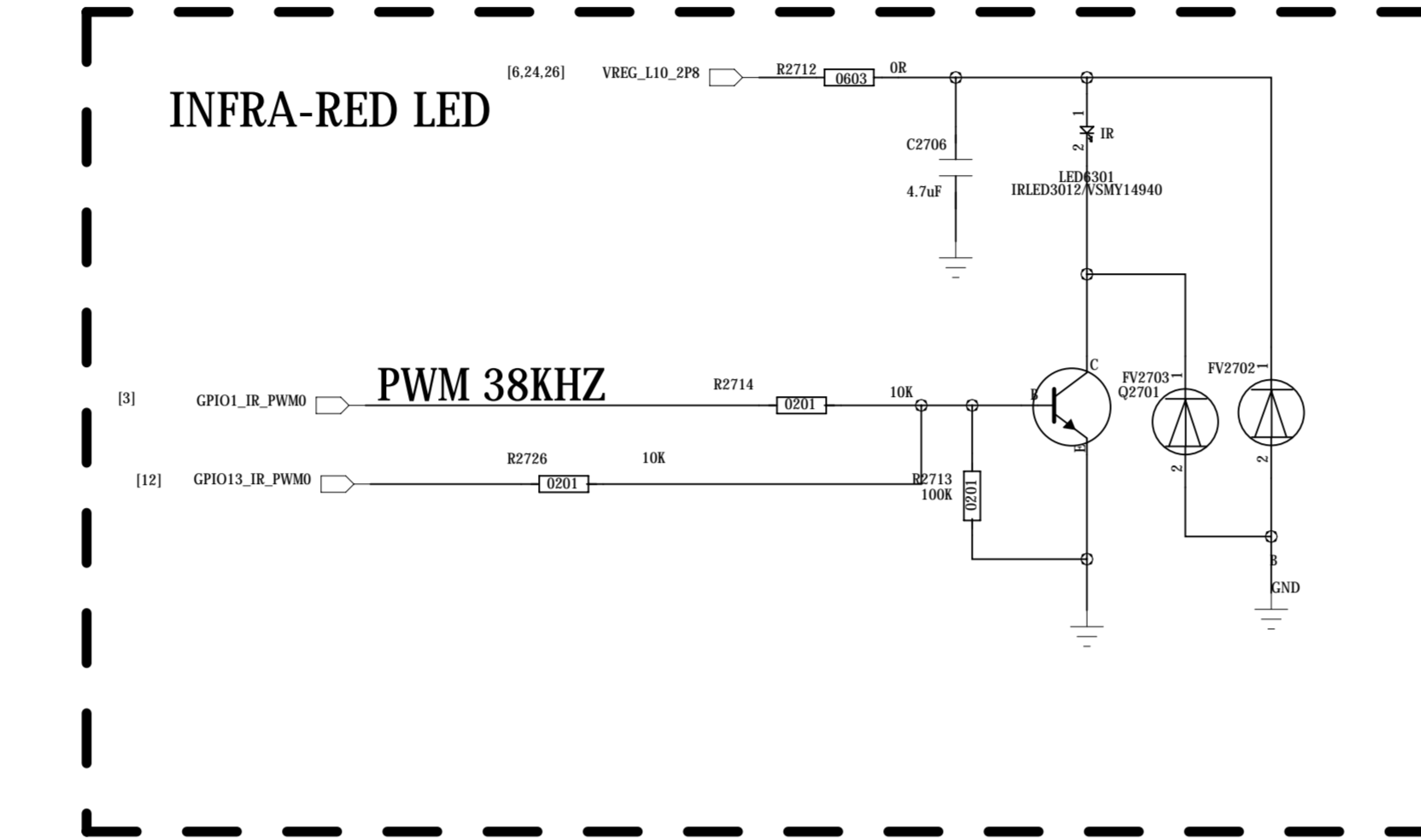
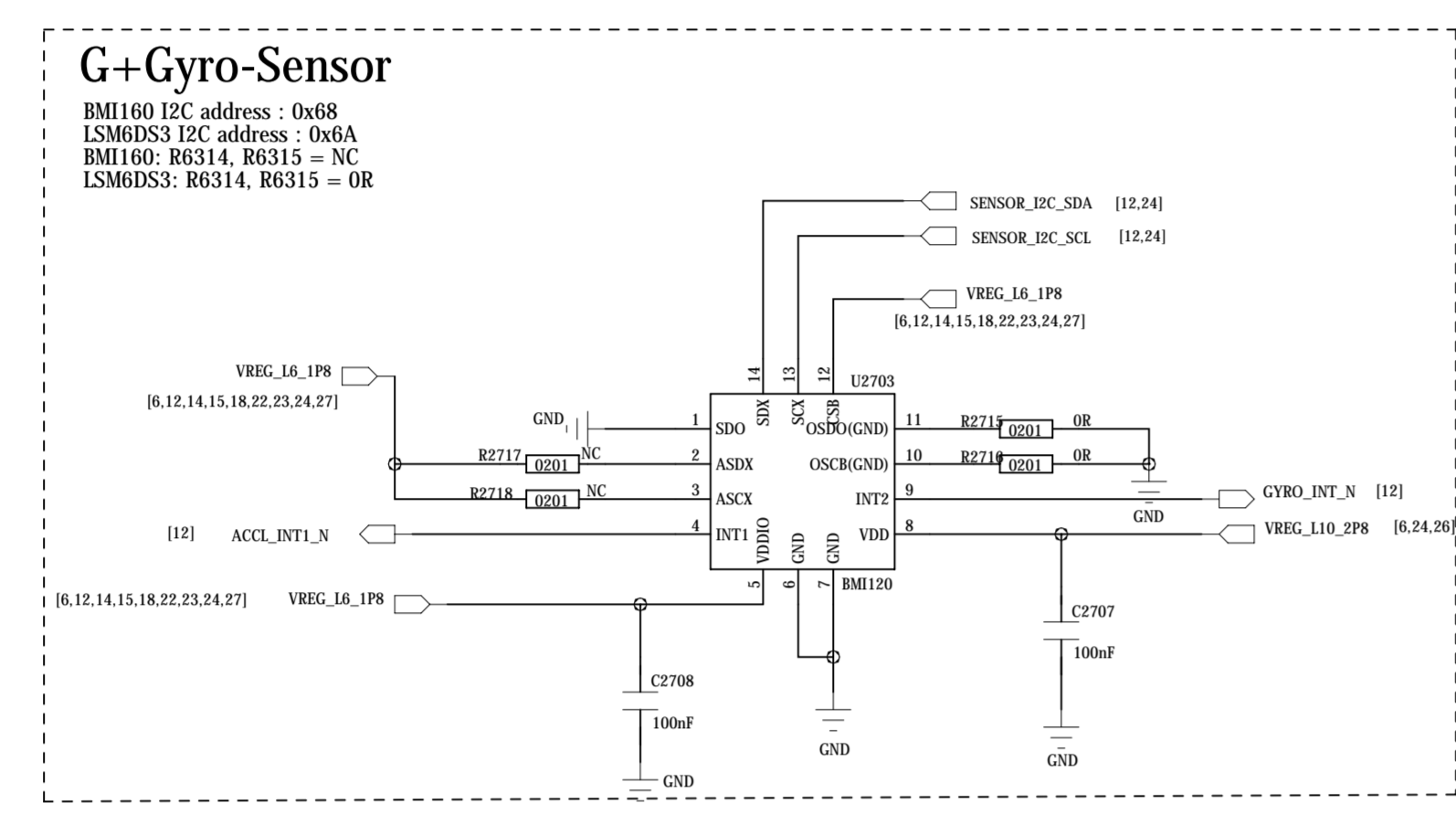
DESIGN	DATE	DESIGN	DATE	CODE	SIZE	DRAWING NO.	REV.
<Drawn By>	<Drawn Date>	<Checked By>	<Checked Date>	<Code By>	<QC Date>	<Released By>	<Release Date>

<Code> A0 <Drawing Number> <Revision>

REVISION RECORD			
LTW	ECO NO.	APPROVED	DATE



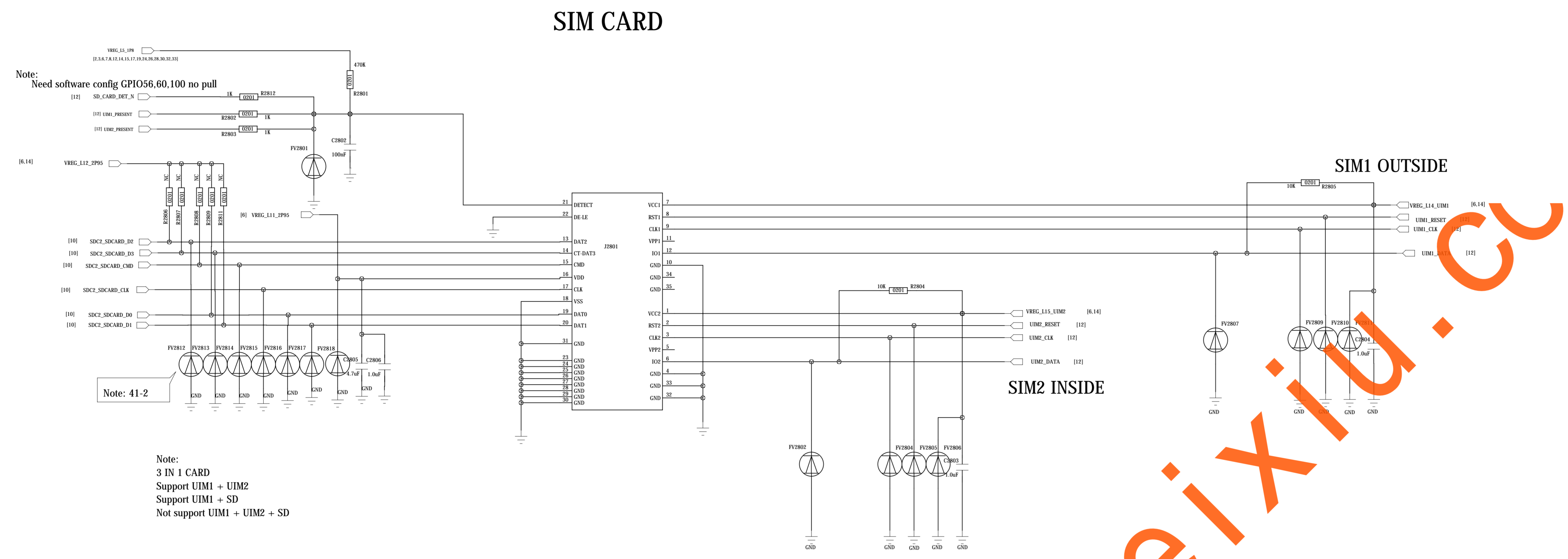
Thermister / To sense board level temperature



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DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: <Title>	
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE: <Code>	SIZE: A0
RELEASED: <Released By>		DATE: <Release Date>		DRAWING NO. <Drawing Number>	
SCALE: <Scale>		SHEET: 24		REV: 34	

REVISION RECORD			
ITER	ECO NO.	APPROVED	DATE



Note:
Need software config GPIO56,60,100 no pull

Note: 41-2

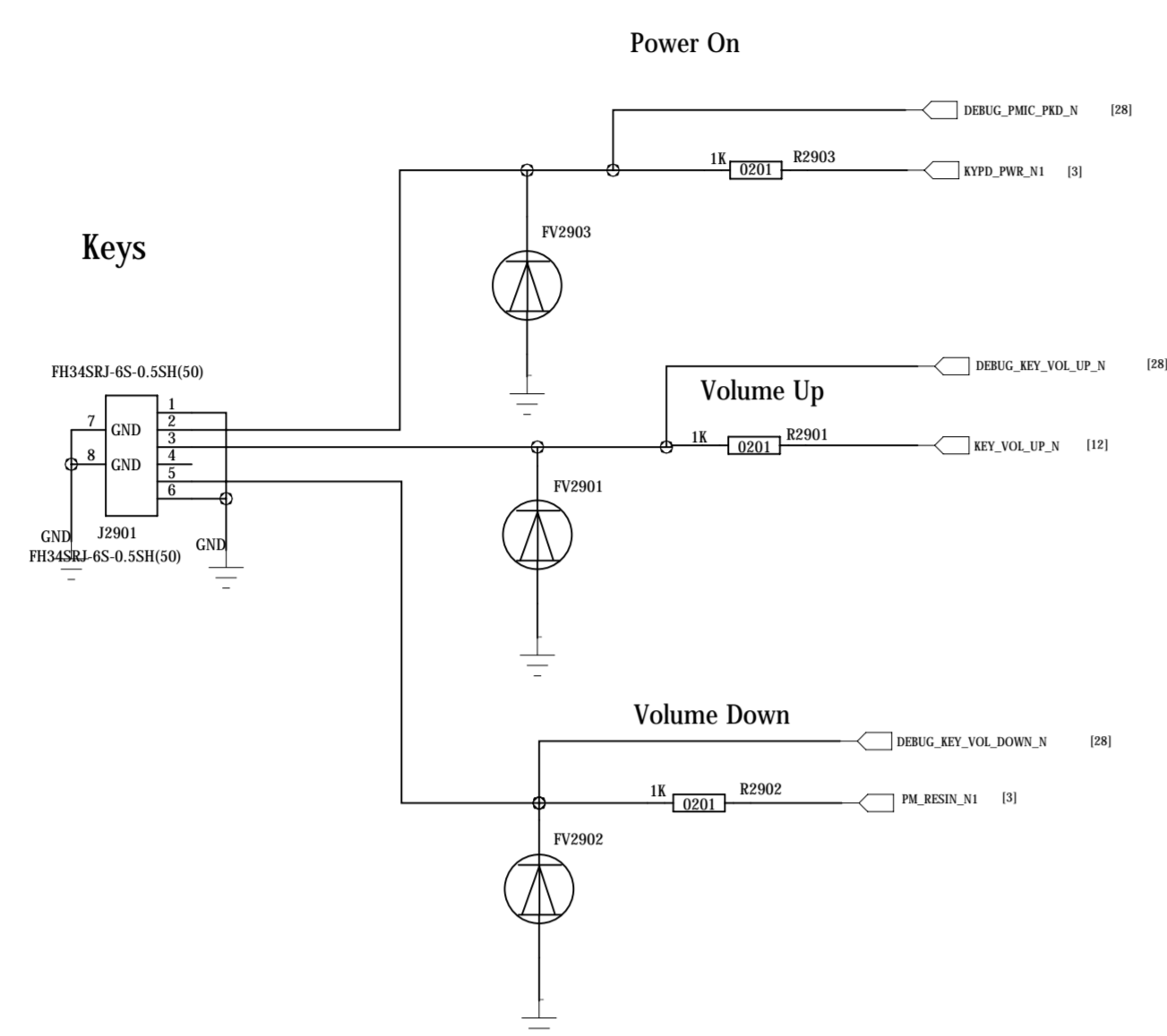
Note:
3 IN 1 CARD
Support UIM1 + UIM2
Support UIM1 + SD
Not support UIM1 + UIM2 + SD

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DRAWN: <Drawn By>		DATED: <Drawn Date>		COMPANY: <Company Name>	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: <Title>	
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE: <Code>	SIZE: A0 <Drawing Number> <Revision>
RELEASED: <Released By>		DATE: <Release Date>		SCALE: <Scale>	SHEET: 28 / 34

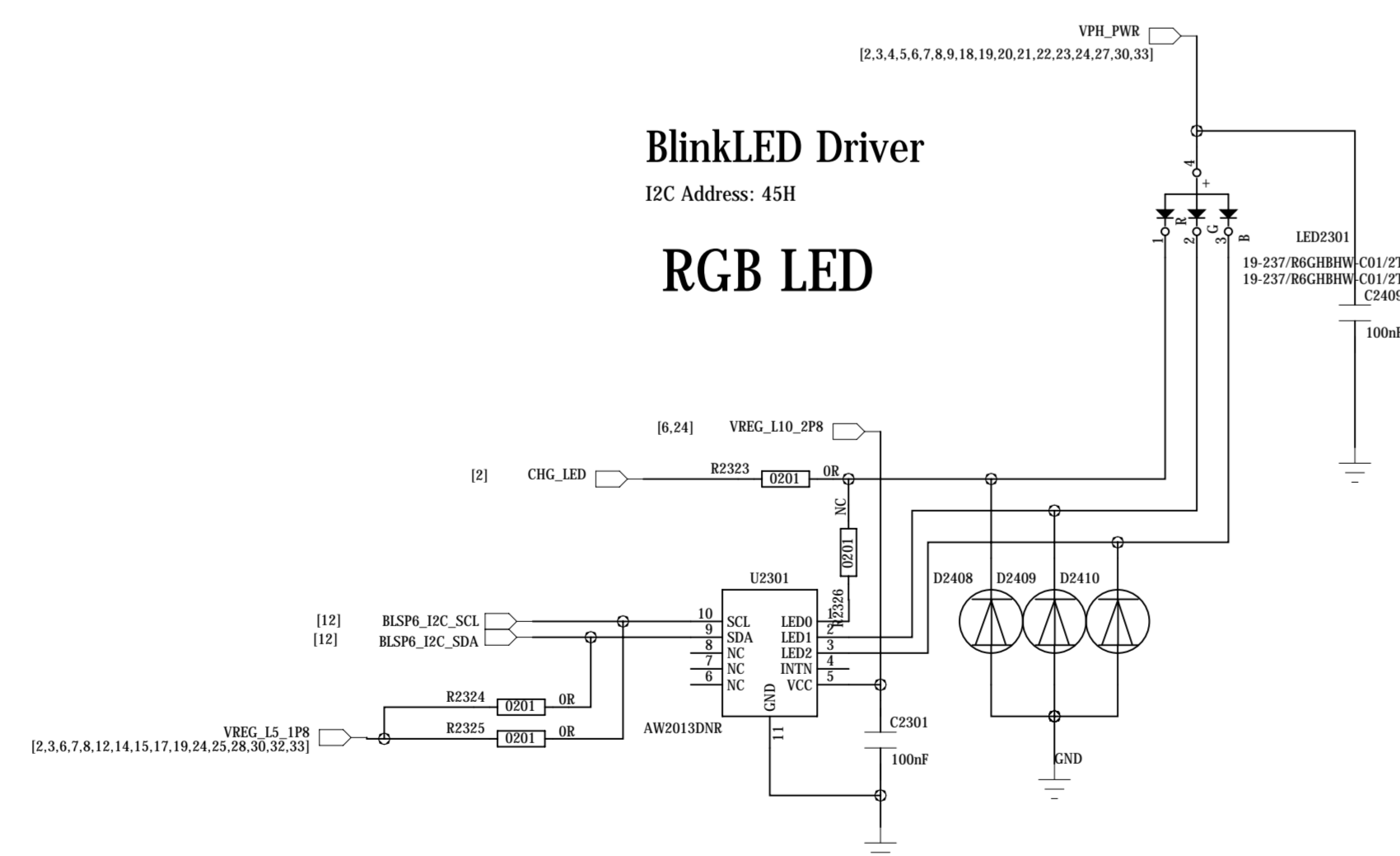
REVISION RECORD			
VER	REV NO	APPROVED	DATE

Power Key



Signal	Description
RPSNS0	Volume Up
PM_RST_N	Volume Down
KYPD_PWR_N	POWER_ON
PM_RST_N +	Hardware Reset
KYPD_PWR_N	

BlinkLED Driver I2C Address: 45H RGB LED



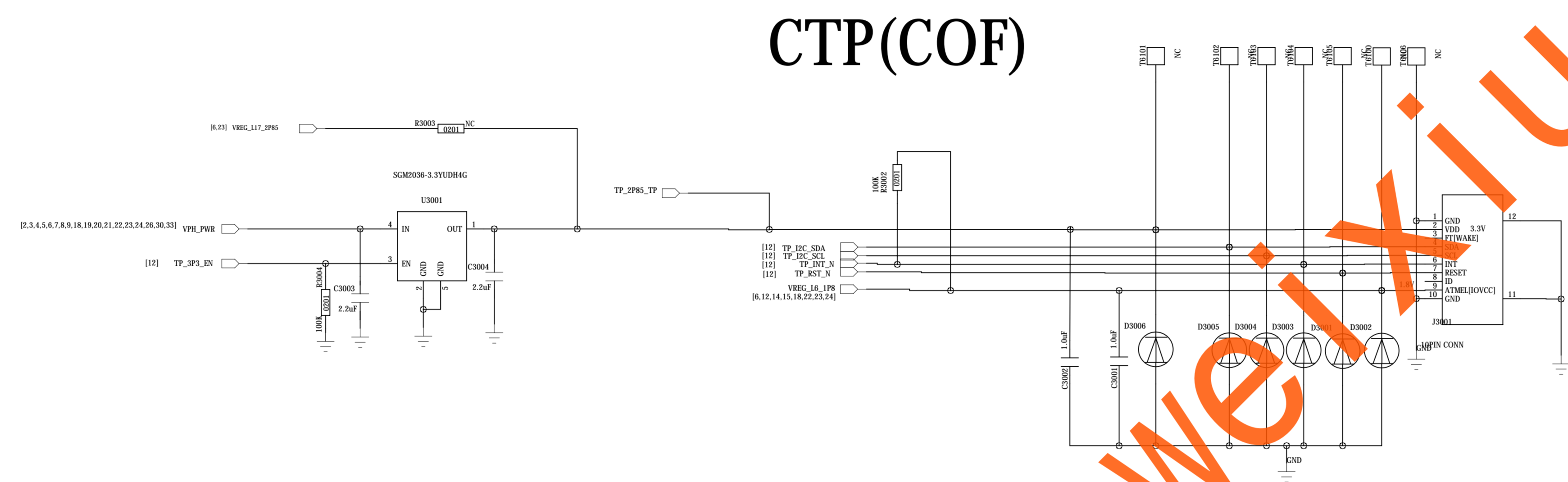
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COMPANY: <Company Name>

TITLE: <Title>

DRAWN: <Drawn By>	DATED: <Drawn Date>	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED: <Checked By>	DATE: <Checked Date>	<Code>	A0	<Drawing Number>	<Revision>
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RELEASED: <Released By>	DATE: <Release Date>	SCALE: <Scale>	SHEET 26 34		

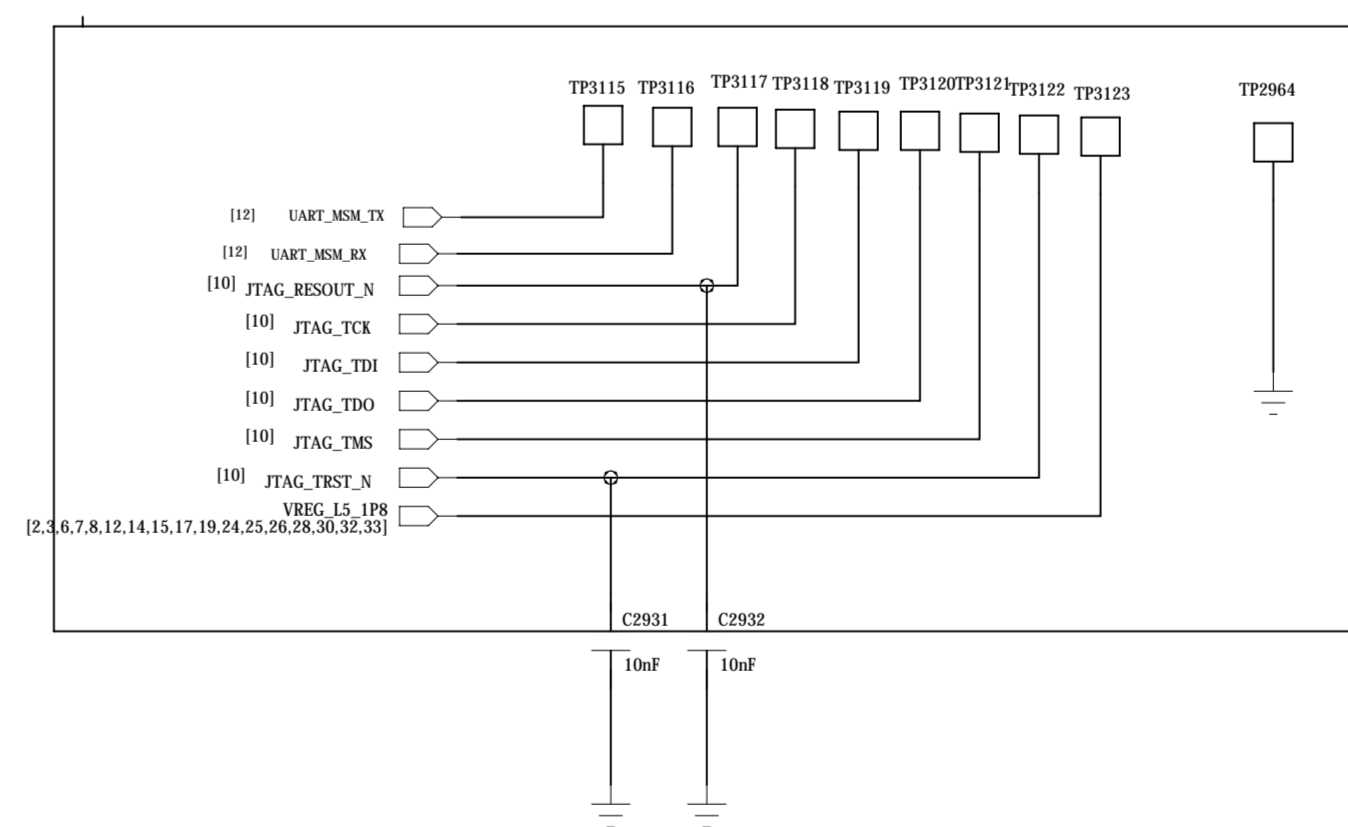
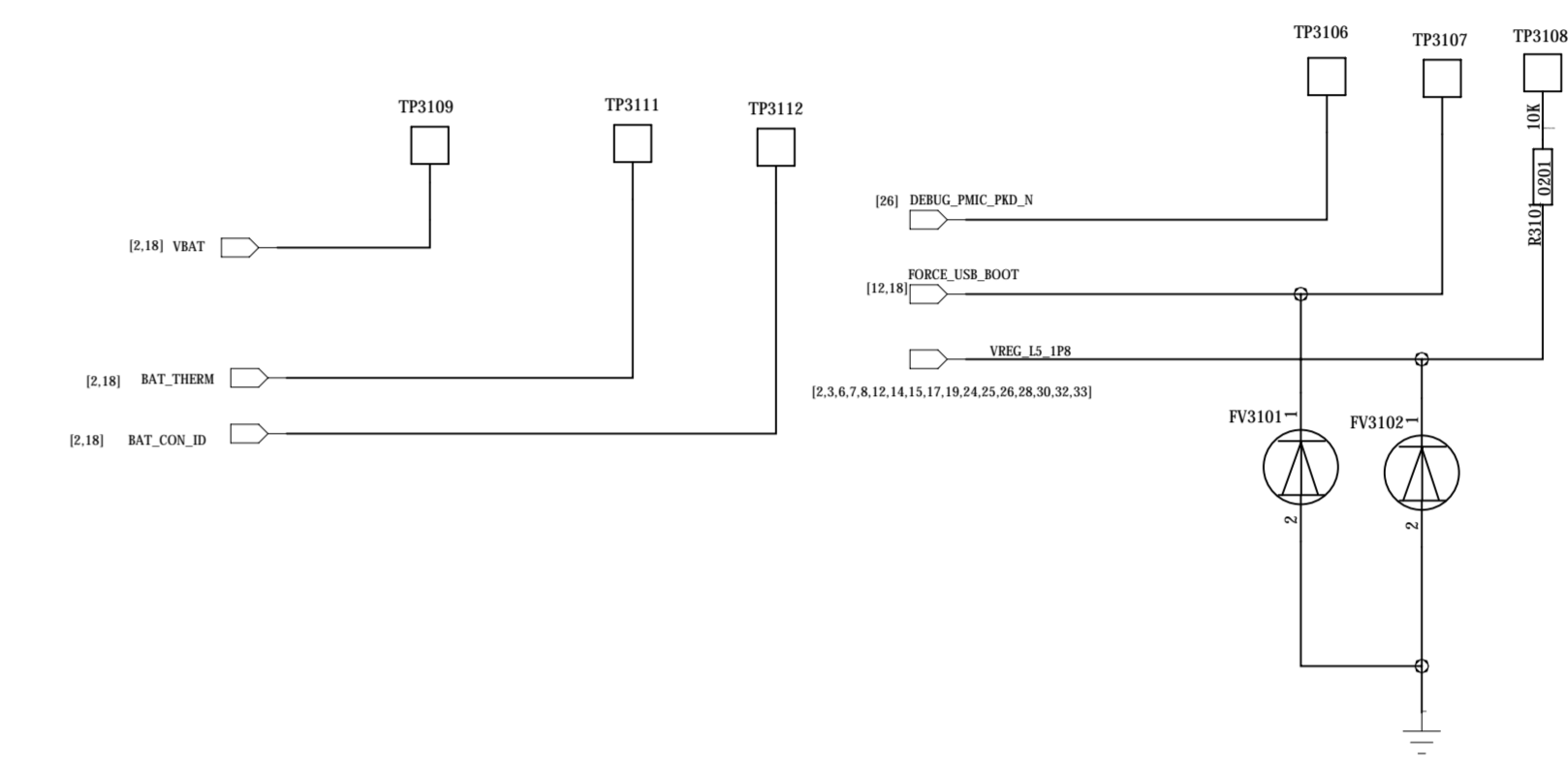
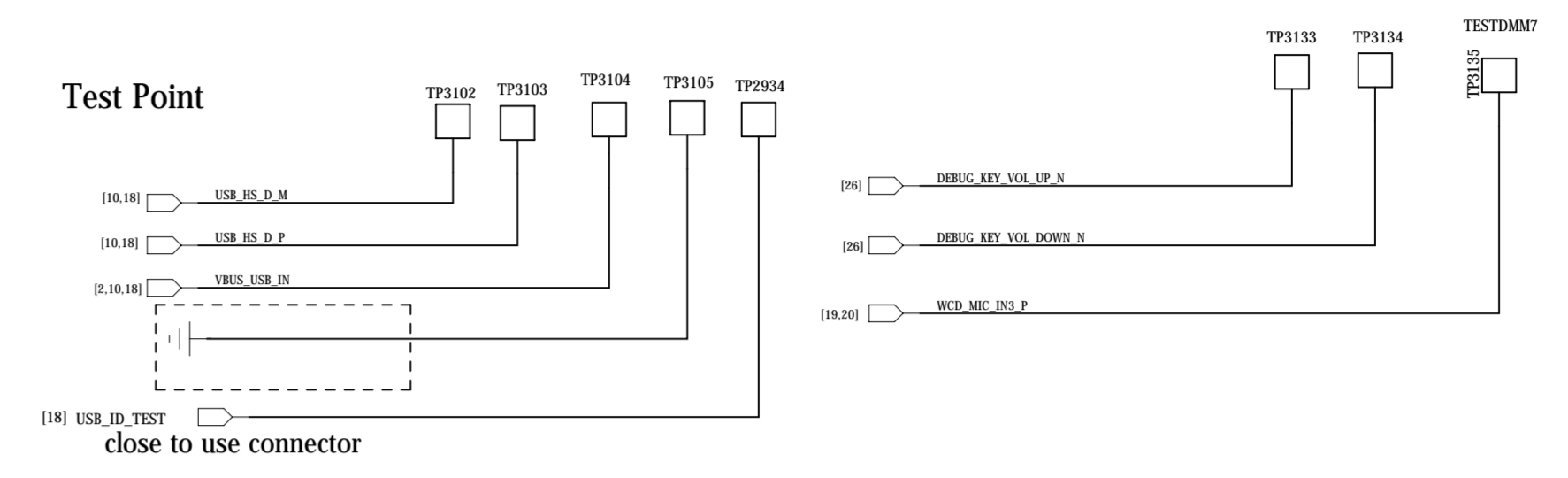
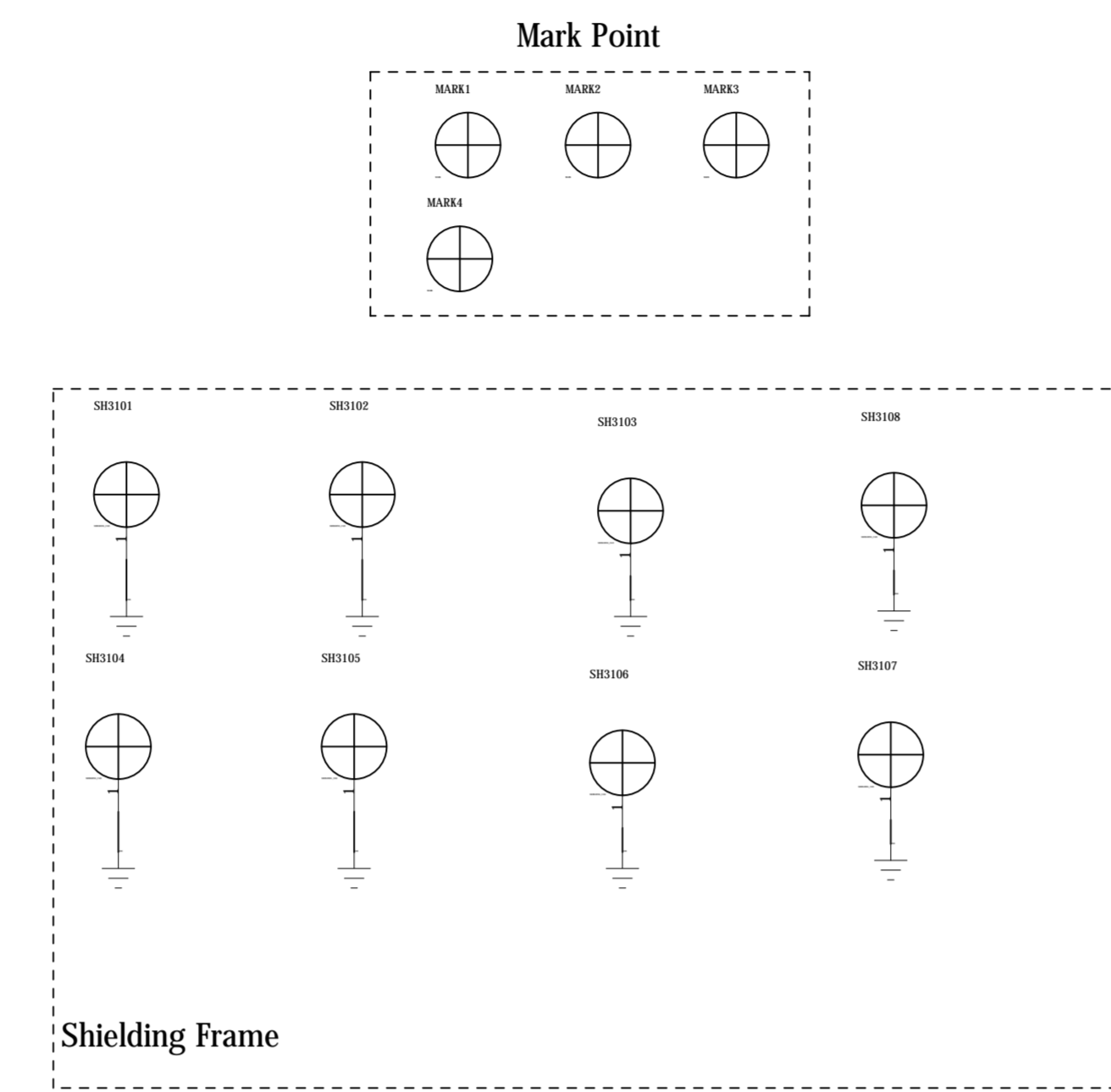
REVISION RECORD			
ITE	ECO NO.	APPROVED	DATE



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DRAWN: <Drawn By>		DATE: <Drawn Date>		COMPANY: <Company Name>	
CHECKED: <Checked By>		DATE: <Checked Date>		TITLE: <Title>	
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE: <Code>	SIZE: A0
RELEASED: <Released By>		DATE: <Release Date>		DRAWING NO: <Drawing Number>	
SCALE: <Scale>				REV: <Revision>	
SHEET: 27				34	

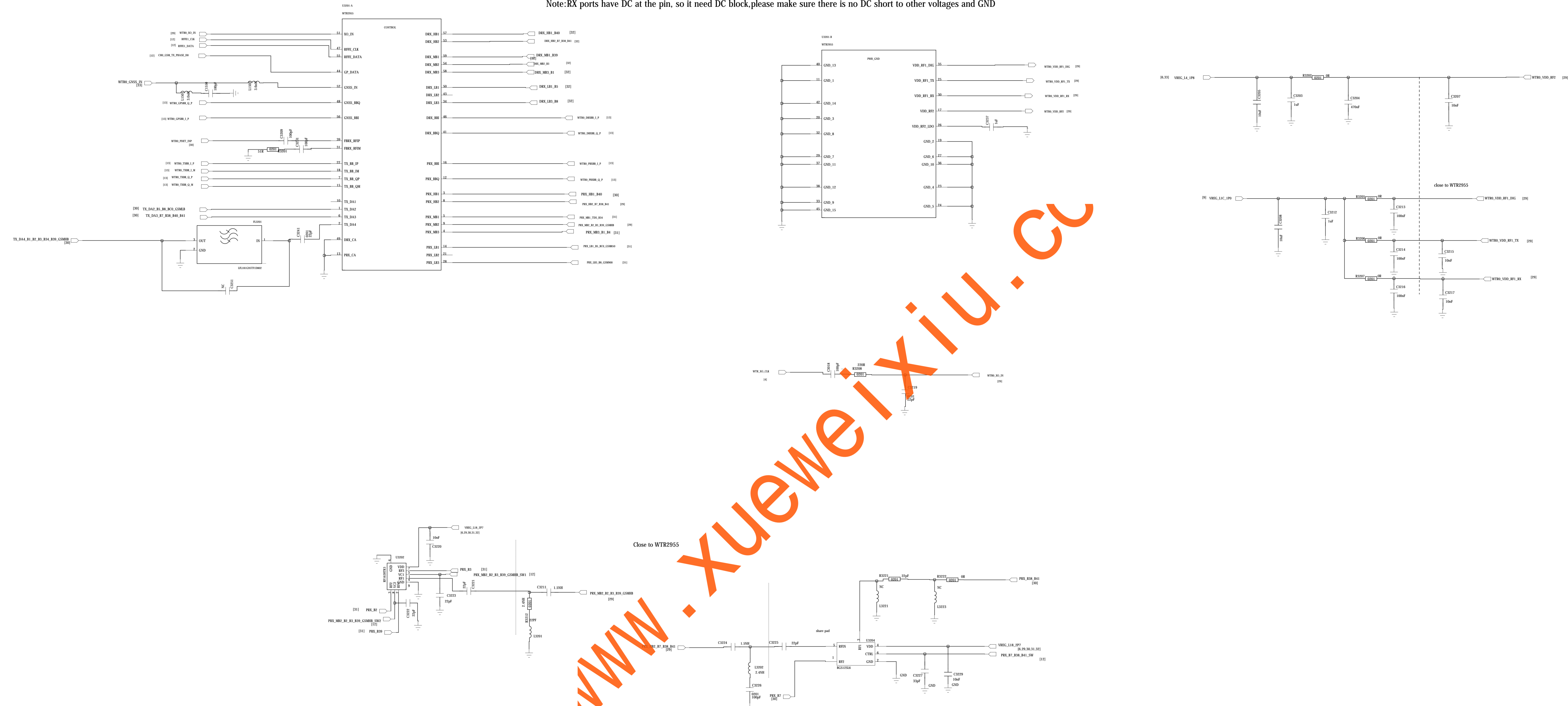
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JTAG CONNECTOR

REVISION RECORD			
VER	REV NO	APPROVED	DATE

Note:RX ports have DC at the pin, so it need DC block,please make sure there is no DC short to other voltages and GND



RF1628

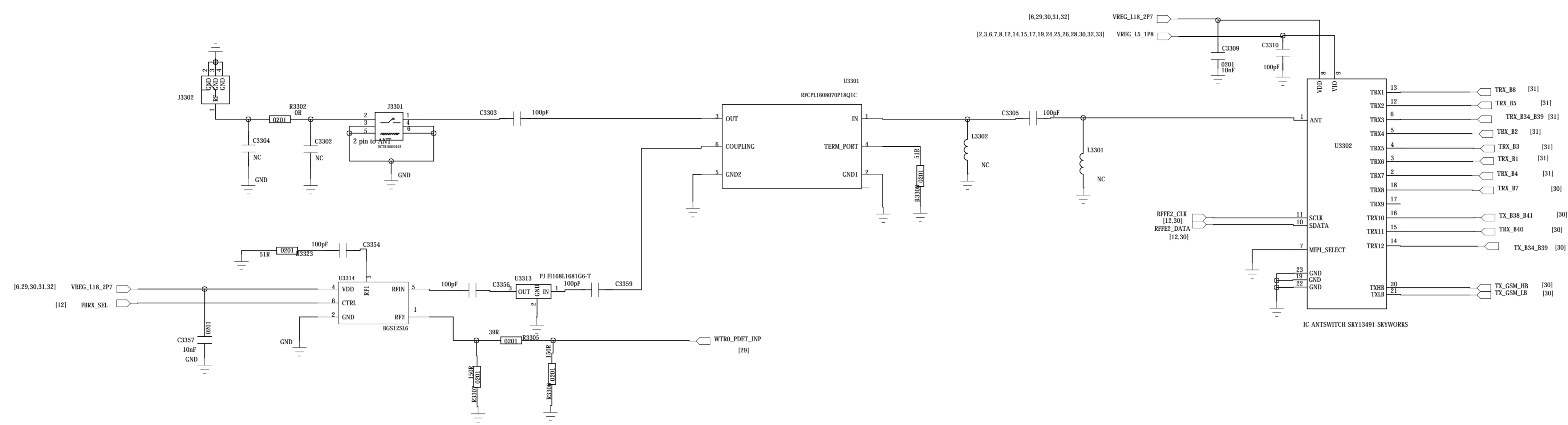
mode	VCTL1	VCTL2
RF1-RF3	L	H
RF1-RF2	H	L
RF1-RF4	H	H

RF1630

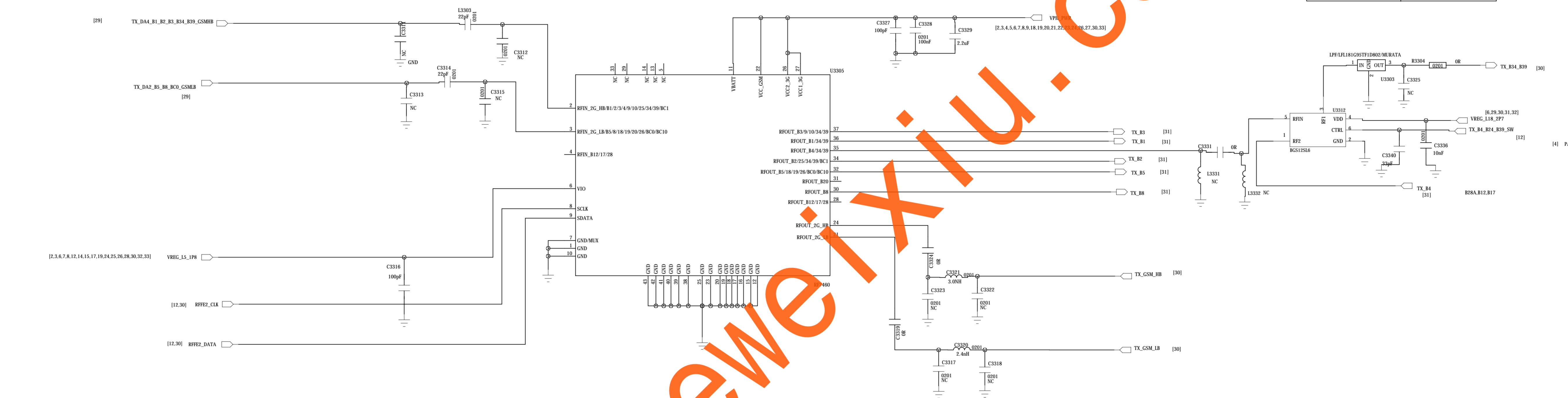
MODE	VC1
RFIN-RF1	0
RFIN-RF2	1

COMPANY: <Company Name>			
TITLE: <Title>			
DESIGN: <Drawn By>	DATE: <Drawn Date>	CODE: <Code>	SIZE: A0
CHECKED: <Checked By>	DATE: <Checked Date>	DRAWING NO: <Drawing Number>	
QUALITY CONTROL: <QC By>	DATE: <QC Date>	REV: <Revision>	
RELEASED: <Released By>	DATE: <Release Date>	SCALE: <Scale>	SHEET: 29 / 34

REVISION RECORD			
ITER	ECO NO.	APPROVED	DATE

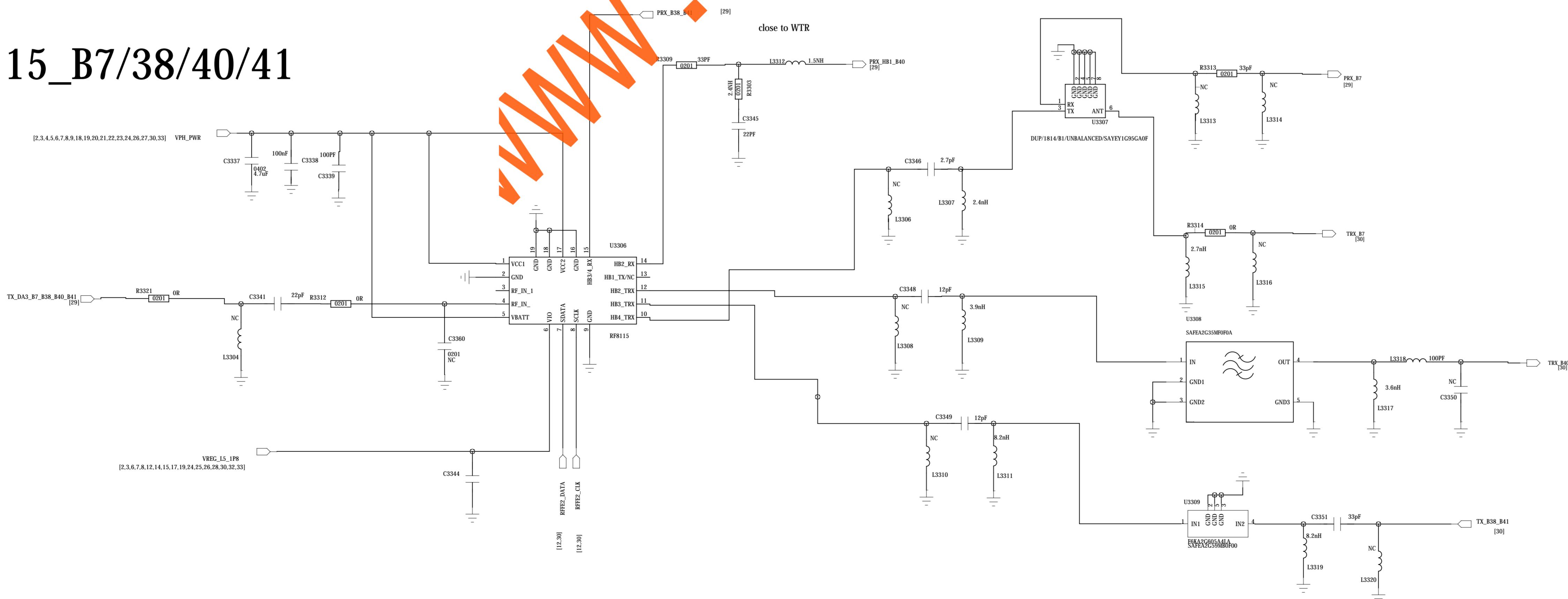


RF1630	
MODE	VC1
RFIN-RF1	0
RFIN-RF2	1



RT2401, C2417 near to PA U3202 U3303

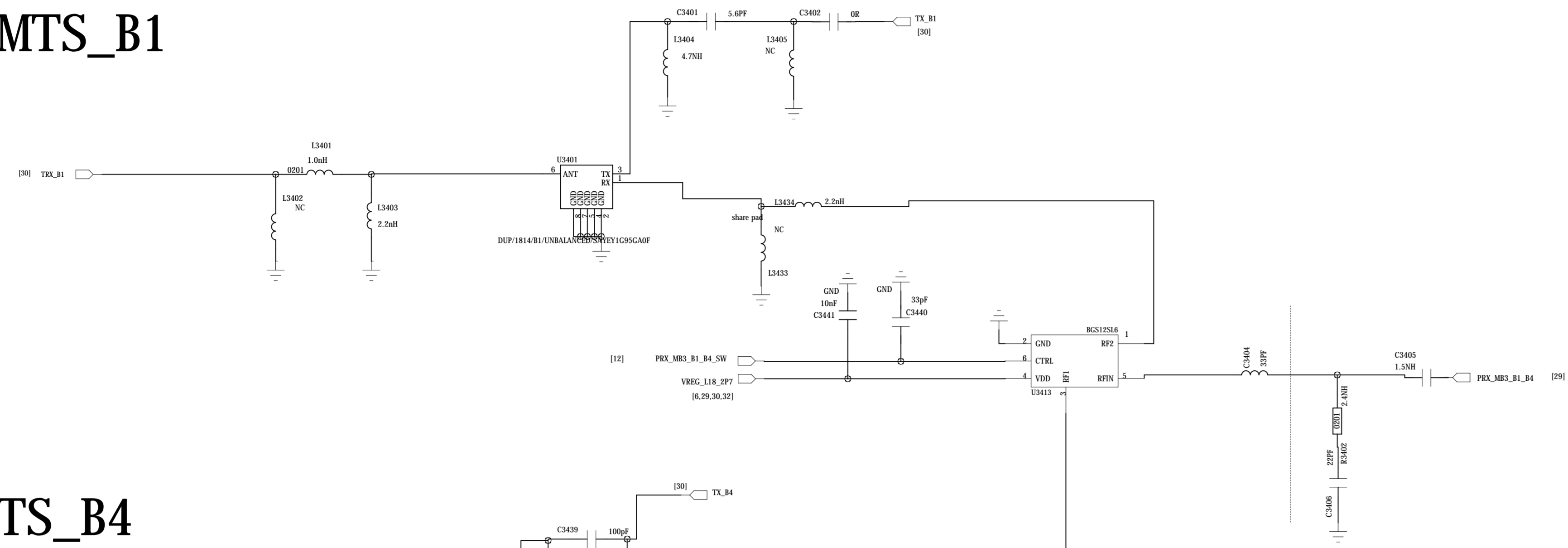
RF8115_B7/38/40/41



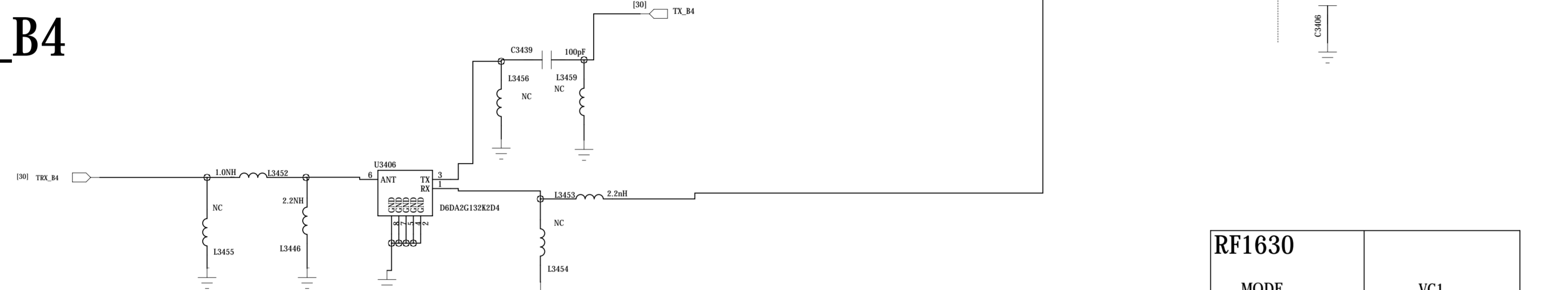
COMPANY		<Company Name>	
TITLE		<Title>	
DRAWN: <Drawn By>	DATE: <Drawn Date>	CODE	SIZE
CHECKED: <Checked By>	DATE: <Checked Date>	DRAWING NO.	
QUALITY CONTROL: <QC By>	DATE: <QC Date>	REV:	
RELEASED: <Released By>	DATE: <Release Date>	SCALE: <Scale>	
A0 <Drawing Number> <Revision>		SHEET 30 34	

REVISION RECORD			
REV	REV NO.	APPROVED	DATE

TRX_UMTS_B1

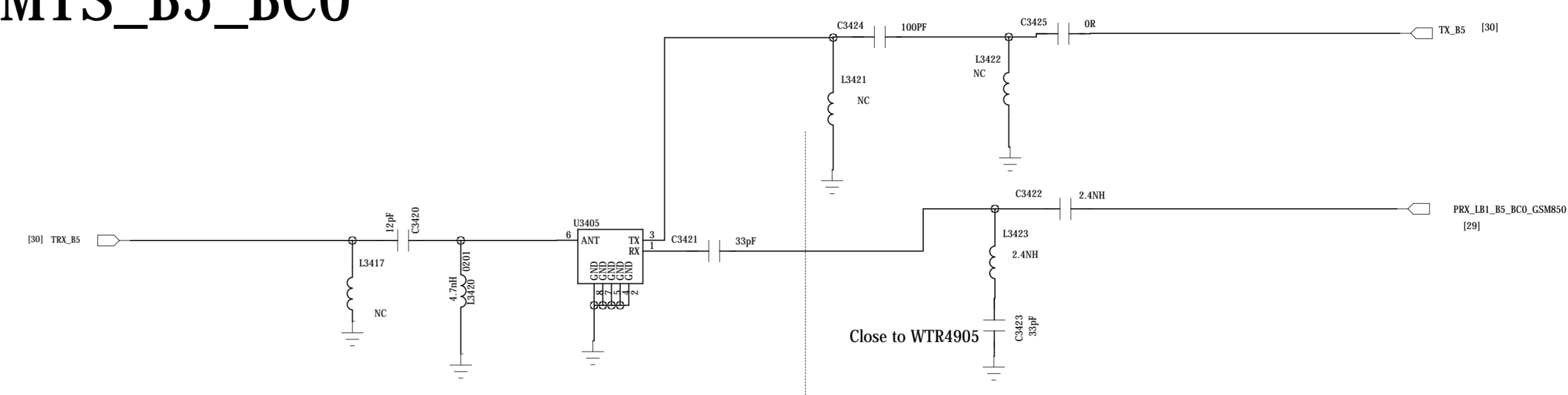


TRX_UMTS_B4

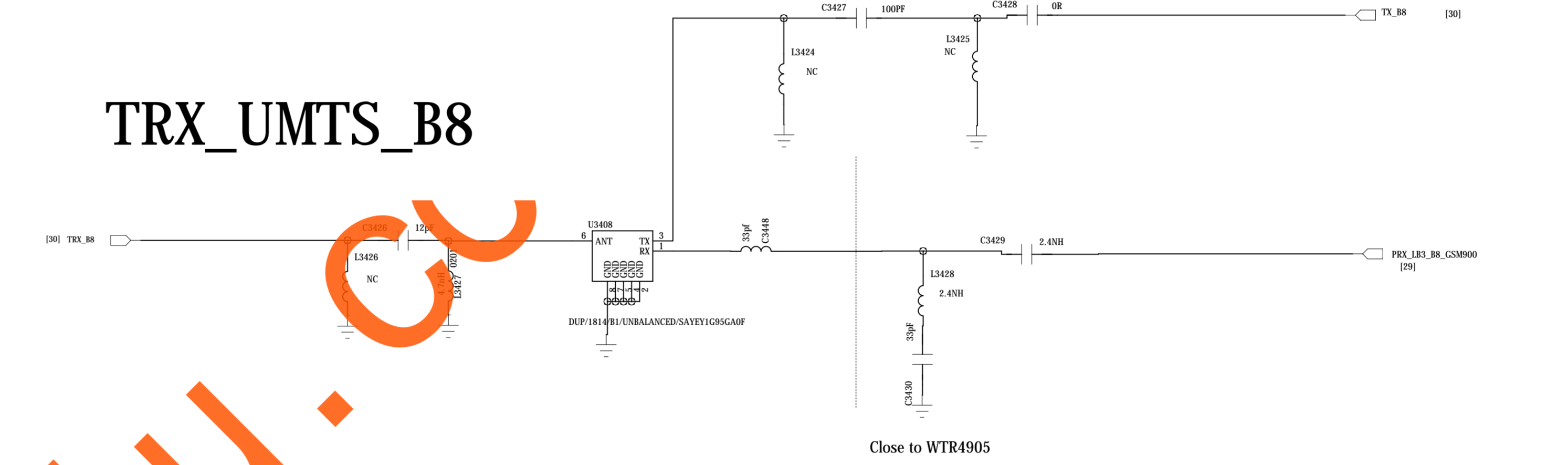


RF1630	
MODE	VC1
RFIN-RF1	0
RFIN-RF2	1

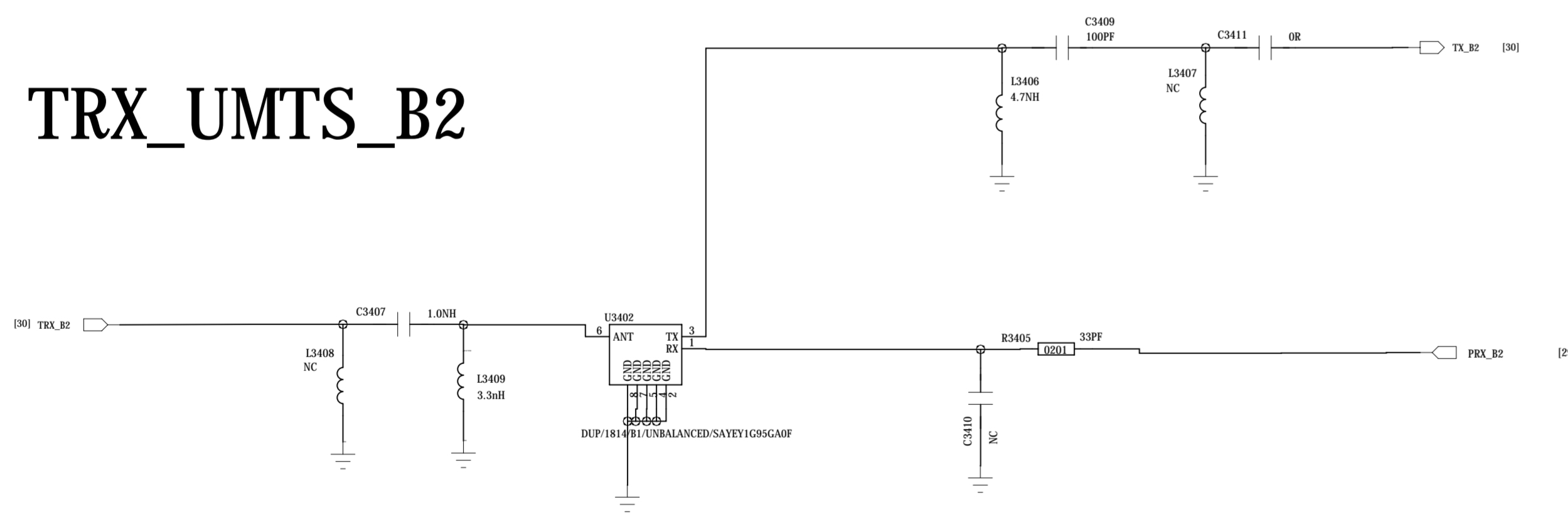
TRX_UMTS_B5_BC0



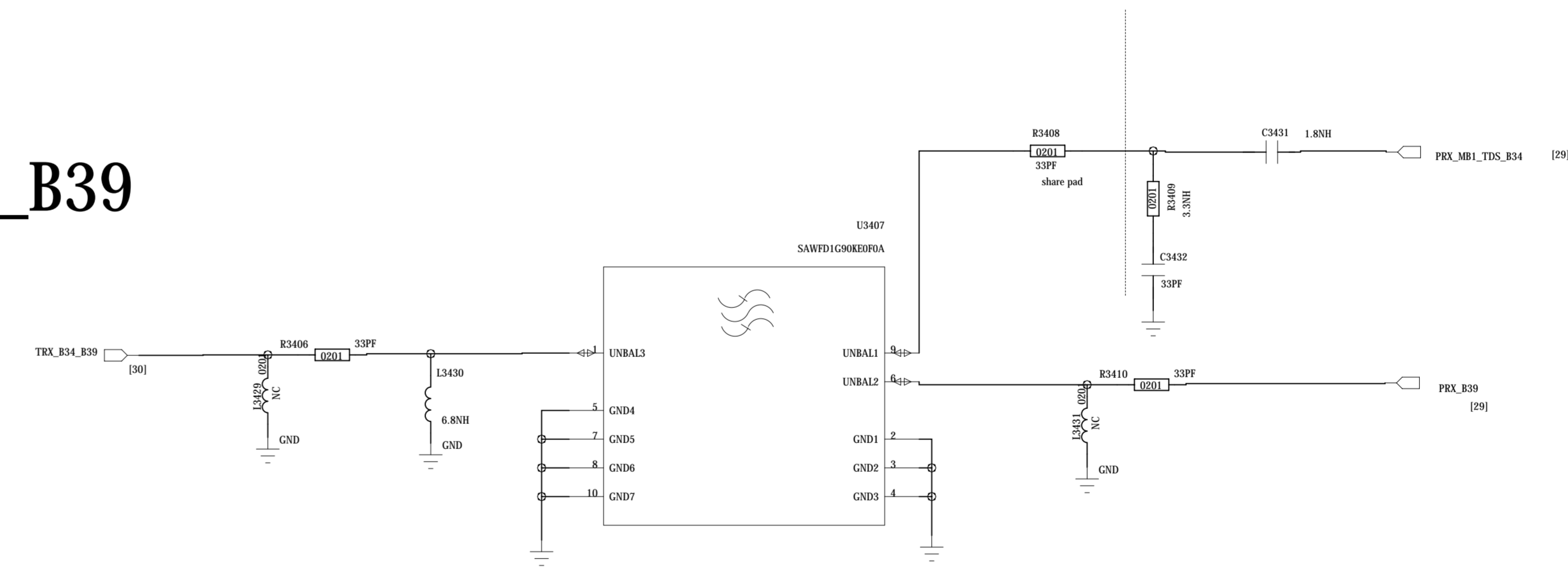
TRX_UMTS_B8



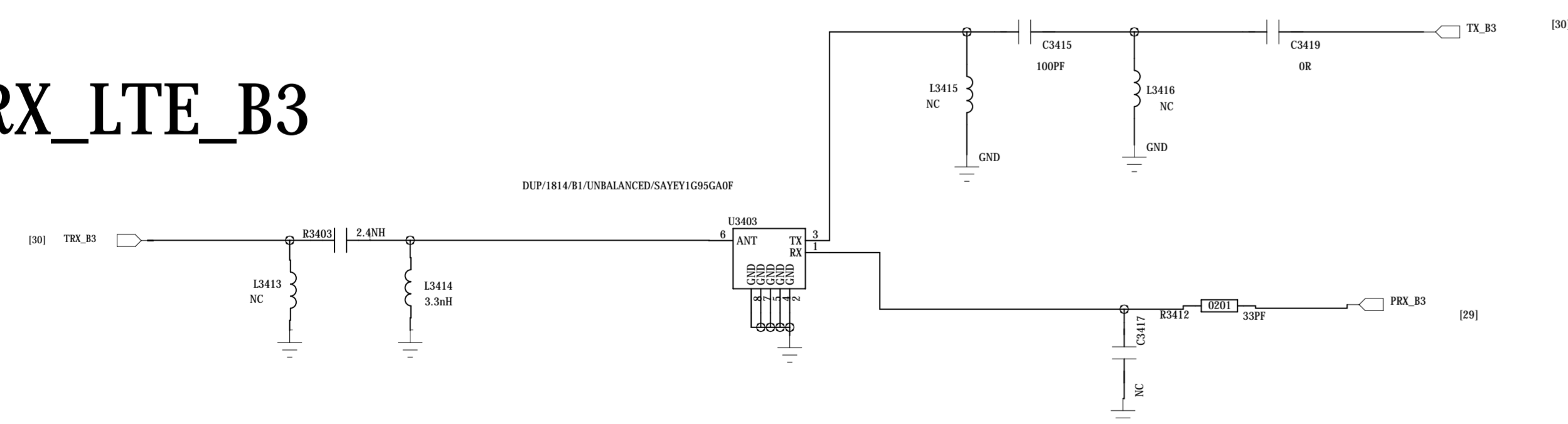
TRX_UMTS_B2



TRX_TDS_B34_B39



TRX_LTE_B3



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ESBA	U3402-SAVEY7148R0300A
ESBA	U3402-SAVEY7148R0300A
ESBA	U3402-SAVEY7148R0300A

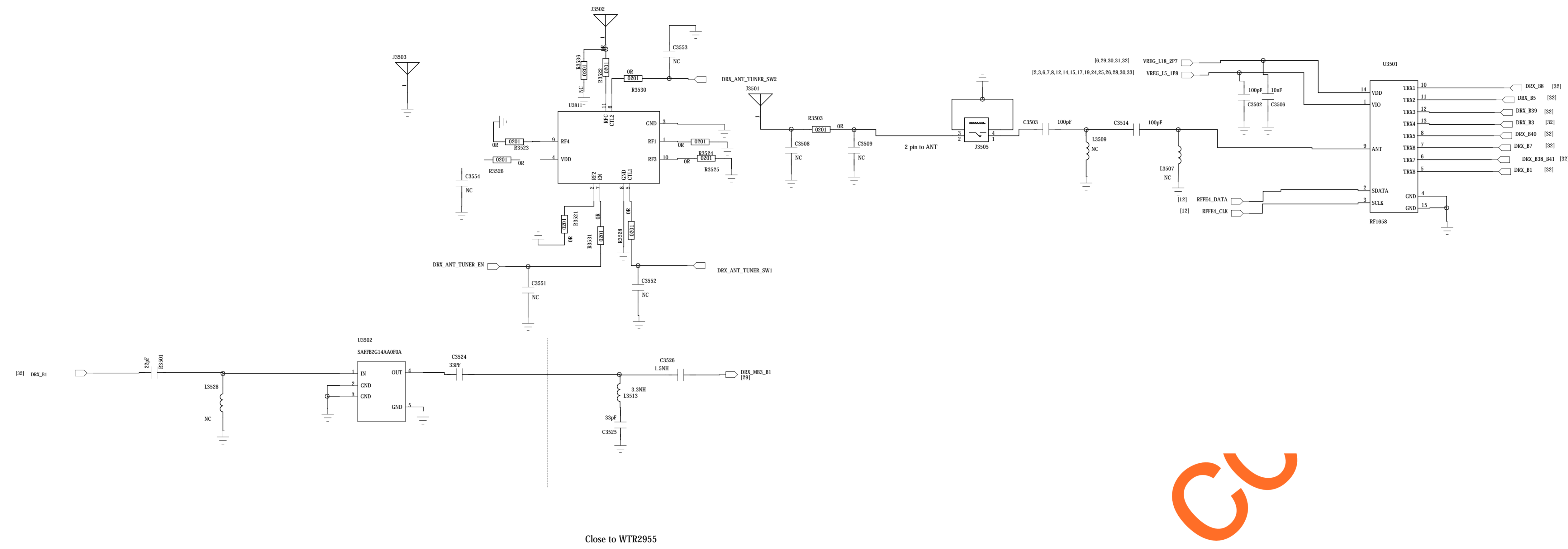
COMPANY: <Company Name>

TITLE: <Title>

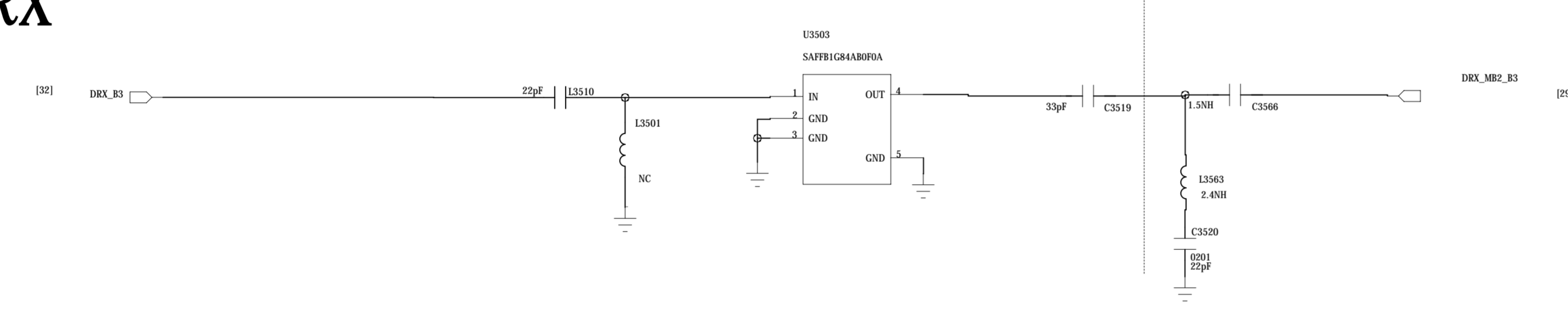
DRAWN	<Drawn By>	DATED	<Drawn Date>	CODE	SIZE	DRAWING NO.	REV.
CHECKED	<Checked By>	DATED	<Checked Date>	<Code>	A0	<Drawing Number>	<Revision>
QUALITY CONTROL	<QC By>	DATED	<QC Date>	SCALE	<Scale>	SHEET	31 34
RELEASED	<Released By>	DATED	<Release Date>				

REVISION RECORD			
LET	REV NO.	APPROVED	DATE

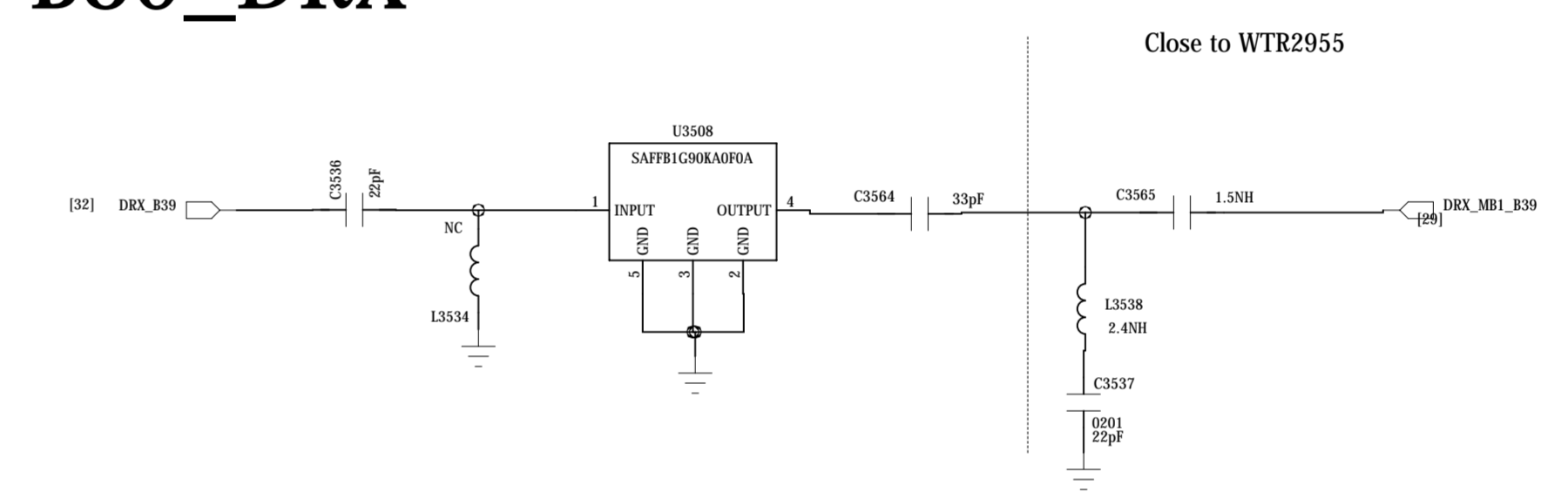
B1/B4_DRX



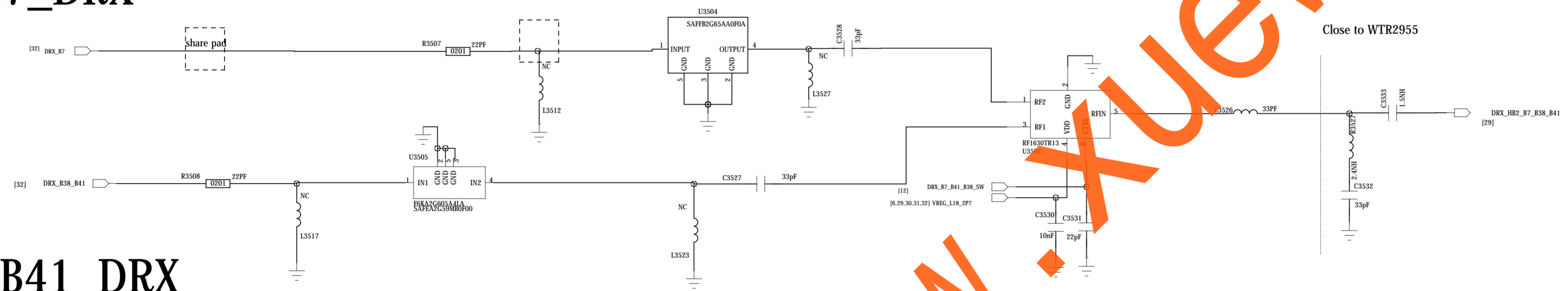
B2/B3_DRX



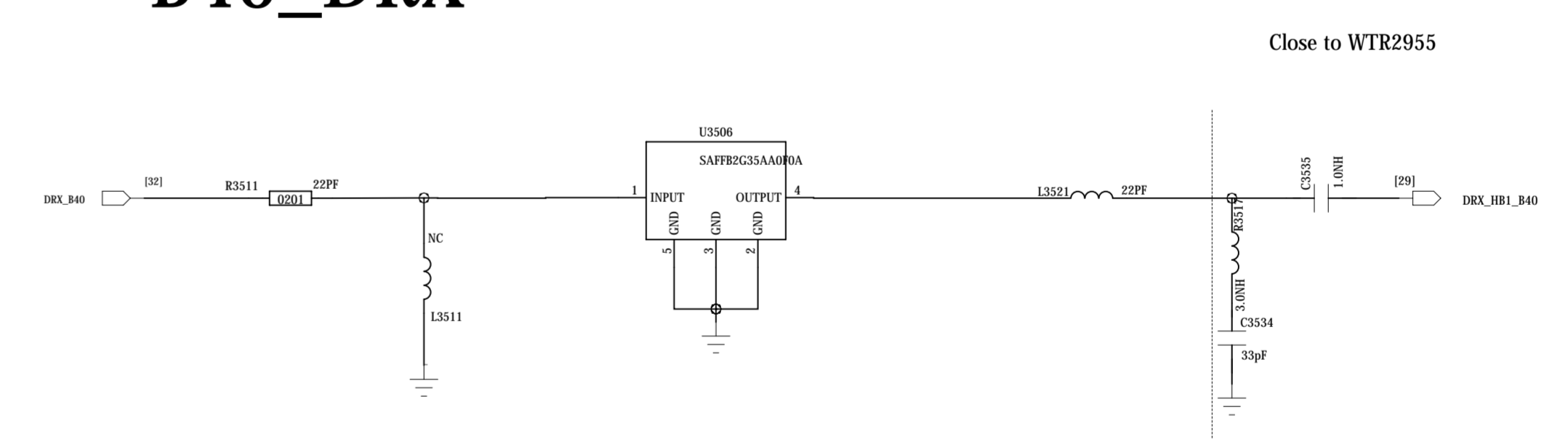
B39_DRX



B7_DRX



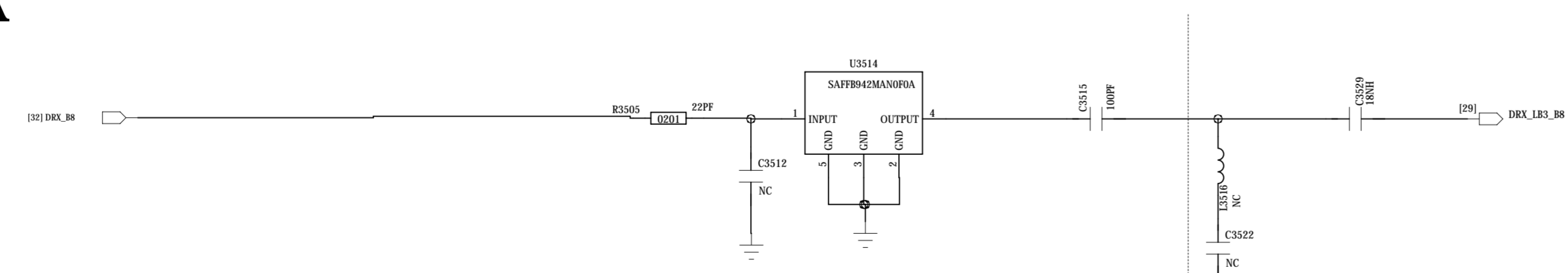
B40_DRX



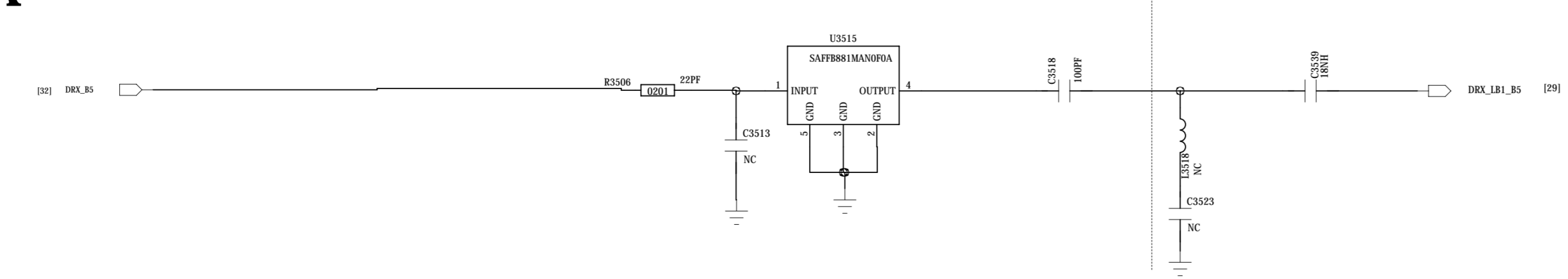
B38_B41_DRX



B8_DRX



B5_DRX



RF1630	
MODE	VC1
RFIN-RF1	0
RFIN-RF2	1

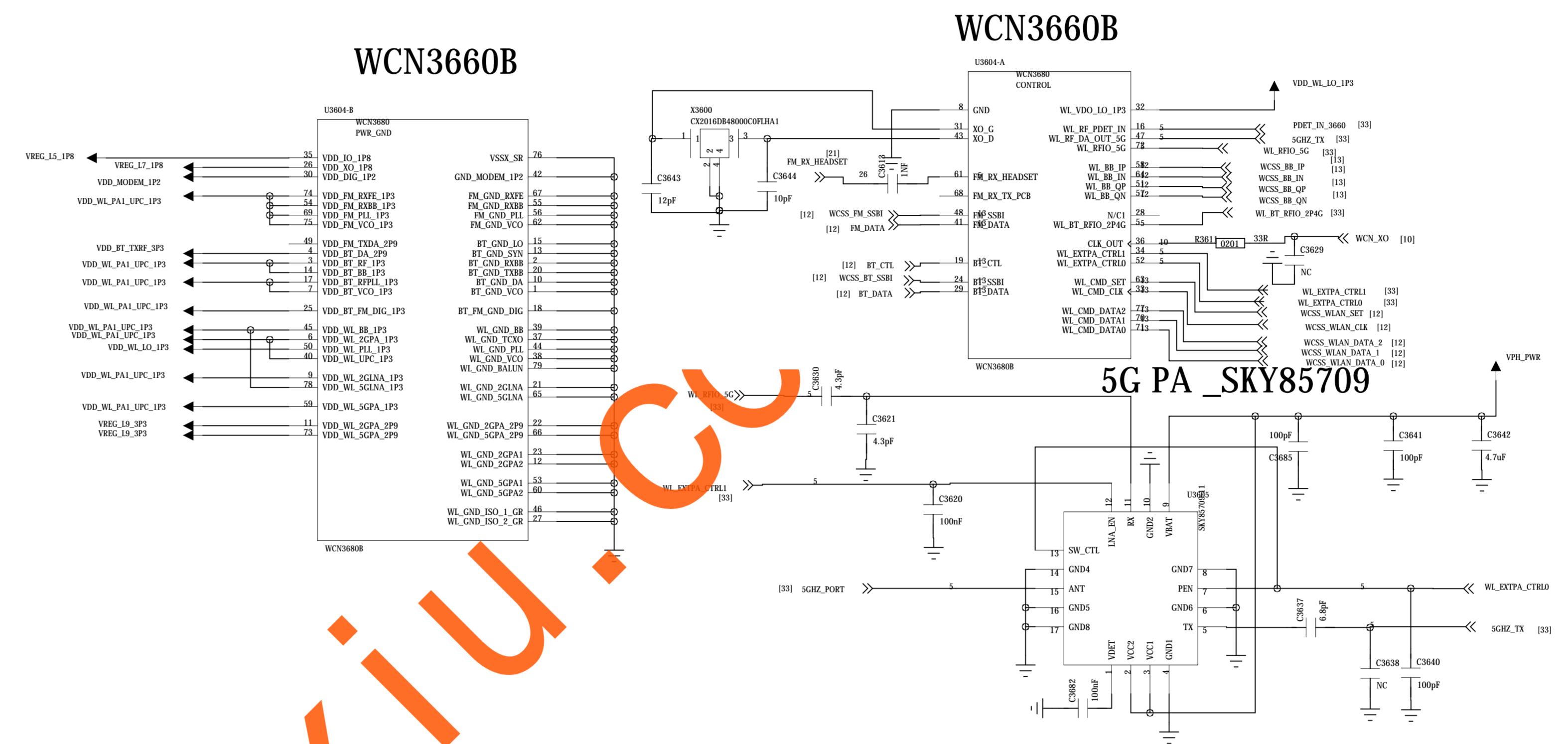
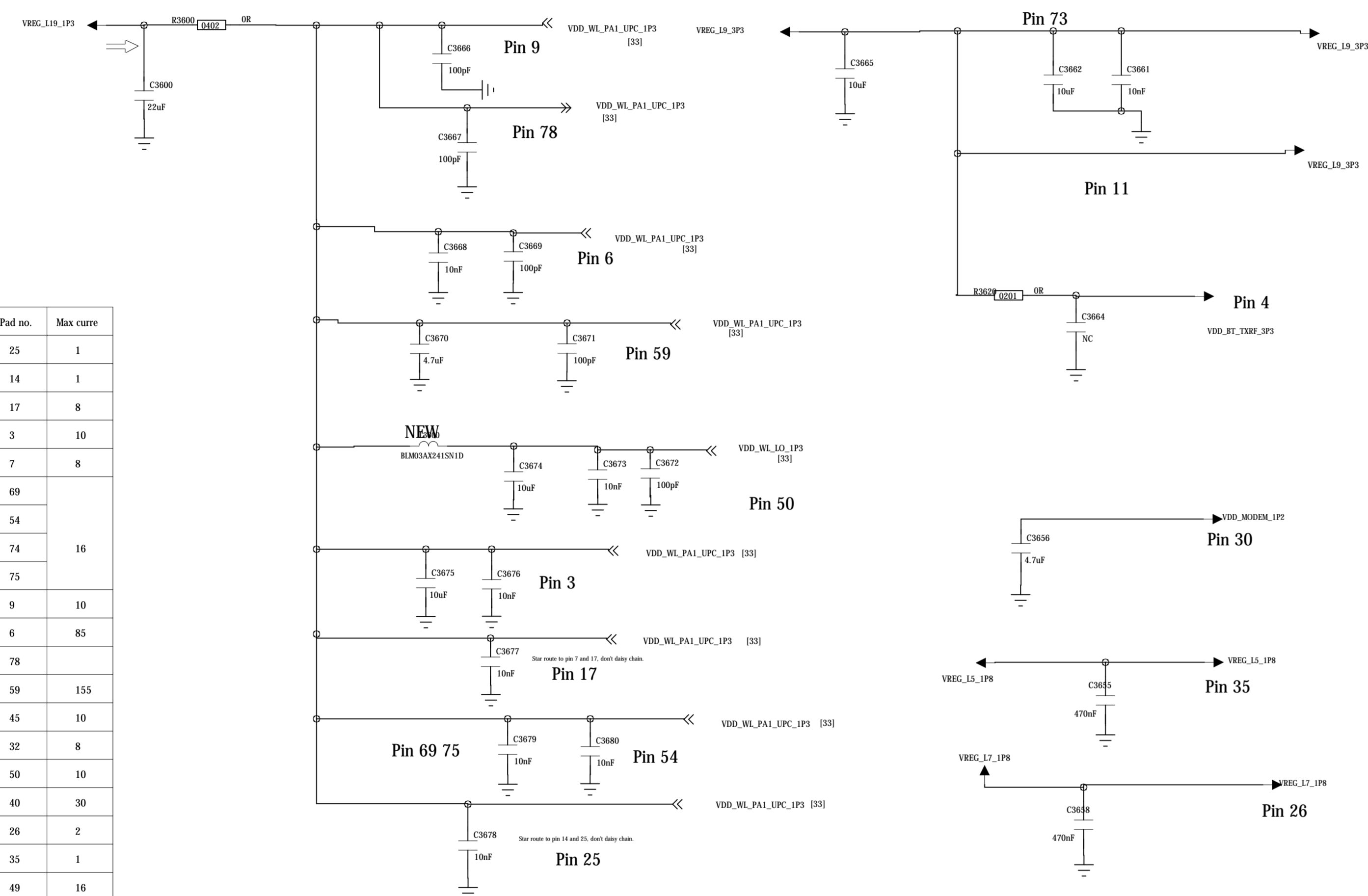
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COMPANY: <Company Name>	
TITLE: <Title>	
DESIGN: <Drawn By>	DATE: <Drawn Date>
CHECKED: <Checked By>	DATE: <Checked Date>
QUALITY CONTROL: <QC By>	DATE: <QC Date>
RELEASED: <Released By>	DATE: <Release Date>
CODE: <Code>	SIZE: A0
DRAWING NO: <Drawing Number> Revision: <Revision>	
SCALE: <Scale>	SHEET: 22 / 34

REVISION RECORD			
REV	REV NO.	APPROVED	DATE

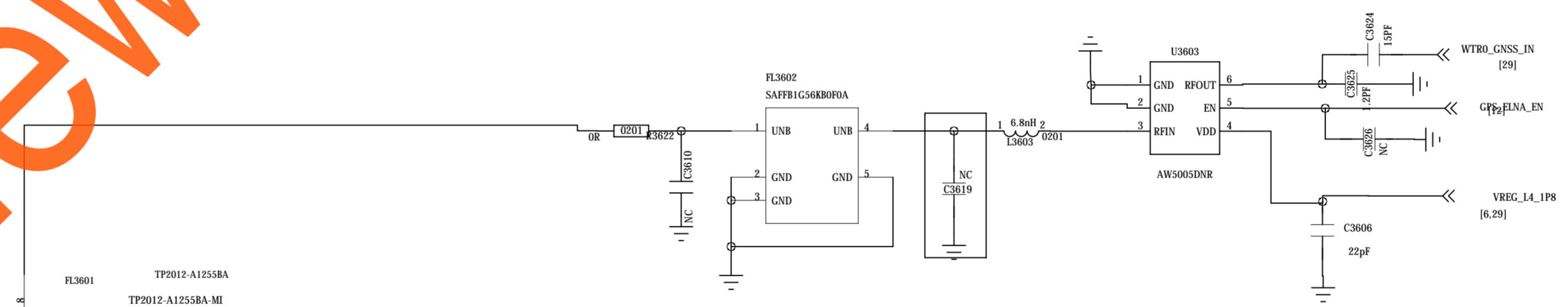
WIFI/BT

Voltage	Pad name	Pad no.	Max currt
VDD_BT_PM_HG_IP3		25	1
VDD_BT_PM_IP3		14	1
VDD_BT_PM_IP3		17	8
VDD_BT_RXIP_IP3		3	10
VDD_BT_VCO_IP3		7	8
VDD_PM_PIL_IP3		69	
VDD_PM_KOBB_IP3		54	
VDD_PM_RXIP_IP3		74	16
VDD_PM_VCO_IP3		75	
VDD_WL_SGNA_IP3		9	10
VDD_WL_SGPA_IP3		6	85
VDD_WL_SGNA_IP3		78	
VDD_WL_SGPA_IP3		59	153
VDD_WL_BB_IP3		45	10
VDD_WL_LL_IP3		32	8
VDD_WL_PIL_IP3		50	10
VDD_WL_LPC_IP3		40	30
VDD_WL_IP8		26	2
VDD_WL_IP8		35	1
VDD_WL_TXDA_IP0		49	16
VDD_WL_SGPA_IP0		11	560
VDD_WL_SGPA_IP0		73	600
VDD_BT_TXIP_IP0		4	45
VDD_DBG_IP2		30	4

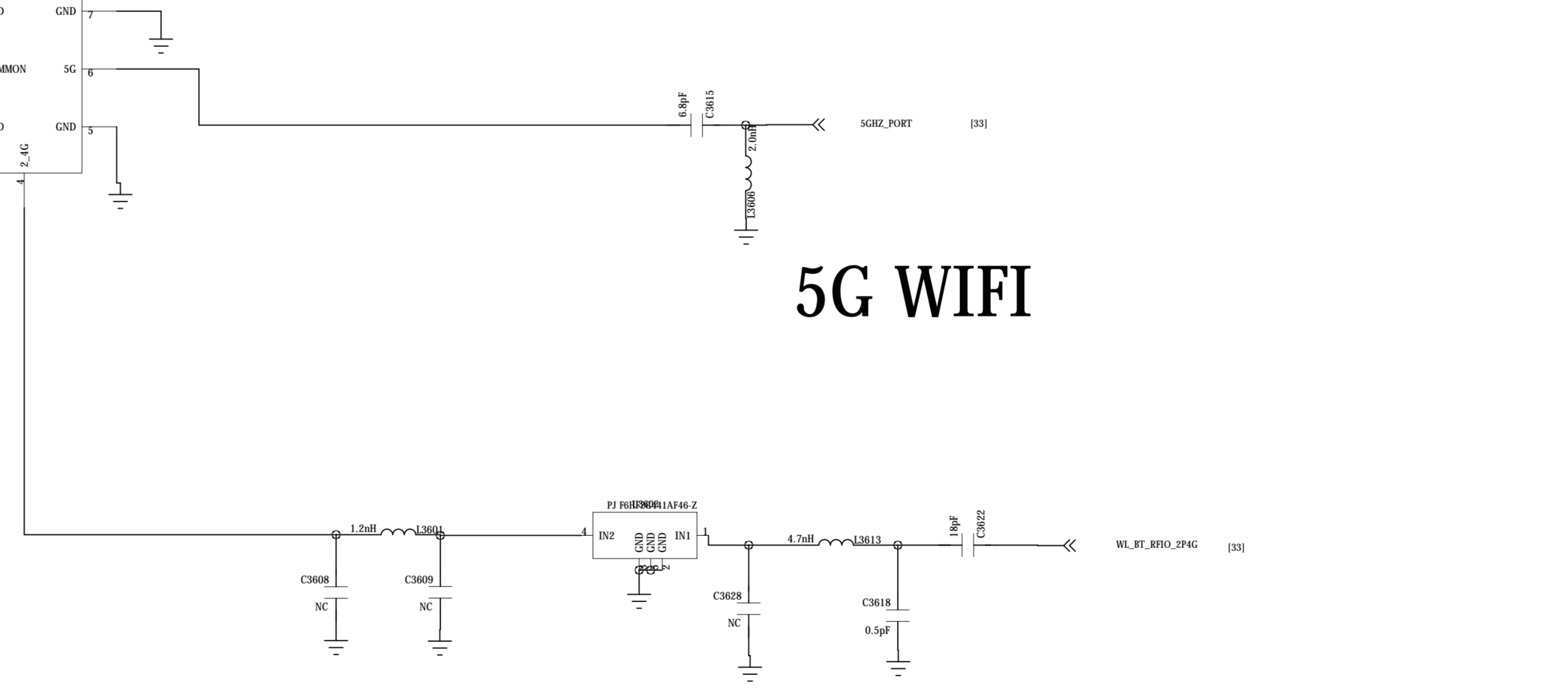


MODE	SW_CTL
WLAN_RX	HIGH
WLAN_TX	LOW
WLAN_OFF	LOW

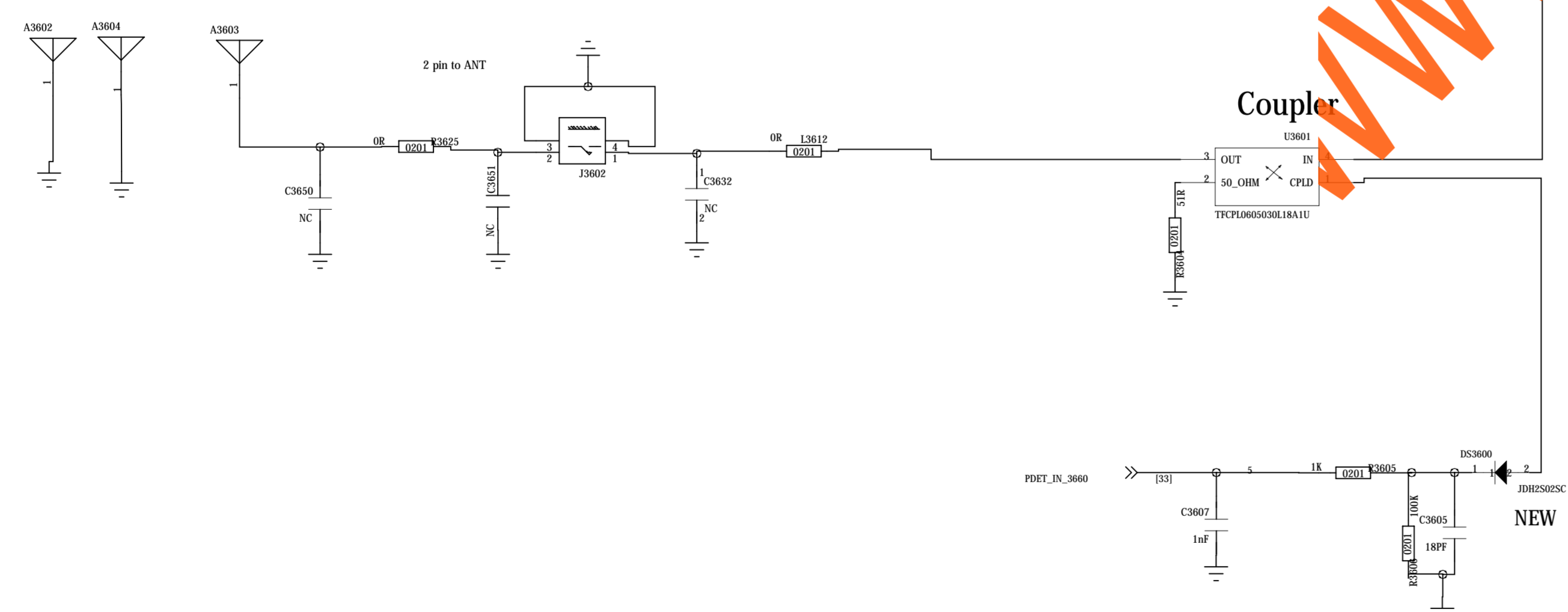
GPS/GLONASS



5G WIFI



2.4G WIFI/BT



DRAWN: <Drawn By>		DATE: <Drawn Date>	
CHECKED: <Checked By>		DATE: <Checked Date>	
QUALITY CONTROL: <QC By>		DATE: <QC Date>	
RELEASED: <Released By>		DATE: <Release Date>	
CODE: <Code>	SIZE: A0	DRAWING NO: <Drawing Number>	REV: <Revision>
SCALE: <Scale>		SHEET: 28 / 34	

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LT#	ECO NO.	APPROVED	DATE

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COMPANY: <Company Name>

TITLE: <Title>

DESIGN: <Drawn By>	DATE: <Drawn Date>	CODE: <Code>	SIZE: A0	DRAWING NO: <Drawing Number>	REV: <Revision>
CHECKED: <Checked By>	DATE: <Checked Date>				
QUALITY CONTROL: <QC By>	DATE: <QC Date>				
RELEASED: <Released By>	DATE: <Release Date>	SCALE: <Scale>	SHEET 34 34		